

Complete Data Sheet available via web, Harris' home page: <http://www.semi.harris.com> or via Harris AnswerFAX, see Section 17

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Dual SPDT, CMOS Analog Switch

Features

- Low $r_{DS(ON)}$ 25 Ω
- Switches Greater than 20V_{p-p} Signals with $\pm 15V$ Supplies
- Quiescent Current <100 μA
- Break-Before-Make Switching
 - t_{OFF} 120ns (Typ)
 - t_{ON} 200ns (Typ)
- TTL, CMOS Compatible
- Complete Monolithic Construction
- Supply Range $\pm 5V$ to $\pm 15V$

Description

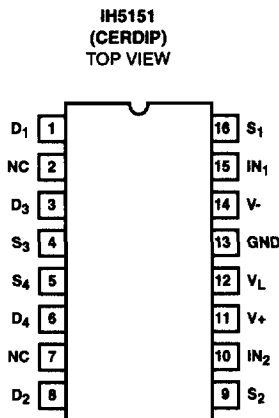
The IH5151 solid state analog switch is designed using an improved, high voltage CMOS technology.

Key performance advantages in the IH5151 are TTL compatibility and ultra low power operation. $r_{DS(ON)}$ switch resistance is typically in the 14 Ω to 18 Ω area, for signals in the -10V to +10V range. Quiescent current is less than 10 μA . The IH5151 also guarantees Break-Before-Make switching which is logically accomplished by extending the t_{ON} time (200ns typical) such that it exceeds t_{OFF} time (120ns typical). This insures that an ON channel will be turned OFF before an OFF channel can turn ON. The need for external logic required to avoid channel to channel shorting during switching is thus eliminated.

Ordering Information

PART NUMBER	TEMP. RANGE (°C)	PACKAGE	PKG. NO.
IH5151CJE	0 to 70	16 Ld CERDIP	F16.3
IH5151MJE	-55 to 125	16 Ld CERDIP	F16.3
IH5151MJE/883B	-55 to 125	16 Ld CERDIP	F16.3

Pinout



Switching State Diagram

SWITCH STATE SHOWN FOR LOGIC "1" INPUT

