

SA2B-M3, SA2D-M3, SA2G-M3, SA2J-M3, SA2K-M3, SA2M-M3

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Vishay General Semiconductor

COMPLIANT HALOGEN

FREE

Surface Mount Glass Passivated Rectifier



DO-214AC (SMA)

PRIMARY CHARACTERISTICS						
I _{F(AV)}	2.0 A					
V _{RRM}	100 V, 200 V, 400 V, 600 V, 800 V, 1000 V					
I _{FSM}	55 A					
I _R	3.0 μΑ					
V _F at I _F = 2.0 A	0.854 V					
T _J max.	150 °C					
Package	DO-214AC (SMA)					
Diode variations	Single die					

FEATURES

- Low profile package
- Ideal for automated placement
- · Glass passivated pellet chip junction
- Low forward voltage drop
- · Low leakage current
- High forward surge capability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

TYPICAL APPLICATIONS

For use in general purpose rectification of power supplies, inverters, converters and freewheeling diodes for consumer and telecommunication.

MECHANICAL DATA

Case: DO-214AC (SMA)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - RoHS-compliant, commercial grade

Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test **Polarity:** Color band denotes the cathode end

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)								
PARAMETER	SYMBOL	SA2B	SA2D	SA2G	SA2J	SA2K	SA2M	UNIT
Device marking code		2B	2D	2G	2J	2K	2M	
Max. repetitive peak reverse voltage	V _{RRM}	100	200	400	600	800	1000	V
Average forward current	I _{F(AV)}	2.0				Α		
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I _{FSM}	55				Α		
Operating junction and storage temperature range	T _J , T _{STG}	-55 to +150				°C		

ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)									
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT			
Instantaneous forward voltage	I _F = 1.0 A	T _{.1} = 25 °C		0.911	-				
	I _F = 2.0 A	1j = 25 C	V _E ⁽¹⁾	0.954	1.1	V			
	I _F = 1.0 A	T _J = 125 °C	V _F \''	0.805	-	v			
	I _F = 2.0 A			0.854	0.95				
Reverse current	Dated V	T _J = 25 °C T _J = 125 °C	I _R ⁽²⁾	0.19	3				
	Rated V _R		IR (=/	28	90	μA			
Typical reverse recovery time	$I_F = 0.5 \text{ A}, I_R = 1.0 \text{ A},$ $I_{rr} = 0.25 \text{ A}$		t _{rr}	1.5	-	μs			
Typical junction capacitance	4.0 V, 1 MHz		CJ	11	-	pF			

Notes

 $^{(1)}$ Pulse test: 300 μs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms



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THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)								
PARAMETER	SYMBOL	SA2B	SA2D	SA2G	SA2J	SA2K	SA2M	UNIT
Typical thermal resistance	R ₀ JA ⁽¹⁾	(1) 80						°C/W
Typical thermal resistance	R _{0JL} (1)	(1) 12					G/ VV	

Note

⁽¹⁾ Thermal resistance from junction to ambient and from junction to lead, PCB mounted on 0.79" x 0.79" (20 mm x 20 mm) copper pad areas

ORDERING INFORMATION (Example)								
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE				
SA2J-M3/61T	0.064	61T	1800	7" diameter plastic tape and reel				
SA2J-M3/5AT	0.064	5AT	7500	13" diameter plastic tape and reel				

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

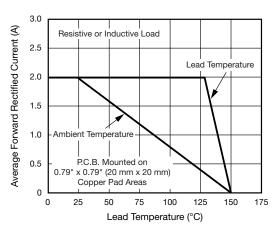


Fig. 1 - Max. Forward Current Derating Curve

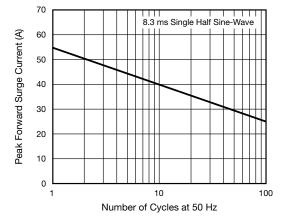


Fig. 3 - Max. Non-Repetitive Peak Forward Surge Current

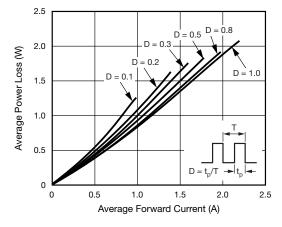


Fig. 2 - Forward Power Loss Characteristics

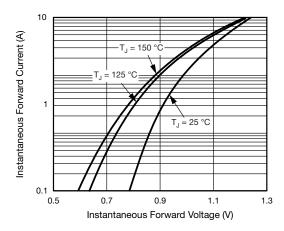


Fig. 4 - Typical Instantaneous Forward Characteristics

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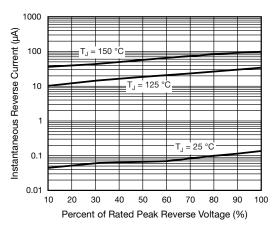


Fig. 5 - Typical Reverse Leakage Characteristics

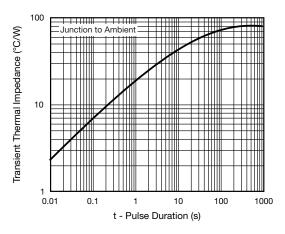


Fig. 7 - Typical Transient Thermal Impedance

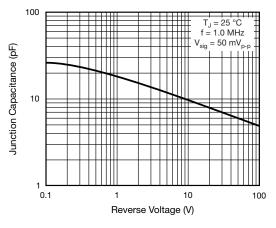
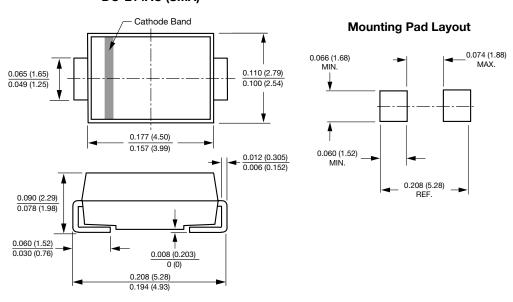


Fig. 6 - Typical Junction Capacitance

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

DO-214AC (SMA)





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