Dual high slew rate, low noise operational amplifier BA15218/BA15218F/BA15218N

The BA15218, BA15218F, and BA15218N are monolithic ICs with two built-in low-noise, low-distortion operational amplifiers featuring internal phase compensation.

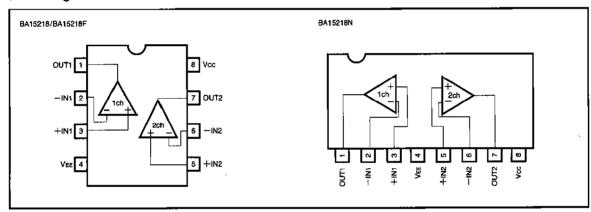
Either a dual or single power supply can be driven, and these products can be driven by a digital system 5V single power supply.

The following packages are available: 8-pin DIP (BA15218), 8-pin SOP (BA15218F), and 8-pin SIP (BA15218N).

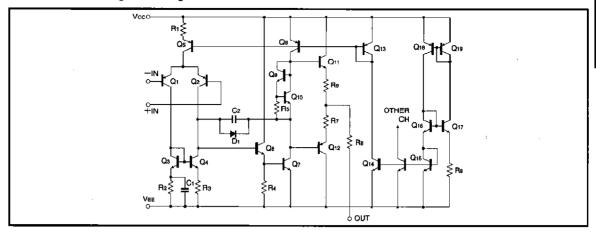
Features

- Low-voltage operation and single power supply drive enabled. (Single power supply: 4 to 32V, dual power supply: ±3 to ±16V)
- 2) Low noise level. ($V_n = 1.0 \mu V_{rms}$ typ. : RIAA)
- 3) High slew rate. (SR = 3V/ μ s, GBW = 10MHz typ.)
- 4) Low offset voltage. (Vio = 0.5mV typ.)
- 5) High gain and low distortion. (Gvo = 110dB, THD = 0.0015%)
- 6) Pin connections are the same as with standard dual operational amplifiers, and outstanding characteristics make these products compatible with the 4558 and 4560 models.

Block diagram



Internal circuit configuration diagram



●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol		1.1		
		BA15218	BA15218F	BA15218N	Unit
Power supply voltage	Voc	±18	±18	±18	٧
Power dissipation	Pd	600*	550*	900*	mW
Differential input voltage	Vio	± V cc	±Vcc	±V∞	٧
In-phase input voltage	Vı	−Vcc~Vcc	−Vcc~Vcc	−Vcc~Vcc	٧
Load current	Іома	±50	±50	±50	mA
Operating temperature	Topr	−40~85	−40~85	-40~85	Ĉ
Storage temperature	Tstg	−55~125	-55~125	-55~125	ొ

^{*}For Pd values, please see Pd characteristic diagram.

Values are those when BA15218F is mounted on a glass epoxy PCB (50 mm x 50 mm x 1.6 mm).

●Electrical characteristics (unless otherwise noted, Ta=25°C, Vcc=+15V, VEE=-15V)

Parameter	Symbol	Mln.	Тур.	Max.	Unit	Conditions
Input offset voltage	Vio	_	0.5	5	mV	Rs≦10kΩ
Input offset current	lio	_	5	200	nA	_
Input blas current	Ів	-	50	500	nA	-
High-amplitude voltage gain	Av	86	110	_	dB	Rı≧2kΩ, Vo≕±10V
Common mode input voltage range	Vісм	±12	±14	_	V	_
Maximum output voltage	Vом	±12	±14	_	V	Rı≧10kΩ
Maximum output voltage	Vом	±10	±13	-	٧	Rı≧2kΩ
Common mode rejection ratio	CMRR	70	90	_	dB	Rs≦10kΩ
Power supply voltage rejection ratio	PSRR	76	90	_	dB	Rs≦10kΩ
Quiescent circuit current	lα	_	5	8	mA	V _{IN} =0V, R∟=∞
Slew rate	S.R.	-	3	_	V/μs	Av=1, RL=2kΩ
Channel separation	cs	_	120	_	dB	f = 1 kHz input conversion
Voltage gain band width	GBW	_	10	_	MHz	f=10kHz
Maximum frequency	fτ	_	7	_	MHz	-
Input noise voltage	Vn	_	1.0		μV _{rms}	RIAA, Rs=1kΩ, 10Hz~30kHz

Electrical characteristic curves

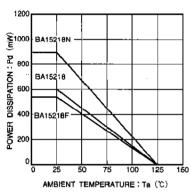


Fig.1 Power dissipation - ambient temperature characteristic

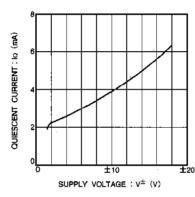


Fig.2 Quiescent current - power supply voltage characteristic

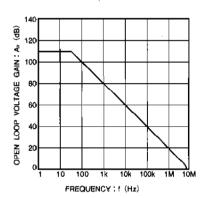


Fig.3 Open loop voltage gain - frequency characteristic



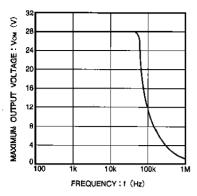


Fig.4 Maximum output voltage - frequency characteristic

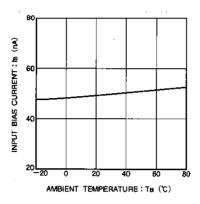


Fig.5 Input bias current - ambient temperature characteristic

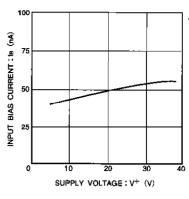
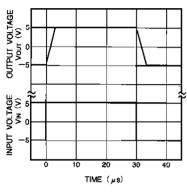


Fig.6 Input bias current - power supply voltage characteristic



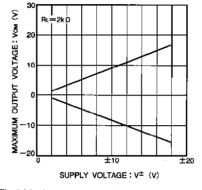


Fig.7 Output response characteristic

Fig.8 Maximum output voltage - power supply voltage characteristic

Operation notes

Unused circuit connections
 If there are any circuits which are not being used, we recommend making connections as shown in Figure 9, with the non-inverted input pin connected to the potential within the in-phase input voltage range (VICM).

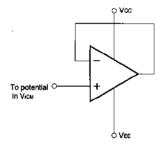
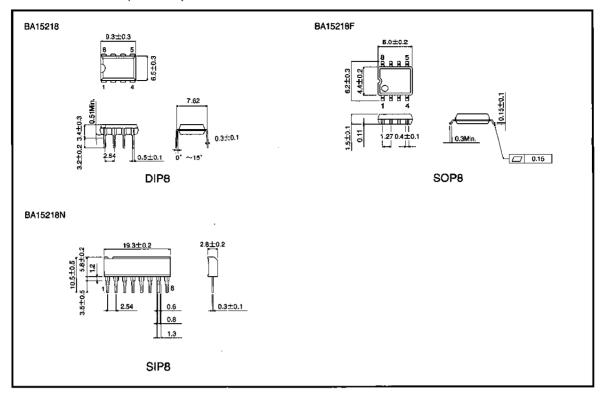


Fig.9 Unused circuit connections

External dimensions (Units: mm)



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