

Analog Switches and Multiplexers

Analog Switches

Part Number	Max $I_{DS(on)}$ (A)	Max $I_{DS(on)}$ (mA)	Max t_{on} (ns)	Typ Q_{on} (pC)	Max Supply Range (V)	Chp. Logic Regulator	Power Consumption (mW)	Package	Comments
1-Channel SPST									
DG417	35	0.25	175	60	44	-	0.035	J, K, Y	Pin package
DG418	35	0.25	175	60	44	-	0.035	J, K, Y	Pin package
2-Channel SPST									
DG180	10	10	400	N/A	36	-	120	A, P, X	JFET
DG181	30	1	150	N/A	36	-	120	A, P, X	JFET
DG401	35	0.25	150	80	44	-	0.035	J, K, Z	-
DG421	35	0.25	250	80	44	-	0.035	J	Latches
DG300A	50	1	300	8	44	-	7.65	A, J, K, P	-
DG304A	50	1	250	30	44	-	0.3	A, J, K, P	-
DG381A	50	1	300	10	44	-	7.65	J	-
DG200A	70	2	1000	-10	44	Yes	60	A, J, K	-
DG182	75	1	250	N/A	36	-	120	A, P, X	JFET
4-Channel SPST									
DG641	15	10	70	-19	21	Yes	108	J, Y	Video
DG411	35	0.25	175	5	44	-	0.035	J, K, Y, Z	-
DG412	35	0.25	175	5	44	-	0.035	J, K, Y, Z	-
DG413	35	0.25	175	5	44	-	0.035	J, K, Y, Z	-
DG271	50	1	65	9	44	Yes	225	J, K, Y, Z	High speed
DG201HS	50	1	50	5	44	Yes	240	J, K, Y, Z	High speed
DG540	60	10	70	-25	21	Yes	108	J, N, P	Video
DG541	60	10	70	-25	21	Yes	108	J, P, Y	Video
DG611	45	0.25	35	4	21	-	0.018	J, K, Y, Z	Glitchless/ high speed
DG612	45	0.25	35	4	21	-	0.018	J, K, Y, Z	Glitchless/ high speed
DG613	45	0.25	35	4	21	-	0.018	J, K, Y, Z	Glitchless/ high speed
DG201B	85	0.5	300	1	44	Yes	0.765	J, K, Y, Q	-
DG202B	85	0.5	300	1	44	Yes	0.765	J, K, Y	-
DG211B	85	0.5	130	1	44	-	0.35	J, Y	General purpose
DG212B	85	0.5	130	1	44	-	0.35	J, Y	General purpose
DG308B	85	0.5	200	1	44	-	0.03	J, K, Y	General purpose
DG309B	85	0.5	200	1	44	-	0.03	J, K, Y	General purpose
DG441	85	0.5	250	-1	44	Yes	1.5	J, K, Y	-
DG442	85	0.5	250	-1	44	Yes	1.5	J, K, Y	-
DG444	85	0.5	250	-1	44	-	0.038	J, Y	-
DG445	85	0.5	250	-1	44	-	0.038	J, Y	-
DG221	90	1	550	20	44	Yes	37.5	J, K, Y	Latches
DG308A	100	1	200	-10	44	-	0.3	J, K, Y	-
DG309	100	1	200	-10	44	-	0.3	J, Y	-
DG201A	175	1	600	20	44	Yes	45	J, K, P, Y, Z	-
DG202	175	1	600	20	44	Yes	45	J, K	-
DG211	175	5	1000	N/A	44	-	20.4	J, Y	-
DG212	175	5	1000	N/A	44	-	20.4	J, Y	-

A = Metal Case
P = Sidebrazed

J = Plastic DIP
X = Flatpack

K = CerDIP
Y = SOIC

N = PLCC
Z = LCC

Part Number	Max. Operating Temp. (°C)	Max. I _{DC} (mA)	Max. I _{PK} (mA)	V _{GS} (V)	V _{DS} (V)	RDS(on) (mΩ)	Power Dissipation (mW)	Package	Comments
1-Channel SPDT									
DG642	8	10	100	-40	21	Yes	108	J, Y	Video
DG186	10	10	400	N/A	38	-	73	A, P	JFET
DG301A	50	1	300	8	44	-	0.3	A, J, K, Z	-
DG187	30	1	150	N/A	36	-	73	A, P, X	JFET
DG419	35	0.25	175	60	44	-	0.035	J, K, Y	Pin package
DG305A	50	1	250	30	44	-	0.3	A, K, P	-
DG387A	50	1	300	10	44	-	7.65	A, J, K	-
DG188	75	1	250	N/A	36	-	73	A, P, X	JFET
DG189	10	10	400	N/A	36	-	120	P	JFET
DG643	15	10	70	-19	21	-	108	J, Y	Video
DG190	30	1	150	N/A	36	-	120	P, X	JFET
2-Channel SPDT									
DG403	30	0.25	150	60	44	-	0.035	J, K, Y, Z	-
DG423	35	0.25	250	60	44	-	0.035	J, K, N	Latches
DG243	50	1	500	60	44	-	10.5	J	Latches
DG303A	50	1	300	8	44	-	7.65	J, K, P, Z	-
DG307A	50	1	250	30	44	-	0.3	J, K, P	-
DG390A	50	1	300	10	44	-	7.65	J, K	-
DG5043	50	1	1200	30	44	-	0.1	J	-
DG5143	50	0.5	175	150	36	-	0.035	J	-
DG542	60	10	100	-25	21	Yes	108	J, P, Y	Video
DG191	75	1	250	N/A	36	-	120	P, X	JFET
2-Channel DPST									
DG183	10	10	400	N/A	36	-	120	P	JFET
DG184	30	1	150	N/A	36	-	120	P, X	JFET
DG129	30	1	600	N/A	36	-	68	P	JFET
DG405	30	0.25	150	60	44	-	0.035	J, K, Y, Z	-
DG425	35	0.25	250	60	44	-	0.035	J	Latches
DG302A	50	1	300	8	44	-	7.65	J, K, P	-
DG306A	50	1	250	30	44	-	0.3	J, K, P	-
DG384A	50	1	300	10	44	-	7.65	J, K	-
DG185	75	1	250	N/A	36	-	120	P, X	JFET

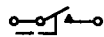
A = Metal Case
P = Sidebrazed

J = Plastic DIP
X = Flatpack

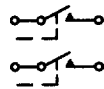
K = CerDIP
Y = SOIC

N = PLCC
Z = LCC

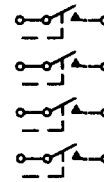
Functional Configurations



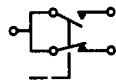
1-Channel SPST



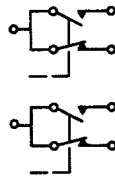
2-Channel SPST



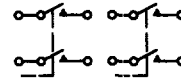
4-Channel SPST



1-Channel SPDT



2-Channel SPDT



2-Channel DPST