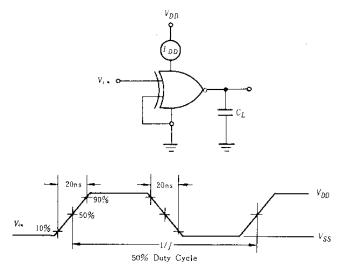
# HD14070B, HD14077B

## Quadruple Exclusive-OR Gate------HD14070B Quadruple Exclusive-NOR Gate------HD14077B

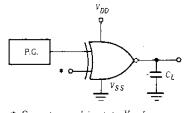
### FEATURES

- Quiescent Current = 0.5nA typ/pkg@5V
- Noise Immunity = 45% of  $V_{DD}$  typ
- Capable of Driving One Low-power Schottky TTL Load Over the Rated Temperature Range
- Pin-for Pin Replacements for CD4070B/77B and MC14070B/ 77B Series

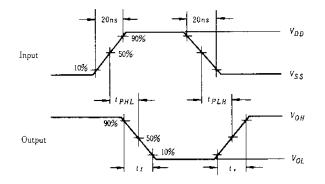
■ POWER DISSIPATION TEST CIRCUIT AND WAVEFORM



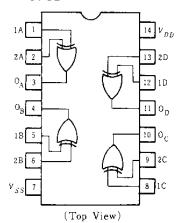
#### SWITCHING TIME TEST CIRCUIT



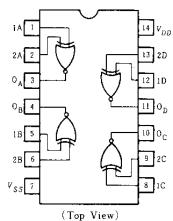
 Connect unused input to V<sub>DD</sub> for HD14070B, to V<sub>ss</sub> for HD14077B



## PIN ARRANGEMENT •HD14070B



#### •HD14077B



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Characterístic	Symbol		Test Conditions	-4	-40°C		<b>25°</b> C		<b>85°</b> C		
	Symbol	$V_{DD}(\mathbf{V})$	Test Conditions	min	max	min	typ	max	min	max	Unit
Output Voltage		5.0	$V_{in} = V_{DD}$ or 0		0.05	-	0	0.05		0.05	v
	Vol	10		_	0.05	-	0	0.05	-	0.05	
		15		-	0.05		0	0.05		0.05	
		5.0	$V_{in}=0$ or $V_{DD}$	4.95	_	4.95	5.0	-	4.95		v
	Von	10		9.95	—	9.95	10	-	9.95	-	
		15		14.95	-	14.95	15	-	14.95	-	
 Input Voltage		5.0	$V_{out} = 4.5$ or $0.5V$	—	1.5	-	2.25	1.5		1.5	
	VIL	10	V <sub>ou1</sub> =9.0 or 1.0V	-	3.0	·	4.50	3.0		3.0	v
		15	$V_{out} = 13.5 \text{ or } 1.5 \text{ V}$	-	4.0	-	6.75	4.0		4.0	
		5.0	$V_{out} = 0.5$ or $4.5 V$	3.5	· -	3.5	2.75	—	3.5	—	v
	V IH	10	$V_{out} = 1.0$ or $9.0V$	7.0	_	7.0	5.50		7.0		
		15	V <sub>out</sub> =1.5 or 13.5V	11.0	-	11.0	8.25	-	11.0	-	
	Іон	5.0	$V_{OH} = 2.5 \text{ V}$	-2.5	—	-2.1	- <b>4</b> ,2	_	-1.7	_	mA
		5.0	$V_{OH} = 4.6 \mathrm{V}$	-0.52		-0.44	-0.88		-0.36	-	
		10	$V_{OH} = 9.5 V$	-1.3	—	-1.1	-2.25	_	-0.9	—	
Output Drive Current		15	$V_{OH} = 13.5 \mathrm{V}$	-3.6		-3.0	-8.8		-2.4	-	
	IOL	5.0	$V_{oL} = 0.4 V$	0.52		0.44	0.88		0.36	-	mA
		10	$V_{oL} = 0.5 V$	1.3	_	1.1	2.25	_	0.9	—	
		15	$V_{oL} = 1.5 V$	3.6		3.0	8.8	_	2.4	—	
Input Current	Iin	15			±0.3	_	±0.00001	±0.3	_	±1.0	μA
Input Capacitance	Cin		V <sub>1,n</sub> =0	-	-		5.0	7.5	-		pF
Quiescent Current	IDD	5.0	- Zero Signal, per Ppckage		1.0	-	0.0005	1.0		7.5	1
		10		_	2.0		0.0010	2.0		15	μA
		15		_	4.0	-	0.0015	4.0	-	30	1
Total Supply Current*	І т	5.0	Dynamic $+I_{DD}$ ,			-	0.3		-	-	-{
		10	per Gate,	_	-	-	0.6			_	
		15	$C_L = 50 \text{pF}, f = 1 \text{kHz}$	_	—	_	0.9	_	<u> </u>		

#### ■ ELECTRICAL CHARACTERISTICS

\* To calculate total supply current at frequency other than 1kHz.

@ Voo=5.0V Ir=(0.3μA/kHz)/+Ioo . @ Voo=10V Ir=(0.6μA/kHz)/+Ioo . @ Voo=15V Ir=(0.9μA/kHz)/+Ioo

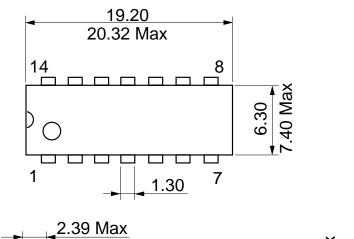
## **SWITCHING CHARACTERISTICS** ( $C_L = 50 \, \text{pF}, Ta = 25^{\circ} \text{C}$ )

Characteristic	Symbol	$V_{DD}(\mathbf{V})$	-40°C		25°C			85°C		
			min	max	min	typ	max	min	max	Unit
		5.0	_	-	-	100	200	-	_	ns
	t -	10		_	_	50	100	—	—	
Output Pice and Pull Time		15		_	_	40	80	_	-	
Output Rise and Fall Time	tj	5.0	-		_	100	200	_	_	
		10	-	_	_	50	100	_	_	ns
		15	-		-	40	80	_	_	
	₿ PLH	5.0	-	-	—	175	350			ns
		10	_		-	75	150	—		
Propagation Delay Time		15	-	-	—	50	100	-	_	
Tropagation Delay 11me	t phi	5.0	_	—	_	175	350		-	ns
		10	-		_	75	150		—	
		15			-	50	100	-	_	

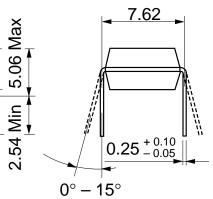


130

Unit: mm



 $0.48 \pm 0.10$ 



0.51 Min

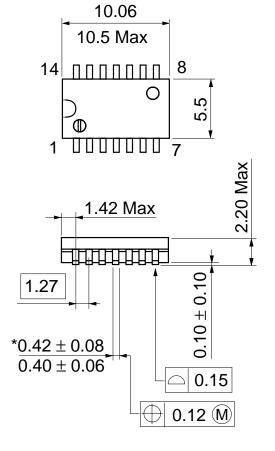
RANK

Hitachi Code	DP-14
JEDEC	Conforms
EIAJ	Conforms
Weight (reference value)	0.97 g

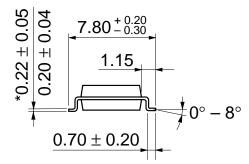
 $2.54\pm0.25$ 

Unit: mm





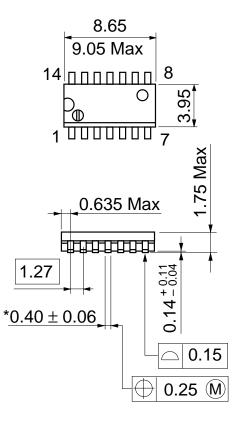
\*Dimension including the plating thickness Base material dimension



Hitachi Code	FP-14DA				
JEDEC					
EIAJ	Conforms				
Weight (reference value)	0.23 g				

Unit: mm



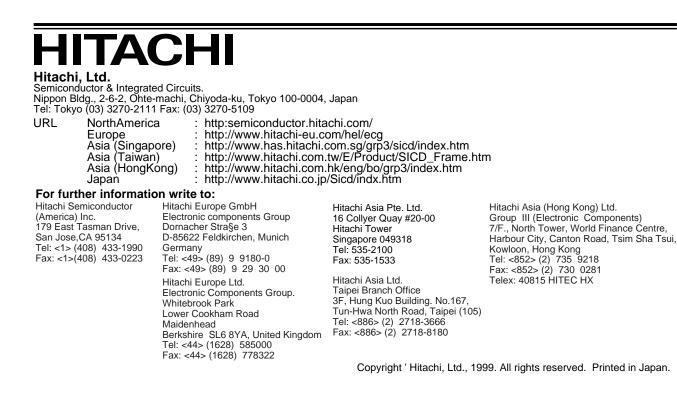


Hitachi Code	FP-14DN
JEDEC	Conforms
EIAJ	Conforms
Weight (reference value)	0.13 g

\*Pd plating

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