TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74VHC08F, TC74VHC08FK

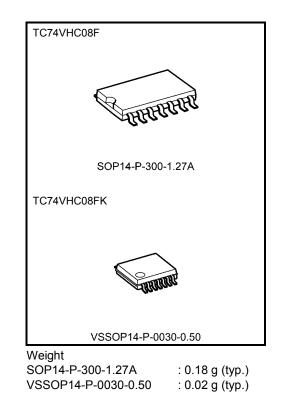
Quad 2-Input AND Gate

The TC74VHC08 is an advanced high speed CMOS 2-INPUT AND GATE fabricated with silicon gate $\rm C^2MOS$ technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

The internal circuit is composed of 4 stages including buffer output, which provide high noise immunity and stable output.

An input protection circuit ensures that 0 to 5.5 V can be applied to the input pins without regard to the supply voltage. This device can be used to interface 5 V to 3 V systems and two supply systems such as battery back up. This circuit prevents device destruction due to mismatched supply and input voltages.

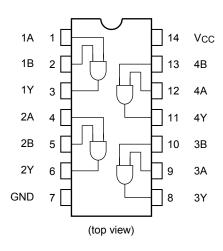


Features

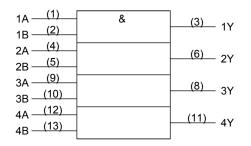
- High speed: $t_{pd} = 4.3 \text{ ns}$ (typ.) at VCC = 5 V
- Low power dissipation: $I_{CC} = 2 \mu A \pmod{at Ta} = 25^{\circ}C$
- High noise immunity: $V_{NIH} = V_{NIL} = 28\% V_{CC}$ (min)
- Power down protection is provided on all inputs.
- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range: V_{CC} (opr) = 2 V to 5.5 V
- Low noise: VOLP = 0.8 V (max)
- Pin and function compatible with 74ALS08

TOSHIBA

Pin Assignment



IEC Logic Symbol



Truth Table

А	В	Y
L	L	L
L	Н	L
Н	L	L
Н	Н	Н

Absolute Maximum Ratings (Note)

Symbol Unit Characteristics Rating Supply voltage range Vcc -0.5 to 7.0 V V DC input voltage -0.5 to 7.0 VIN DC output voltage Vout -0.5 to Vcc + 0.5 V Input diode current Iк -20 mΑ Output diode current ±20 mΑ loĸ DC output current ±25 mΑ IOUT DC V_{cc}/ground current Icc ± 50 mΑ Power dissipation PD 180 mW Storage temperature -65 to 150 °C Tstg

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	2.0 to 5.5	V
Input voltage	VIN	0 to 5.5	V
Output voltage	Vout	0 to V _{CC}	V
Operating temperature	Topr	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 100 (V _{CC} = 3.3 ± 0.3 V) 0 to 20 (V _{CC} = 5 ± 0.5 V)	ns/V

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

Electrical Characteristics

DC Characteristics

Characteristics Symbol		Test Condition			Ta = 25°C			Ta = −40 to 85°C		Unit
				Vcc (V)	Min	Тур.	Max	Min	Max	••••
High-level input voltage	VIH	_		2.0 3.0 to 5.5	1.50 V _{CC} × 0.7		_	1.50 V _{CC} × 0.7		V
Low-level input voltage	VIL	_		2.0 3.0 to 5.5			0.50 V _{CC} × 0.3		0.50 V _{CC} × 0.3	V
High-level output voltage VOH	Vон	VIN = VIH	$I_{OH} = -50 \ \mu A$ $I_{OH} = -4 \ m A$	2.0 3.0 4.5 3.0	1.9 2.9 4.4 2.58	2.0 3.0 4.5	 	1.9 2.9 4.4 2.48		v
			I _{OH} = -8 mA	4.5 2.0	3.94	— 0.0	— 0.1	3.80	— 0.1	
	VIN = VIH or VIL	I _{OL} = 50 μA	3.0 4.5	_ _	0.0 0.0	0.1 0.1	_	0.1 0.1	V	
			I _{OL} = 4 mA I _{OL} = 8 mA	3.0 4.5			0.36 0.36		0.44 0.44	
Input leakage current	lın	VIN = 5.5 V or GND		0 to 5.5	-	_	±0.1	Ι	±1.0	μA
Quiescent supply current	lcc	VIN = VCC or GND		5.5	_		2.0	_	20.0	μA



AC Characteristics (input: $t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = −40 to 85°C		Unit	
	Cymbol		V _{CC} (V)	C _L (pF)	Min	Тур.	Max	Min	Max	Ont
Propagation delay time	tpLH tpHL	_	3.3 ± 0.3	15	_	6.2	8.8	1.0	10.5	- ns
				50	_	8.7	12.3	1.0	14.0	
			5.0 ± 0.5	15		4.3	5.9	1.0	7.0	
				50		5.8	7.9	1.0	9.0	
Input capacitance	CIN		_		_	4	10	_	10	pF
Power dissipation capacitance	Cpd			(Note)	_	18		_	_	pF

Note: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

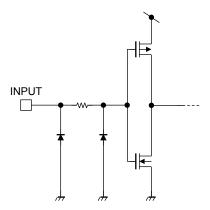
Average operating current can be obtained by the equation:

ICC (opr) = CPD·VCC·fIN + ICC/4 (per gate)

Noise Characteristics (input: $t_r = t_f = 3 ns$)

Ob and sharing the		Test Condition		Ta =	25°C	l lució
Characteristics	Symbol		V _{CC} (V)	Тур.	Limit	Unit
Quiet output maximum dynamic VOL	Volp	CL = 50 pF	5.0	0.3	0.8	V
Quiet output minimum dynamic VOL	Volv	CL = 50 pF	5.0	-0.3	-0.8	V
Minimum high level dynamic input voltage	VIHD	CL = 50 pF	5.0	-	3.5	V
Maximum low level dynamic input voltage	VILD	CL = 50 pF	5.0		1.5	V

Input Equivalent Circuit

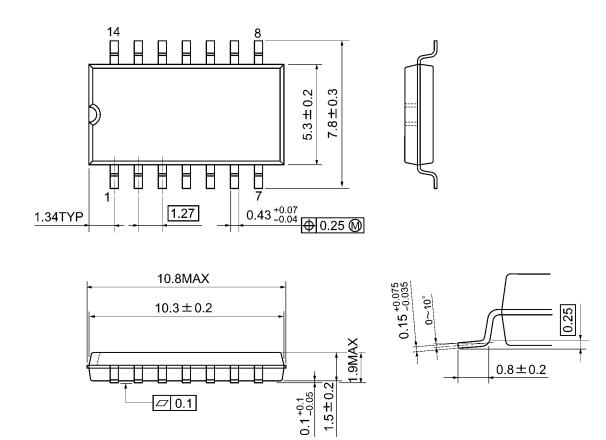




Package Dimensions

SOP14-P-300-1.27A

Unit: mm



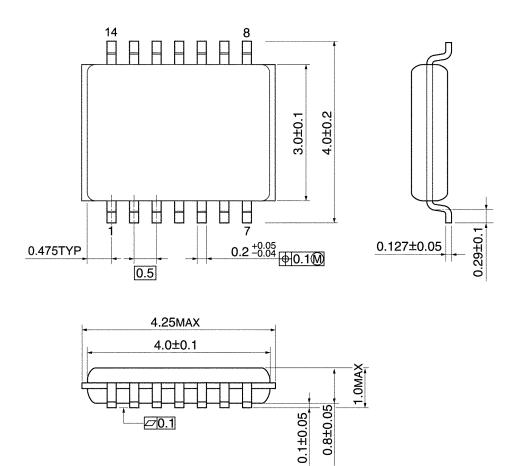
Weight: 0.18 g (typ.)



Package Dimensions

VSSOP14-P-0030-0.50

Unit: mm



Weight: 0.02 g (typ.)

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