

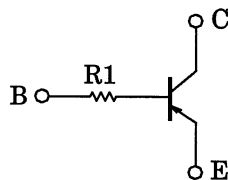
TOSHIBA Transistor Silicon PNP Epitaxial Type (PCT Process) (Bias Resistor built-in Transistor)

RN2610,RN2611

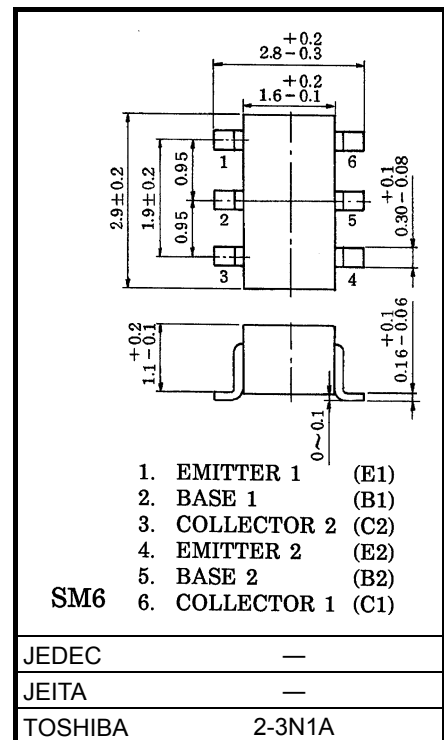
Switching, Inverter Circuit, Interface Circuit and Driver Circuit Applications

- Including twodevices in SM6 (super mini type with 6 leads)
- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- Complementary to RN1610 to RN1611

Equivalent Circuit



Unit in mm



1. EMITTER 1 (E1)
 2. BASE 1 (B1)
 3. COLLECTOR 2 (C2)
 4. EMITTER 2 (E2)
 5. BASE 2 (B2)
 6. COLLECTOR 1 (C1)
- SM6

Absolute Maximum Ratings (Ta = 25°C) (Q1, Q2 common)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	V_{CBO}	-50	V
Collector-emitter voltage	V_{CEO}	-50	V
Emitter-base voltage	V_{EBO}	-5	V
Collector current	I_C	-100	mA
Collector power dissipation	P_C^*	300	mW
Junction temperature	T_j	150	°C
Storage temperature range	T_{stg}	-55 to 150	°C

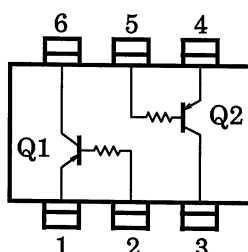
Weight: 15 mg (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

*Total rating

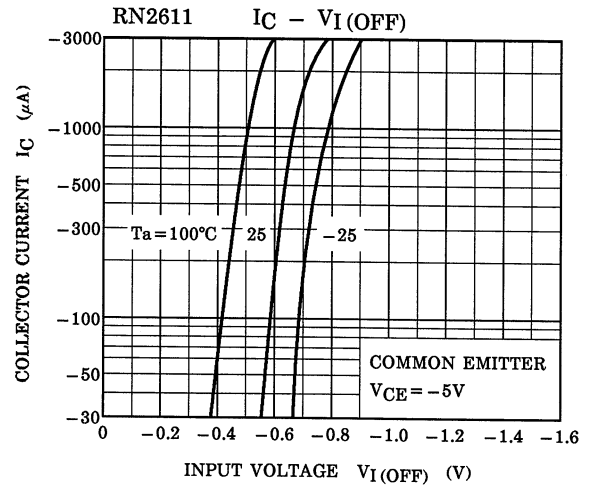
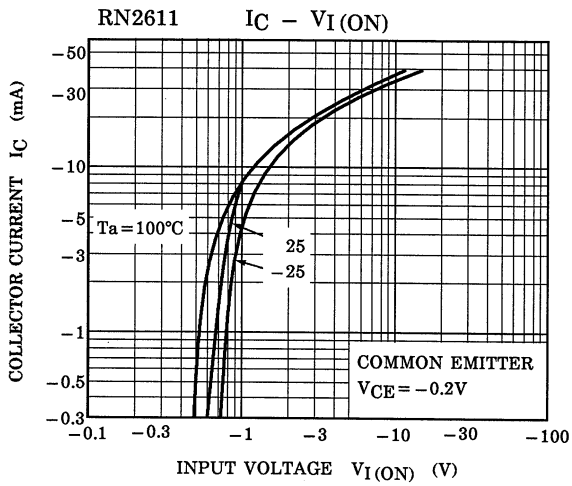
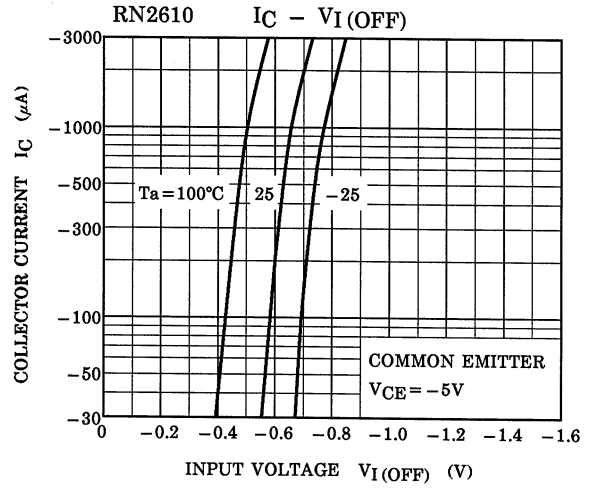
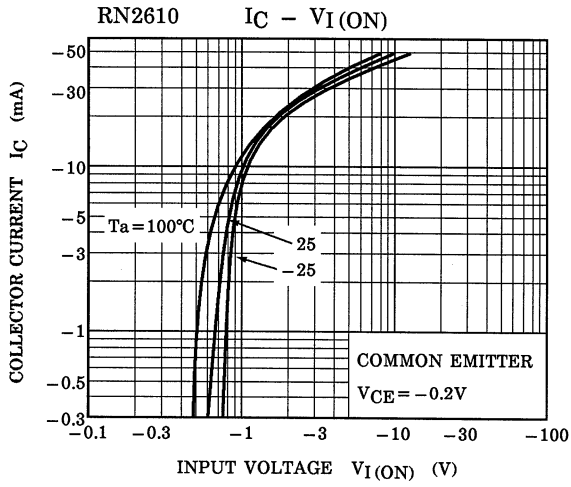
Equivalent Circuit (top view)



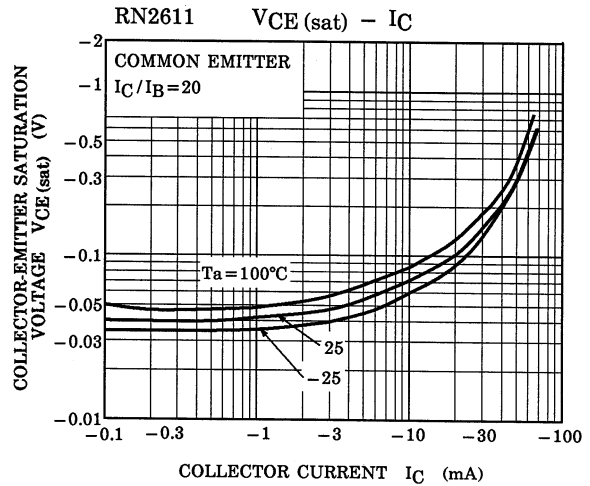
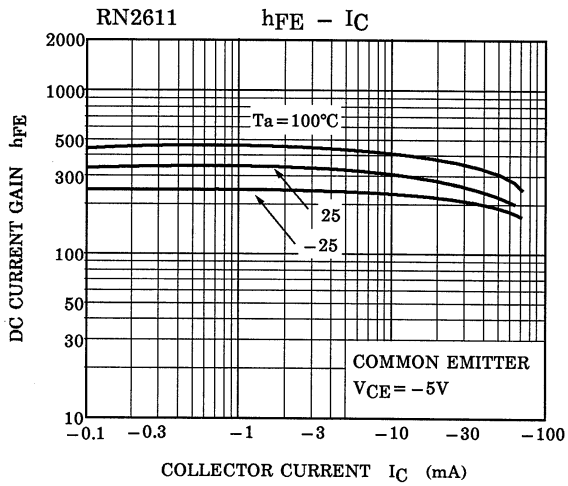
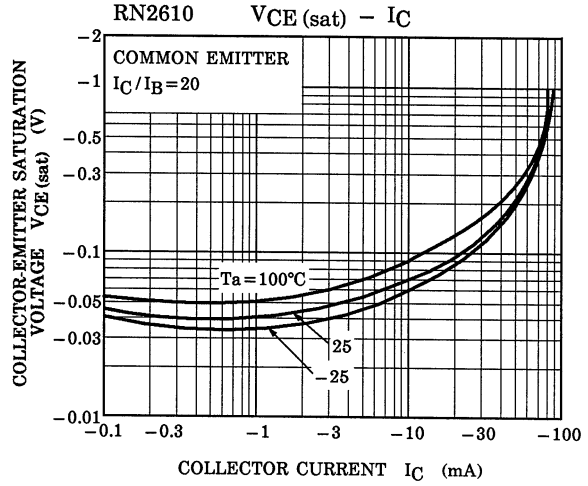
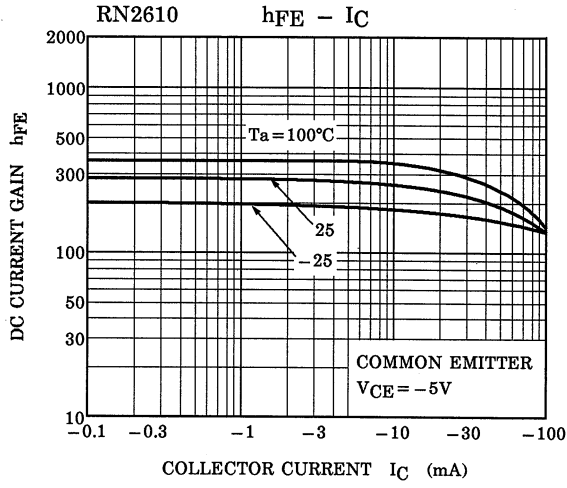
Electrical Characteristics (Ta = 25°C) (Q1, Q2 Common)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Typ.	Max	Unit
Collector cut-off current	I_{CBO}	—	$V_{CB} = -50V, I_E = 0$	—	—	-100	nA
Emitter cut-off current	I_{EBO}	—	$V_{EB} = -5V, I_C = 0$	—	—	-100	nA
DC current gain	h_{FE}	—	$V_{CE} = -5V, I_C = -1mA$	120	—	400	—
Collector-emitter saturation voltage	$V_{CE(sat)}$	—	$I_C = -5mA, I_B = -0.25mA$	—	-0.1	-0.3	V
Transition frequency	f_T	—	$V_{CE} = -10V, I_C = -5mA$	—	200	—	MHz
Collector output capacitance	C_{ob}	—	$V_{CB} = -10V, I_E = 0, f = 1MHz$	—	3	6	pF
Input resistor	RN2610	R1	—	3.29	4.7	6.11	kΩ
	RN2611			7	10	13	

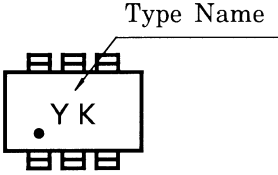
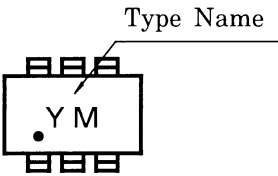
(Q1, Q2 Common)



(Q1, Q2 Common)



Marking

Type Name	Marking
RN2610	 A schematic diagram of a rectangular component with four pins on each of the top and bottom edges. The marking 'Y K' is printed in the center, with a small dot to the left of the 'Y'. A line labeled 'Type Name' points to the top edge of the component.
RN2611	 A schematic diagram of a rectangular component with four pins on each of the top and bottom edges. The marking 'Y M' is printed in the center, with a small dot to the left of the 'Y'. A line labeled 'Type Name' points to the top edge of the component.

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