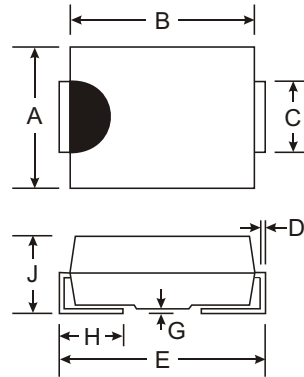


### Features

- Glass Passivated Die Construction
- Super-Fast Recovery Time For High Efficiency
- Low Forward Voltage Drop and High Current Capability
- Surge Overload Rating to 75A Peak
- Ideally Suited for Automated Assembly
- Plastic Material: UL Flammability Classification Rating 94V-0



SMC		
Dim	Min	Max
A	5.59	6.22
B	6.60	7.11
C	2.75	3.18
D	0.15	0.31
E	7.75	8.13
G	0.10	0.20
H	0.76	1.52
J	2.00	2.62
All Dimensions in mm		

### Mechanical Data

- Case: SMC, Molded Plastic
- Terminals: Solder Plated Terminal - Solderable per MIL-STD-202, Method 208
- Moisture sensitivity: Level 1 per J-STD-020A
- Also Available in Lead Free Plating (Matte Tin Finish). Please see Ordering Information, Note 4, on Page 1
- Marking: U3D
- Polarity: Cathode Band or Cathode Notch
- Weight: 0.21 grams (approx.)

### Maximum Ratings and Electrical Characteristics @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load.  
For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	$V_{RRM}$ $V_{RWM}$ $V_R$	200	V
RMS Reverse Voltage	$V_{R(RMS)}$	140	V
Average Rectified Output Current @ $T_L = 140^\circ\text{C}$	$I_O$	3.0	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave Superimposed on Rated Load (JEDEC Method)	$I_{FSM}$	75	A
Forward Voltage @ $I_F = 3.0\text{A}$ , $T_J = 25^\circ\text{C}$	$V_{FM}$	0.9	V
Peak Reverse Current @ $T_J = 25^\circ\text{C}$ at Rated DC Blocking Voltage @ $T_J = 150^\circ\text{C}$	$I_{RM}$	5.0 100	$\mu\text{A}$
Reverse Recovery Time (Note 3)	$t_{rr}$	25	ns
Typical Total Capacitance (Note 2)	$C_T$	45	pF
Typical Thermal Resistance, Junction to Terminal (Note 1)	$R_{\theta JT}$	11	$^\circ\text{C/W}$
Operating and Storage Temperature Range	$T_j$ , $T_{STG}$	-55 to +150	$^\circ\text{C}$

- Notes:
1. Unit mounted on PC board with  $5.0\text{mm}^2$  (0.013 mm thick) copper pads as heat sink.
  2. Measured at 1.0MHz and applied reverse voltage of 0V DC.
  3. Measured with  $I_F = 0.5\text{A}$ ,  $I_R = 1.0\text{A}$ ,  $I_{rr} = 0.25\text{A}$ . See Figure 5.
  4. For Lead Free version (with Lead Free terminal finish) part number, please add "-F" suffix to part number above.  
Example: MURS320-13-F.

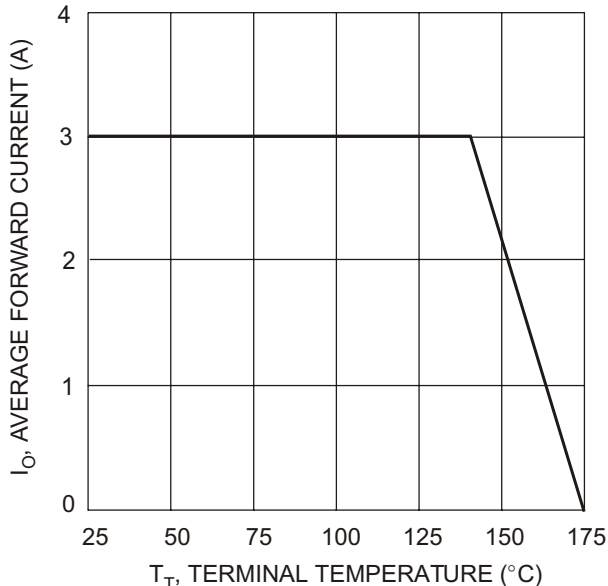


Fig. 1 Forward Current Derating Curve

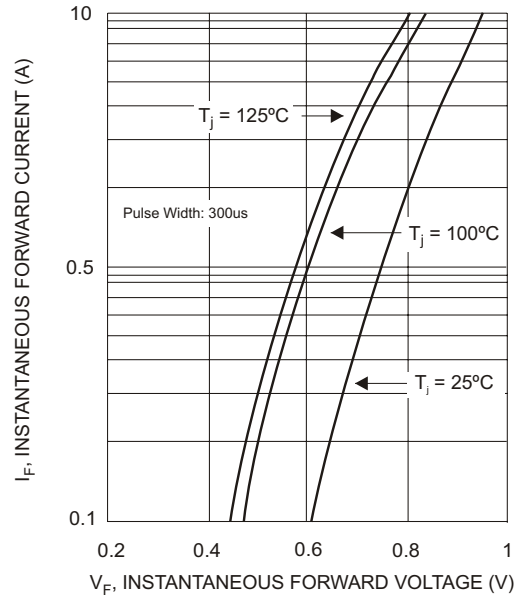


Fig. 2 Typical Forward Characteristics

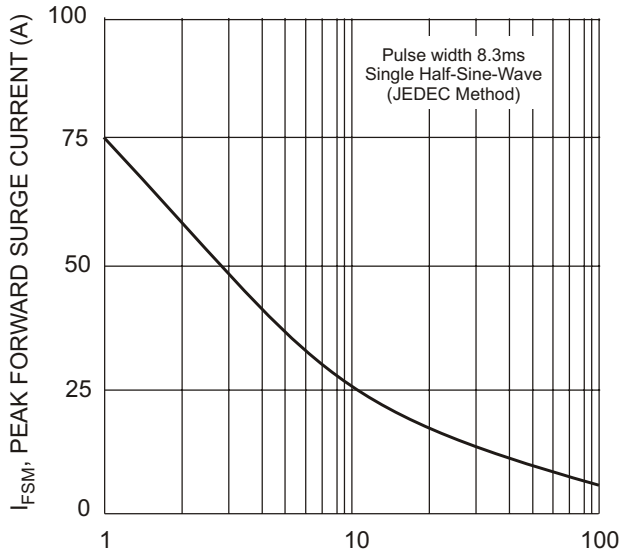


Fig. 3 Surge Current Derating Curve

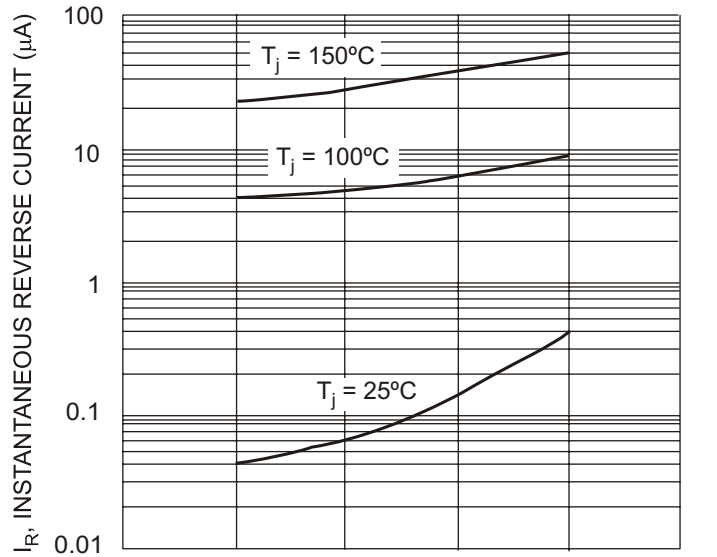
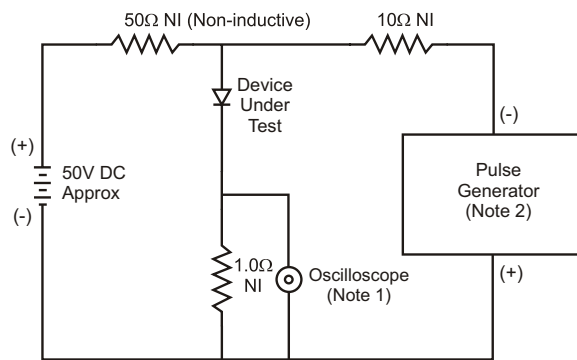
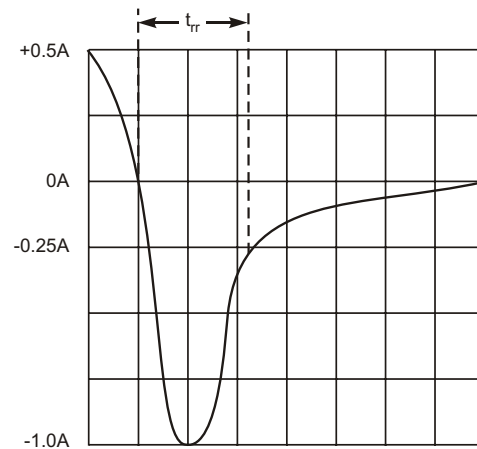


Fig. 4 Typical Reverse Characteristics



- Notes:  
 1. Rise Time = 7.0ns max. Input Impedance = 1.0MΩ, 22pF.  
 2. Rise Time = 10ns max. Input Impedance = 50Ω.



Set time base for 50/100 ns/cm

Fig. 5 Reverse Recovery Time Characteristic and Test Circuit