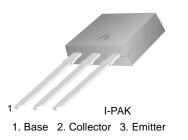
FAIRCHILD

SEMICONDUCTOR®

MJD1222

Power Amplifier Applications High DC Current Gain

- Low Collector-Emitter Saturation Voltage
- Built in a Damper Diode at E-C
- Darlington TR
- Complement to MJD907



NPN Epitaxial Silicon Transistor

Absolute Maximum Ratings T_C=25°C unless otherwise noted

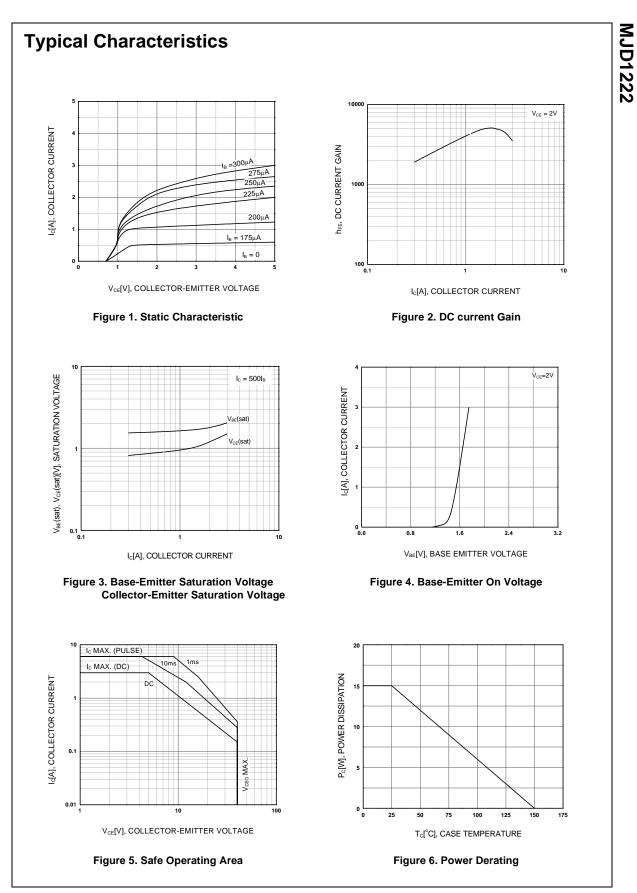
Symbol	Parameter	Value	Units	
V _{CBO}	Collector-Base Voltage	60	V	
V _{CEO}	Collector-Emitter Voltage	40	V	
V _{EBO}	Emitter-Base Voltage	5	V	
I _C	Collector Current	3	А	
I _B	Base Current	0.3	А	
P _C Collecto	Collector Dissipation (T _C =25°C)	15	W	
	Collector Dissipation (T _a =25°C)	1	W	
TJ	Junction Temperature	150	°C	
T _{STG}	Storage Temperature	- 55 ~ 150	°C	

Electrical Characteristics T_C=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV _{CEO}	*Collector-Emitter Breakdown Voltage	I _C = 25mA, I _B = 0	40			V
I _{CBO}	Collector Cut-off Current	$V_{CB} = 60V, I_E = 0$			20	μΑ
I _{EBO}	Emitter Cut-off Current	$V_{EB} = 5V, I_{C} = 0$			2.5	mA
h _{FE1} h _{FE2}	*DC Current Gain	$V_{CE} = 2V, I_C = 1A$ $V_{CE} = 2V, I_C = 3A$	2000 1000			
V _{CE} (sat)	*Collector-Emitter Saturation Voltage	$I_{\rm C} = 2A, I_{\rm B} = 4mA$			1.5	V
V _{BE} (sat)	*Base-Emitter Saturation Voltage	$I_{\rm C} = 2A, I_{\rm B} = 4mA$			2	V
t _{ON}	Turn ON Time	$V_{CC} = 30V, I_{C} = 3A$		0.1		μs
t _{STG}	Storage Time	$I_{B1} = -I_{B2} = 6mA$		1		μs
t _F	Fall Time	$R_{L} = 10\Omega$		0.2		μs

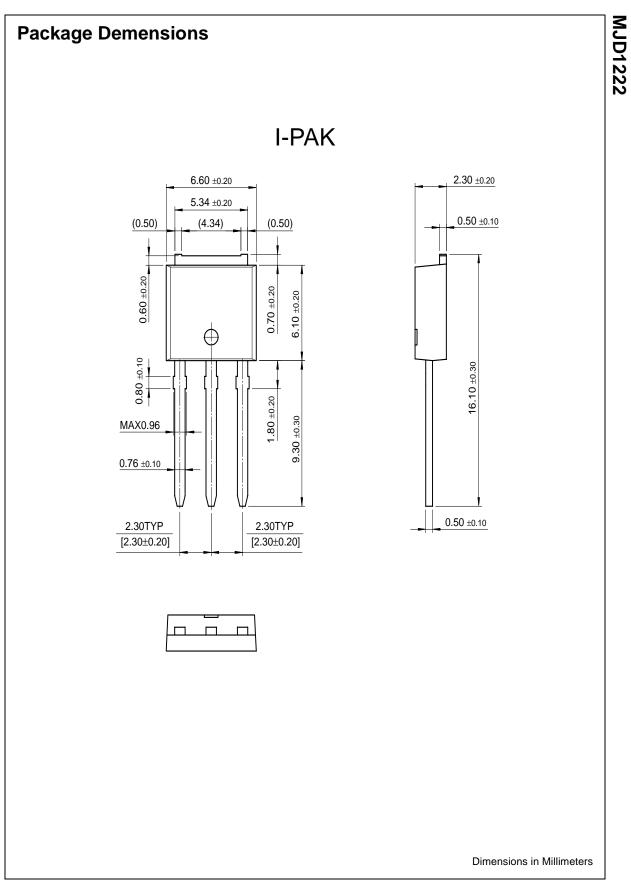
* Pulse Test: PW ≤ 300ms, Duty Cycle ≤ 2%

MJD1222



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Definition of Terms

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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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