

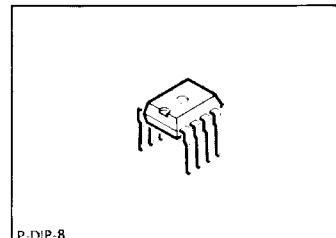
Dual Operational Amplifier

TBB 1458

Features

- NPN input
- High differential input voltage
- Short-circuit proof
- Push-pull output
- Fully compatible with industrial standard type 1458

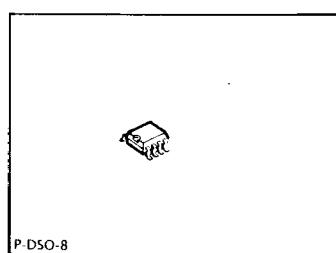
Bipolar IC



P-DIP-8

Applications

- Amplifier
- Comparator



P-DSO-8

The op amp TBB 1458 is outstanding for its large common-mode and differential input voltage range, as well as its short-circuit strength. No external components are required for frequency compensation.

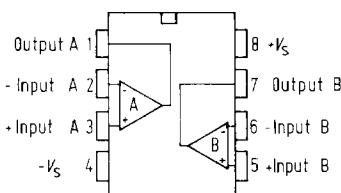
For single amplifier performance refer to the TBA 221 op amp.

Type	Ordering Code	Package	Color Code
■ TBB 1458 B	Q67000-A1036	P-DIP-8	—
■ TBB 1458 G	Q67000-A1458	P-DSO-8 (SMD)	orange/orange

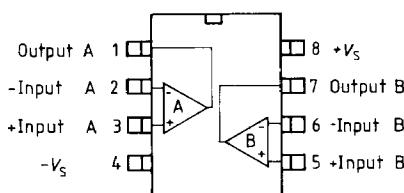
■ = Not for new Design

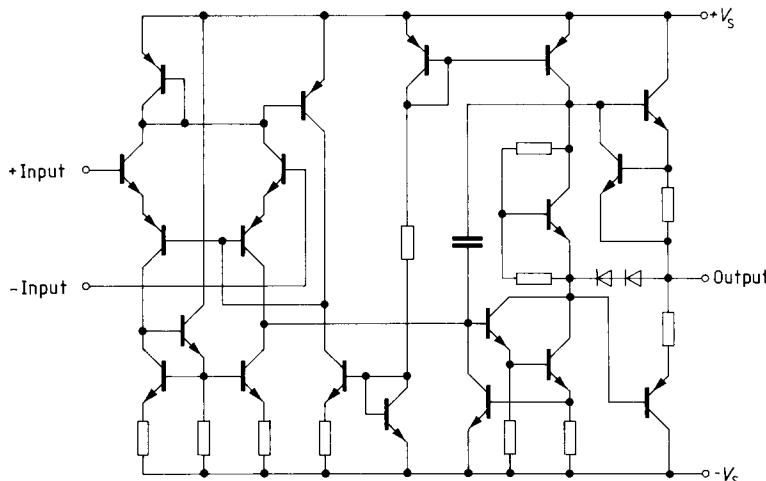
Pin Configurations (top view)

TBB 1458 B



TBB 1458 G



Circuit Diagram of one Op Amp**Absolute Maximum Ratings**

Parameter		Symbol	Limit Values	Unit
Supply voltage		V_S	± 18	V
Input voltage ¹⁾		V_I	± 15	V
Differential input voltage ²⁾		V_{ID}	± 30	V
Output short-circuit duration ³⁾		t_{QSC}	∞	
Junction temperature		T_j	150	°C
Storage temperature range		T_{stg}	-55 to 125	°C
Thermal resistance	TBB 1458 B TBB 1458 G	$R_{th\ SA}$	100 170	K/W K/W

Operating Range

Supply voltage	V_S	± 4 to ± 18	V
Ambient temperature	T_A	0 to 70	°C

¹⁾ For supply voltages less than ± 15 V, the maximum input voltage is equal to the supply voltage.

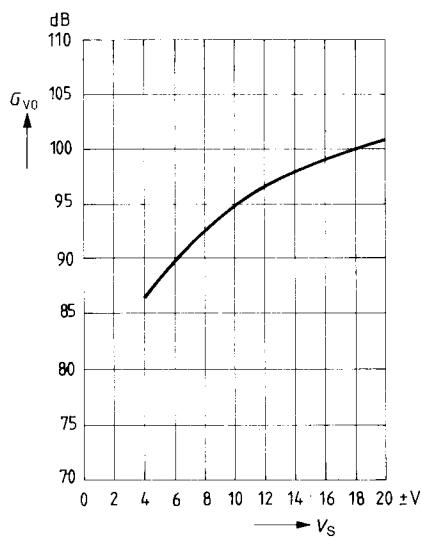
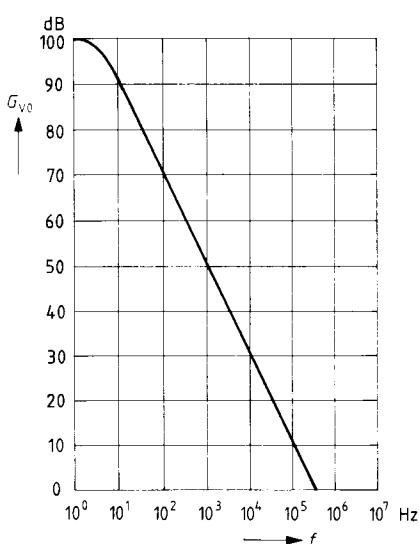
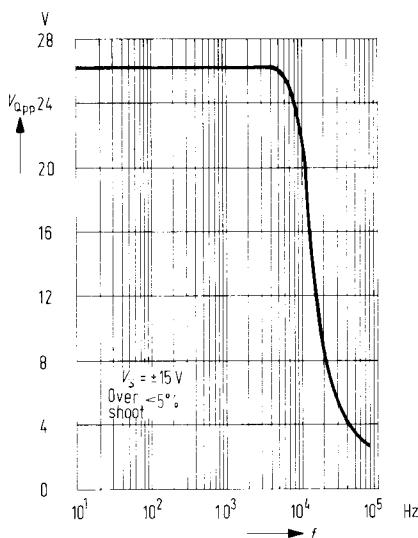
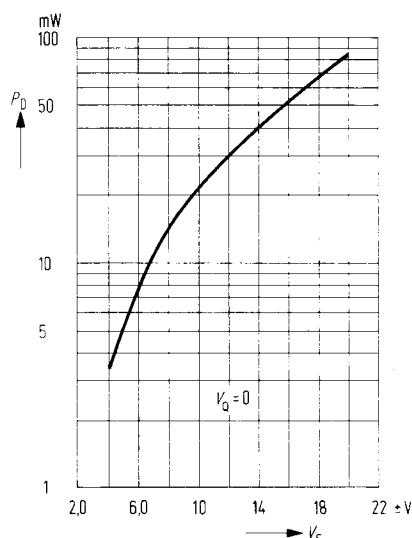
²⁾ For supply voltages less than ± 15 V, the maximum differential input voltage is equal to $(V_S + |V_{S-}|)$.

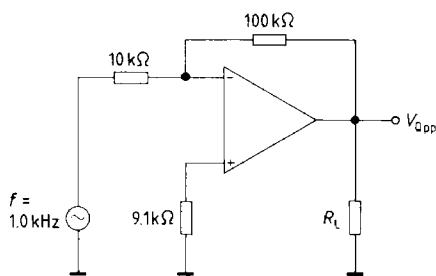
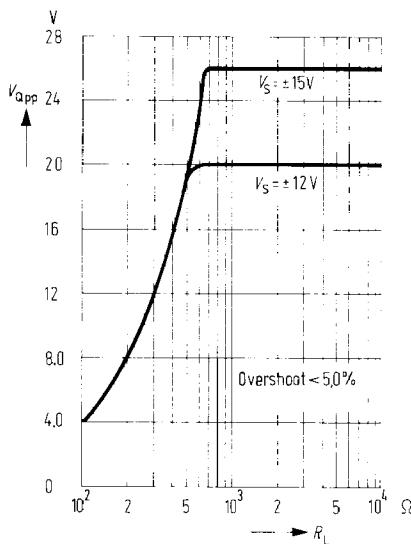
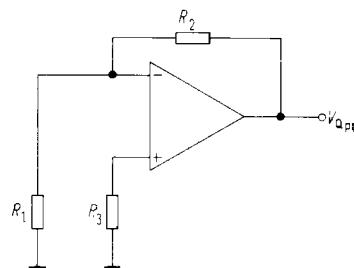
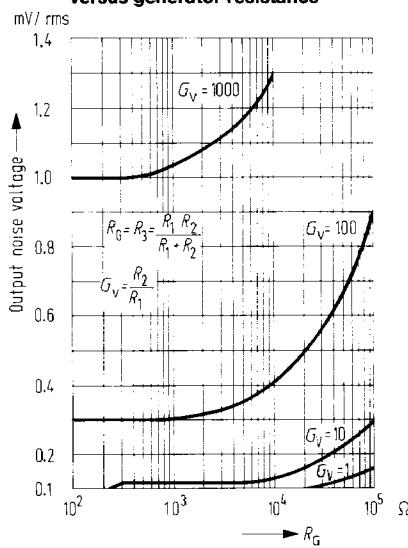
³⁾ Short circuit may be ground or to be supply voltage $\pm V_S$, whereby the maximum ratings must not be exceeded.

Characteristics $V_S = \pm 15 \text{ V}$

Parameter	Symbol	Limit Values $T_A = 25^\circ\text{C}$			Limit Values $T_A = 0^\circ\text{C}$ to 70°C		Unit
		min.	typ.	max.	min.	max.	
Input offset voltage $R_G \leq 10 \text{ k}\Omega$	V_{IO}	-6		6	-7.5	7.5	mV
Input offset current Input current Open-loop supply current consumption, total Output short-circuit current	I_{IO} I_I I_S I_{QSC}	-200	± 20 80	200 500	-300	300 800	nA nA mA mA
Input resistance	R_I	0.3	1				MΩ
Input capacitance	C_I		6				pF
Output resistance	R_Q		75				Ω
Control range $R_L \geq 10 \text{ k}\Omega$ $R_L \geq 2 \text{ k}\Omega$	V_{Q_pp} V_{Q_pp}	13 11	± 14 ± 13	-13 -11			V V
Common-mode input voltage range	V_{IC}	$-V_S + 3$		$V_S - 3$			V
Voltage gain $V_{Q_pp} = \pm 10 \text{ V}$, $R_L = 2 \text{ k}\Omega$	G_V	86	100		84		dB
Common-mode rejection $R_G \leq 10 \text{ k}\Omega$	k_{CMR}	70	90				dB
Supply voltage rejection	k_{SCR}		30	150		150	μV/V
Temperature coefficient of V_{IO} Temperature coefficient of I_{IO}	α_{VIO} α_{IIO}		3 0.4				μV/K nA/K
Slew rate ¹⁾ $G_V = 1$, $R_L \geq 2 \text{ k}\Omega$	SR		0.5				V/μs

¹⁾ For the relationship between power bandwidth and slew rate refer to "Introduction to Operational Amplifiers"

Open-loop voltage gain versus supply voltage**Open-loop voltage gain versus frequency****Power bandwidth
Output voltage versus frequency****Power dissipation versus supply voltage**

Output voltage versus load resistance**Output noise voltage versus generator resistance**

For additional characteristic curves refer to TBA 221.