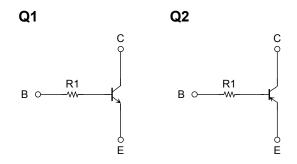
TOSHIBA Transistor Silicon NPN/PNP Epitaxial Type (PCT Process) (Transistor with Built-in Bias Resistor)

# RN4990AFS

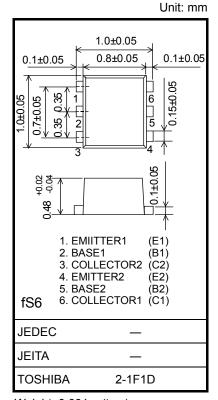
# Switching, Inverter Circuit, Interface Circuit and Driver Circuit Applications

- Two devices are incorporated into a fine-pitch, small-mold (6-pin) package.
- Incorporating a bias resistor into the transistor reduces the number of parts, so enabling the manufacture of ever more compact equipment and lowering assembly cost.

### **Equivalent Circuit and Bias Resistor Values**

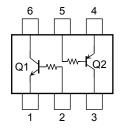


R1:  $4.7 \text{ k}\Omega$  (Q1, Q2 common)

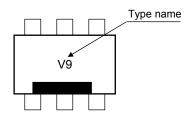


Weight: 0.001 g (typ.)

#### **Equivalent Circuit (top view)**



#### Marking



#### Absolute Maximum Ratings (Ta = 25°C) (Q1)

| Characteristic            | Symbol           | Rating | Unit |
|---------------------------|------------------|--------|------|
| Collector-base voltage    | $V_{CBO}$        | 50     | V    |
| Collector-emitter voltage | V <sub>CEO</sub> | 50     | V    |
| Emitter-base voltage      | V <sub>EBO</sub> | 5      | V    |
| Collector current         | IC               | 80     | mA   |

#### Absolute Maximum Ratings (Ta = 25°C) (Q2)

| Characteristic            | Symbol           | Rating | Unit |
|---------------------------|------------------|--------|------|
| Collector-base voltage    | V <sub>CBO</sub> | -50    | V    |
| Collector-emitter voltage | V <sub>CEO</sub> | -50    | V    |
| Emitter-base voltage      | V <sub>EBO</sub> | -5     | V    |
| Collector current         | IC               | -80    | mA   |

#### Absolute Maximum Ratings (Ta = 25°C) (Q1, Q2 common)

| Characteristic              | Symbol                  | Rating     | Unit |
|-----------------------------|-------------------------|------------|------|
| Collector power dissipation | P <sub>C</sub> (Note 1) | 50         | mW   |
| Junction temperature        | Tj                      | 150        | °C   |
| Storage temperature range   | T <sub>stg</sub>        | -55 to 150 | °C   |

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

2

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Total rating

2014-03-01

# Electrical Characteristics (Ta = 25°C) (Q1)

| Characteristic                       | Symbol                | Test Condition  | Min | Тур. | Max  | Unit |
|--------------------------------------|-----------------------|---|-----|------|------|------|
| Collector cutoff current             | I <sub>CBO</sub>      | $V_{CB} = 50 \text{ V}, I_E = 0$                      | _   | _    | 100  | nA   |
| Emitter cutoff current               | I <sub>EBO</sub>      | V <sub>EB</sub> = 5 V, I <sub>C</sub> = 0             | _   | _    | 100  | nA   |
| DC current gain                      | h <sub>FE</sub>       | $V_{CE} = 5 \text{ V}, I_{C} = 1 \text{ mA}$          | 120 | _    | 700  |      |
| Collector-emitter saturation voltage | V <sub>CE</sub> (sat) | $I_C = 5 \text{ mA}, I_B = 0.25 \text{ mA}$           | _   | _    | 0.15 | ٧    |
| Collector output capacitance         | C <sub>ob</sub>       | V <sub>CB</sub> = 10 V, I <sub>E</sub> = 0, f = 1 MHz | _   | 0.7  | _    | pF   |

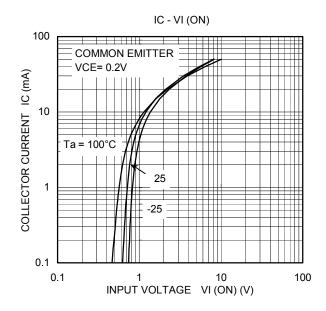
# Electrical Characteristics (Ta = 25°C) (Q2)

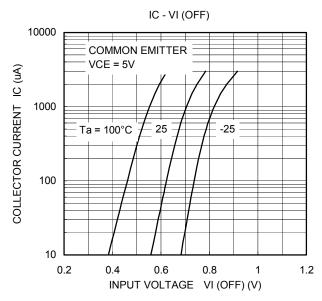
| Characteristics                      | Symbol                | Test Condition                                       | Min | Тур. | Max   | Unit |
|--------------------------------------|-----------------------|--|-----|------|-------|------|
| Collector cutoff current             | I <sub>CBO</sub>      | $V_{CB} = -50 \text{ V}, I_E = 0$                    | _   | _    | -100  | nA   |
| Emitter cutoff current               | I <sub>EBO</sub>      | $V_{EB} = -5 \text{ V}, I_{C} = 0$                   | _   | _    | -100  | nA   |
| DC current gain                      | h <sub>FE</sub>       | $V_{CE} = -5 \text{ V}, I_{C} = -1 \text{ mA}$       | 120 | _    | 400   |      |
| Collector-emitter saturation voltage | V <sub>CE</sub> (sat) | $I_C = -5 \text{ mA}, I_B = -0.25 \text{ mA}$        | _   | _    | -0.15 | V    |
| Collector output capacitance         | C <sub>ob</sub>       | $V_{CB} = -10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$ |     | 0.9  | _     | pF   |

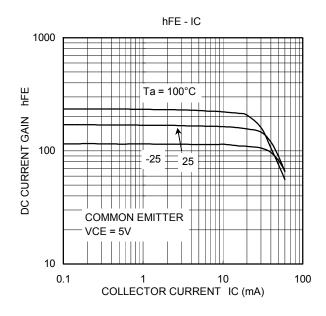
# Electrical Characteristics (Ta = 25°C) (Q1, Q2 common)

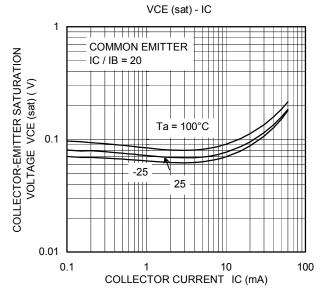
| Characteristic | Symbol | Test Condition | Min  | Тур. | Max  | Unit |
|----------------|--------|----------------|------|------|------|------|
| Input resistor | R1     | _              | 3.76 | 4.7  | 5.64 | kΩ   |

Q1

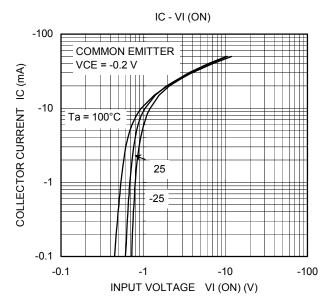


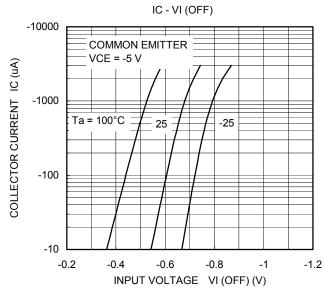


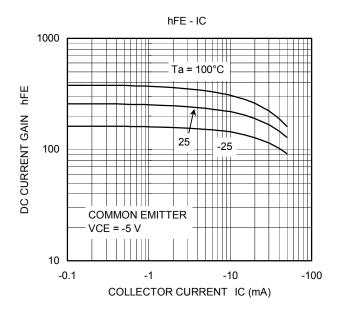


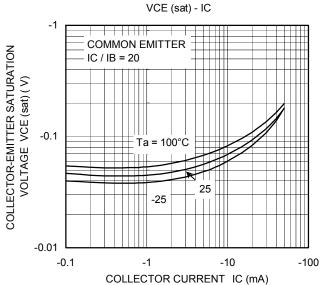


Q2









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