



3.3V CMOS Bus Interface 8-bit Latches

QS74FCT3573

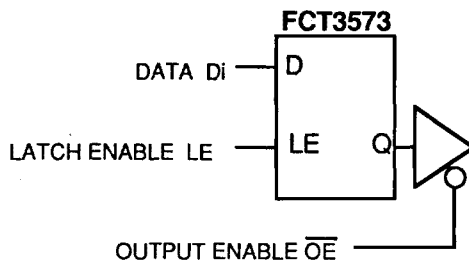
FEATURES/BENEFITS

- Pin and Function compatible to the 74F573 74FCT573 and 74FCT573T
- CMOS power levels: <7.5 mW static
- Available in DIP, ZIP, SOIC, QSOP, LCC
- Undershoot clamp diodes on all inputs
- Military product compliant to MIL-STD-883
- JEDEC spec compatible
- Standard through C speed grades
- I_{OL} = 24 mA Com.
- TTL-compatible input and output levels
- Ground bounce controlled outputs

DESCRIPTION

The QSFACT3573 device is an 8-bit high-speed CMOS TTL-compatible buffered latch with three-state outputs that are ideal for driving high capacitance loads such as memory and address buses, and all inputs have clamp diodes for undershoot noise suppression. All outputs have ground bounce suppression.

FUNCTIONAL BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS

Supply Voltage to Ground..... -0.5V to +4.6V
 DC Output Voltage V_O -0.5V to $V_{CC}+0.5V$
 DC Input Voltage V_I -0.5V to $V_{CC}+0.5V$
 AC Input Voltage (for a pulse width ≤ 20 ns)..... -3.0V
 DC Input Diode Current with $V_I < 0$ -20 mA
 DC Output Diode Current with $V_O < 0$ -60 mA
 DC Output Current Max. sink current/pin..... 60 mA
 Maximum Power Dissipation..... 0.5 watts
 T_{STG} Storage Temperature..... -65° to +165°C

DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

Commercial $T_A=0^\circ\text{C}$ to 70°C , $V_{CC}=3.3V\pm 0.3V$

Symbol	Parameter	Test Conditions		Min	Typ (1)	Max	Unit
V_{ih}	Input High Voltage	Logic HIGH for All Inputs		2.0	-	$V_{CC}-0.5$	Volts
V_{il}	Input LOW Voltage	Logic LOW for All Inputs		-0.5	-	0.8	
ΔV_t	Input Hysteresis	$V_{th} - V_{tl}$ for All Inputs		-	0.2	-	
$ i_{ih} $ $ i_{il} $	Input Current Input HIGH or LOW	$V_{CC} = \text{MAX}$	$0 \leq V_{in} < V_{CC}$	-	-	5	μA
$ i_{oz} $	Off State Output Current (Hi-Z)	$V_{CC} = \text{MAX}, 0 \leq V_{in} \leq V_{CC}$		-	-	5	
i_{os}	Short Circuit Current	$V_{CC} = \text{MAX}, V_o = \text{GND} (2,3)$		-60	-	-225	mA
V_{ic}	Input Clamp Voltage	$V_{CC} = \text{MIN}, i_{in} = 18 \text{ mA} (3)$		-	-0.7	-1.2	Volts
V_{oh}	Output HIGH Voltage	$V_{CC} = \text{MIN}$	$i_{oh} = 100\mu\text{A}$	$V_{CC}-0.2$	-	-	Volts
			$i_{oh} = 8\text{mA}$	$V_{CC}-0.6$	-	-	
V_{ol}	Output LOW Voltage	$V_{CC} = \text{MIN}$	$i_{ol} = 100\mu\text{A}$	-	-	0.2	
			$i_{ol} = 16\text{mA}$	-	-	0.4	
			$i_{ol} = 24\text{mA}$	-	-	0.5	

Notes:

1. Typical values indicate $V_{CC}=3.3V$ and $T_A=25^\circ\text{C}$.
2. Not more than one output should be shorted and the duration is ≤ 1 second.
3. These parameters are guaranteed by design but not tested.