

SN54HC157, SN54HC158, SN74HC157, SN74HC158 QUADRUPLE 2-LINE TO 1-LINE DATA SELECTORS/MULTIPLEXERS

D2684, DECEMBER 1982—REVISED SEPTEMBER 1987

- Package Options Include Plastic "Small Outline" Packages, Ceramic Chip Carriers, and Standard Plastic and Ceramic 300-mil DIPs
- Dependable Texas Instruments Quality and Reliability

description

These monolithic data selectors/multiplexers contain inverters and drivers to supply full data selection to the four output gates. A separate strobe input (\bar{G}) is provided. A 4-bit word is selected from one of two sources and is routed to the four outputs. The 'HC157 presents true data whereas the 'HC158 presents inverted data.

The SN54HC157 and SN54HC158 are characterized for operation over the full military temperature range of -55°C to 125°C . The SN74HC157 and SN74HC158 are characterized for operation from -40°C to 85°C .

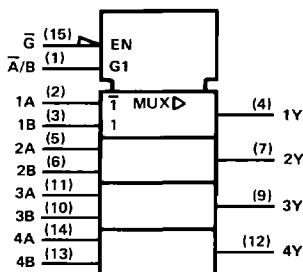
FUNCTION TABLE

STROBE \bar{G}	SELECT \bar{A}/B	INPUTS		OUTPUT Y	
		A	B	'HC157	'HC158
H	X	X	X	L	H
L	L	L	X	L	H
L	L	H	X	H	L
L	H	X	L	L	H
L	H	X	H	H	L

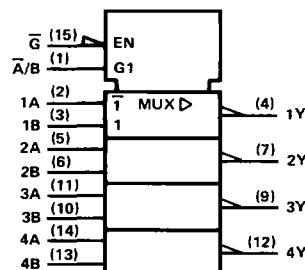
H = high level, L = low level, X = irrelevant

logic symbols‡

'HC157

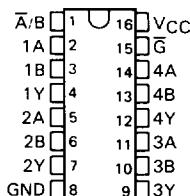


'HC158

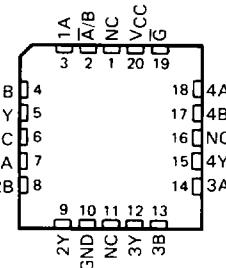


‡These symbols are in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.
Pin numbers shown are for D/DW, J, and N packages.

SN54HC157, SN54HC158 . . . J PACKAGE
SN74HC157, SN74HC158 . . . D/DW[†] OR N PACKAGE
(TOP VIEW)



SN54HC157, SN54HC158 . . . FK PACKAGE
(TOP VIEW)



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HCMOS Devices

NC—No internal connection

†Contact the factory for D or DW availability.

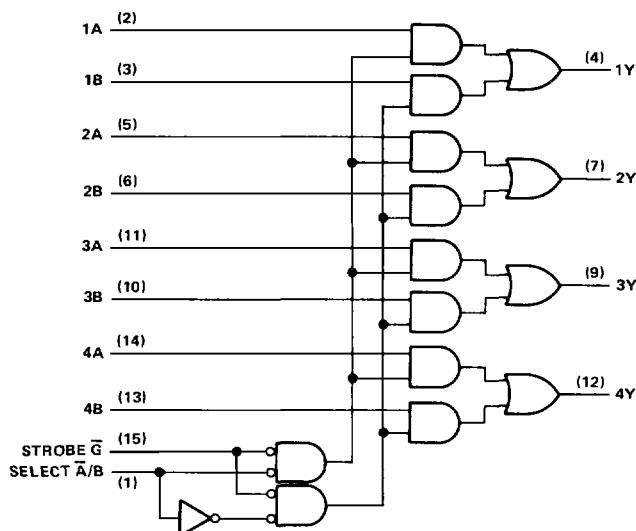
SN54HC157, SN54HC158, SN74HC157, SN74HC158
QUADRUPLE 2-LINE TO 1-LINE DATA SELECTORS/MULTIPLEXERS

logic diagrams (positive logic)

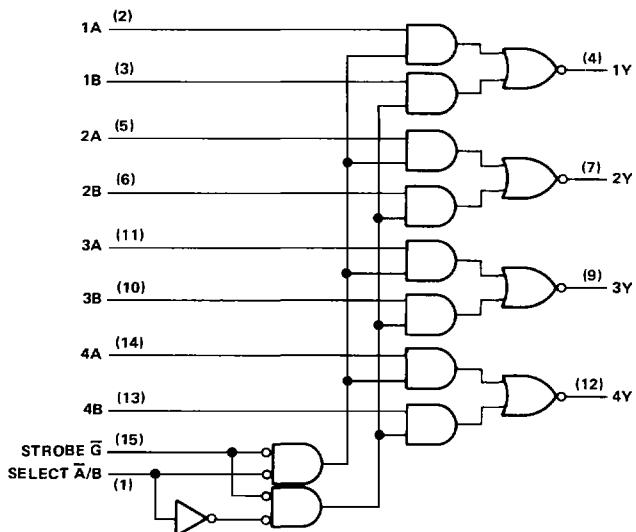
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HCMOS Devices

'HC157



'HC158



Pin numbers shown are for D:DW, J, and N packages.

SN54HC157, SN54HC158, SN74HC157, SN74HC158

QUADRUPLE 2-LINE TO 1-LINE DATA SELECTORS/MUXES

absolute maximum ratings over operating free-air temperature range†

Supply voltage, V _{CC}	-0.5 V to 7 V
Input clamp current, I _{IK} (V _I < 0 or V _I > V _{CC})	±20 mA
Output clamp current, I _{OK} (V _O < 0 or V _O > V _{CC})	±20 mA
Continuous output current, I _O (V _O = 0 to V _{CC})	±35 mA
Continuous current through V _{CC} or GND pins	±70 mA
Lead temperature 1.6 mm (1/16 in) from case for 60 s: FK or J package	300°C
Lead temperature 1.6 mm (1/16 in) from case for 10 s: D/DW or N package	260°C
Storage temperature range	-65°C to 150°C

†Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

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HCMOS Devices

recommended operating conditions

		SN54HC157 SN54HC158			SN74HC157 SN74HC158			UNIT
		MIN	NOM	MAX	MIN	NOM	MAX	
V _{CC} Supply voltage		2	5	6	2	5	6	V
V _{IH} High-level input voltage	V _{CC} = 2 V	1.5			1.5			
	V _{CC} = 4.5 V	3.15			3.15			
	V _{CC} = 6 V	4.2			4.2			
V _{IL} Low-level input voltage	V _{CC} = 2 V	0	0.3	0	0	0.3	0	V
	V _{CC} = 4.5 V	0	0.9	0	0	0.9	0	
	V _{CC} = 6 V	0	1.2	0	0	1.2	0	
V _I Input voltage		0	V _{CC}	0	0	V _{CC}	0	V
V _O Output voltage		0	V _{CC}	0	0	V _{CC}	0	V
t _{tr} Input transition (rise and fall) times	V _{CC} = 2 V	0	1000	0	0	1000	0	ns
	V _{CC} = 4.5 V	0	500	0	0	500	0	
	V _{CC} = 6 V	0	400	0	0	400	0	
T _A Operating free-air temperature		-55	125	-40	85		85	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS	V _{CC}	T _A = 25°C			SN54HC157 SN54HC158		UNIT
			MIN	TYP	MAX	MIN	MAX	
V _{DH}	V _I = V _{IH} or V _{IL} , I _{OH} = -20 μA	2 V	1.9	1.998	-	1.9	1.9	V
		4.5 V	4.4	4.499	-	4.4	4.4	
		6 V	5.9	5.999	-	5.9	5.9	
V _{OL}	V _I = V _{IH} or V _{IL} , I _{OL} = -6 mA	4.5 V	3.98	4.30	-	3.7	3.84	V
		6 V	5.48	5.80	-	5.2	5.34	
		2 V	0.002	0.1	-	0.1	0.1	
I _I	V _I = V _{CC} or 0	4.5 V	0.001	0.1	-	0.1	0.1	nA
		6 V	0.001	0.1	-	0.1	0.1	
		2 V	±0.1	±100	-	±1000	±1000	
I _{CC}	V _I = V _{CC} or 0, I _O = 0	6 V		8	-	160	80	μA
C _i		2 to 6 V	3	10	-	10	10	pF

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switching characteristics over recommended operating free-air temperature range (unless otherwise noted), $C_L = 50 \text{ pF}$ (see Note 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V _{CC}	T _A = 25°C			SN54HC157		SN74HC157		UNIT
				MIN	TYP	MAX	MIN	MAX	MIN	MAX	
t_{pd}	A or B	Y	2 V	63	125		190		160		ns
			4.5 V	13	25		38		32		
			6 V	11	21		32		27		
t_{pd}	\bar{A}/B	Y	2 V	67	125		190		160		ns
			4.5 V	18	25		38		31		
			6 V	14	21		32		27		
t_{pd}	\bar{G}	Y	2 V	59	115		170		145		ns
			4.5 V	16	23		34		29		
			6 V	13	20		29		25		
t_t		Y	2 V	28	60		90		75		ns
			4.5 V	8	12		18		15		
			6 V	6	10		15		13		

C _{pd}	Power dissipation capacitance	No load, T _A = 25°C	40 pF typ
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switching characteristics over recommended operating free-air temperature range (unless otherwise noted), $C_L = 150 \text{ pF}$ (see Note 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V _{CC}	T _A = 25°C			SN54HC157		SN74HC157		UNIT
				MIN	TYP	MAX	MIN	MAX	MIN	MAX	
t_{pd}	A or B	Y	2 V	81	190		290		235		ns
			4.5 V	23	38		58		47		
			6 V	18	33		49		41		
t_{pd}	\bar{A}/B	Y	2 V	81	210		320		260		ns
			4.5 V	23	42		64		52		
			6 V	18	36		54		45		
t_{pd}	\bar{G}	Y	2 V	91	190		290		235		ns
			4.5 V	24	38		58		47		
			6 V	18	33		49		41		
t_t		Y	2 V	45	210		315		265		ns
			4.5 V	17	42		63		53		
			6 V	13	36		53		45		

NOTE 1: Load circuit and voltage waveforms are shown in Section 1.