

# TYPES SN54AS756, SN54AS757, SN74AS756, SN74AS757 OCTAL BUFFERS AND LINE DRIVERS WITH OPEN-COLLECTOR OUTPUTS

D2261, DECEMBER 1983

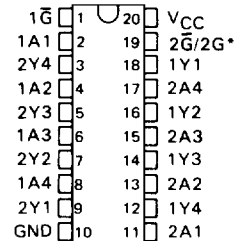
- Open-Collector Outputs Drive Bus Lines or Buffer Memory Address Registers
- Eliminates the Need for 3-State Overlap Protection
- P-N-P Inputs Reduce DC Loading
- Dependable Texas Instruments Quality and Reliability
- Open-Collector Versions of 'AS240, 'AS241

## description

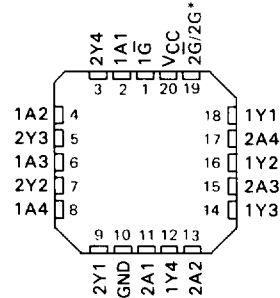
These octal bus transceivers are designed specifically to improve both the performance and density of three-state memory address drivers, clock drivers, and bus-oriented receivers and transmitters by eliminating the need for three-state overlap protection. The designer has a choice of selected combinations of inverting and noninverting outputs, symmetrical  $\bar{G}$  (active-low output control) inputs, and complementary  $G$  and  $\bar{G}$  inputs. These devices feature high fan-out and improved fan-in.

The SN54' family is characterized for operation over the full military temperature range of  $-55^{\circ}\text{C}$  to  $125^{\circ}\text{C}$ . The SN74' family is characterized for operation from  $0^{\circ}\text{C}$  to  $70^{\circ}\text{C}$ .

SN54AS' . . . J PACKAGE  
SN74AS' . . . N PACKAGE  
(TOP VIEW)



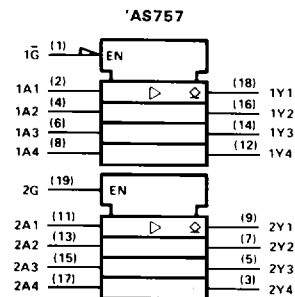
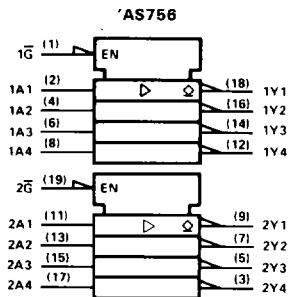
SN54AS' . . . FH PACKAGE  
SN74AS' . . . FN PACKAGE  
(TOP VIEW)



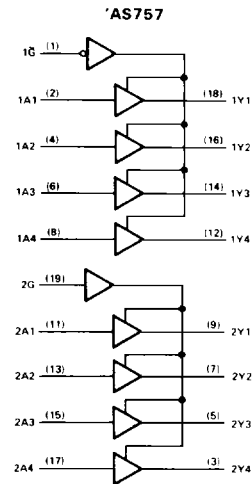
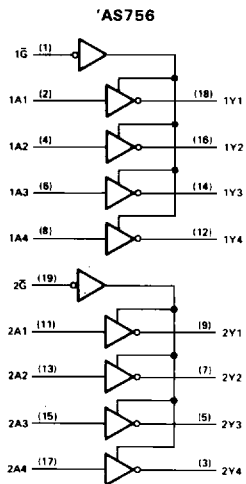
\*2 $\bar{G}$  for 'AS756 or 2G for 'AS757

**TYPES SN54AS756, SN54AS757, SN74AS756, SN74AS757**  
**OCTAL BUFFERS AND LINE DRIVERS WITH OPEN-COLLECTOR OUTPUTS**

**logic symbols**



**logic diagrams (positive logic)**



Pin numbers shown are for J and N packages.

# TYPES SN54AS756, SN54AS757, SN74AS756, SN74AS757 OCTAL BUFFERS AND LINE DRIVERS WITH OPEN-COLLECTOR OUTPUTS

## absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, $V_{CC}$ .....	7 V
Input voltage .....	7 V
Off-state output voltage .....	7 V
Operating free-air temperature range: SN54AS756, SN54AS757 .....	-55 °C to 125 °C
SN74AS756, SN74AS757 .....	0 °C to 70 °C
Storage temperature range .....	-65 °C to 150 °C

## recommended operating conditions

		SN54AS756 SN54AS757			SN74AS756 SN74AS757			UNIT		
		MIN	NOM	MAX	MIN	NOM	MAX			
$V_{CC}$	Supply voltage	4.5	5	5.5	4.5	5	5.5	V		
$V_{IH}$	High-level input voltage	2			2			V		
$V_{IL}$	Low-level input voltage	0.8			0.8			V		
$V_{OH}$	High-level output voltage	5.5			5.5			V		
$I_{OL}$	Low-level output current	48			64			mA		
$T_A$	Operating free-air temperature	-55			125			0	70	°C

## electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS	SN54AS756 SN54AS757			SN74AS756 SN74AS757			UNIT
		MIN	TYP†	MAX	MIN	TYP†	MAX	
$V_{IK}$	$V_{CC} = 4.5$ V, $I_I = -18$ mA	-1.2			-1.2			V
$I_{OH}$	$V_{CC} = 4.5$ V, $V_{OH} = 5.5$ V	0.1			0.1			mA
$V_{OL}$	$V_{CC} = 4.5$ V, $I_{OL} = 48$ mA	0.55						V
	$V_{CC} = 4.5$ V, $I_{OL} = 64$ mA				0.55			
$I_I$	$V_{CC} = 5.5$ V, $V_I = 7$ V	0.1			0.1			mA
$I_{IH}$	$V_{CC} = 5.5$ V, $V_I = 2.7$ V	20			20			μA
$I_{IL}$	$V_{CC} = 5.5$ V, $V_I = 0.4$ V	-0.3			-0.3			mA
$I_{CC}$	$V_{CC} = 5.5$ V		Outputs high	16	16			mA
			Outputs low	51	51			
			Outputs high	27	27			
			Outputs low	61	61			

† All typical values are at  $V_{CC} = 5$  V,  $T_A = 25$  °C

Additional information on these products can be obtained from the factory as it becomes available.

2  
ALS AND AS CIRCUITS

**TYPES SN54AS756, SN54AS757, SN74AS756, SN74AS757**  
**OCTAL BUFFERS AND LINE DRIVERS WITH OPEN-COLLECTOR OUTPUTS**

**'AS756 switching characteristics (see Note 1)**

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V <sub>CC</sub> = 4.5 V to 5.5 V, C <sub>L</sub> = 50 pF, R <sub>L</sub> = 500 Ω, T <sub>A</sub> = MIN to MAX						UNIT
			SN54AS756			SN74AS756			
			MIN	TYP†	MAX	MIN	TYP†	MAX	
t <sub>PLH</sub>	A	Y	20			20			ns
t <sub>PHL</sub>			6			6			
t <sub>PLH</sub>	Ḡ	Y	22			22			ns
t <sub>PHL</sub>			8			8			

**'AS757 switching characteristics (see Note 1)**

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V <sub>CC</sub> = 4.5 V to 5.5 V, C <sub>L</sub> = 50 pF, R <sub>L</sub> = 500 Ω, T <sub>A</sub> = MIN to MAX						UNIT
			SN54AS757			SN74AS757			
			MIN	TYP†	MAX	MIN	TYP†	MAX	
t <sub>PLH</sub>	A	Y	20			20			ns
t <sub>PHL</sub>			6			6			
t <sub>PLH</sub>	1Ḡ	Y	22			22			ns
t <sub>PHL</sub>			8			8			
t <sub>PLH</sub>	2G	Y	23			23			ns
t <sub>PHL</sub>			9			9			

† All typical values are at V<sub>CC</sub> = 5 V, T<sub>A</sub> = 25°C.

NOTE 1: For load circuit and voltage waveforms, see page 1-12.

Additional information on these products can be obtained from the factory as it becomes available.

**2 ALS AND AS CIRCUITS**