# TOSHIBA



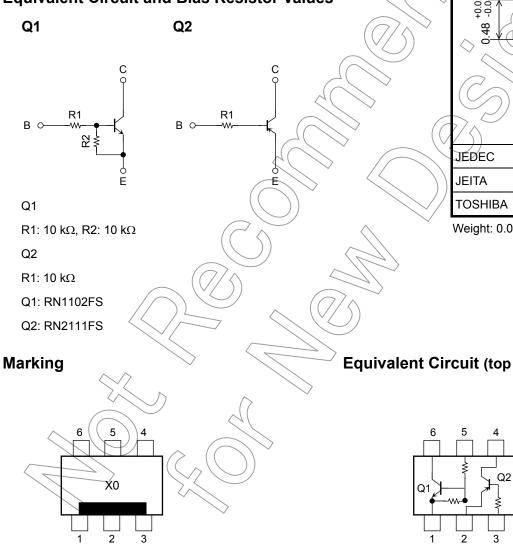
TOSHIBA Transistor Silicon NPN · PNP Epitaxial Type (PCT Process) (Transistor with Built-in Bias Resistor)

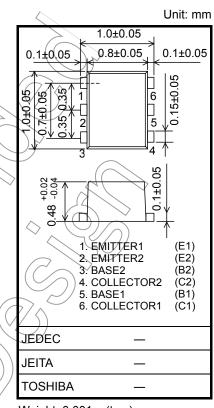
# RN49P1FS

Switching, Inverter Circuit, Interface Circuit and Driver **Circuit Applications** 

- Two devices are incorporated into a fine-pitch, Small-Mold (6-pin) • package.
- Incorporating a bias resistor into a transistor reduces the parts count. Reducing the parts count enables the manufacture of ever more compact equipment and saves assembly costs.

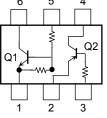
#### **Equivalent Circuit and Bias Resistor Values**





Weight: 0.001 g (typ.)

#### Equivalent Circuit (top view)



#### Absolute Maximum Ratings (Ta = 25°C) (Q1)

Characteristic	Symbol	Rating	Unit
Collector-base voltage	V <sub>CBO</sub>	20	V
Collector-emitter voltage	V <sub>CEO</sub>	20	V
Emitter-base voltage	V <sub>EBO</sub>	10	V
Collector current	ΙC	50	mA

#### Absolute Maximum Ratings (Ta = 25°C) (Q2)

Characteristic	Symbol	Rating	Unit
Collector-base voltage	V <sub>CBO</sub>	-20	V
Collector-emitter voltage	V <sub>CEO</sub>	-20	V
Emitter-base voltage	V <sub>EBO</sub>	-5	K.
Collector current	Ι <sub>C</sub>	-50	mA

#### Absolute Maximum Ratings (Ta = 25°C) (Q1, Q2 common)

Characteristic	Symbol	Rating	V Unit
Collector power dissipation	P <sub>C</sub> (Note 1)	50	mW
Junction temperature	Tj 🔶	150	°C
Storage temperature range	T <sub>stg</sub>	-55~150	∠ (°c

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Total rating

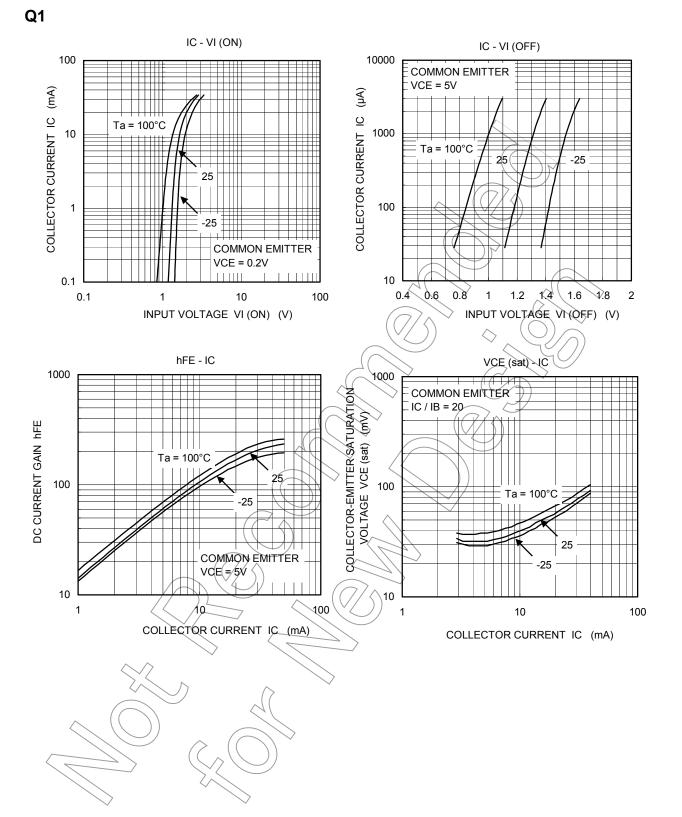
#### Electrical Characteristics (Ta = 25°C) (Q1)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cutoff current	I <sub>CBO</sub>	$V_{CB} = 20 V, I_E = 0$		_	100	nA
	ICEO	$V_{CE} = 20 \text{ V}, \text{ I}_{B} = 0$	_	_	500	117
Emitter cutoff current	I <sub>EBO</sub>	$V_{EB} = 10 \text{ V}, \text{ I}_{C} = 0$	0.41	_	0.63	mA
DC current gain	h <sub>FE</sub>	$V_{CE} = 5 \text{ V}, \text{ I}_{C} = 10 \text{ mA}$	60		_	
Collector-emitter saturation voltage	V <sub>CE (sat)</sub>	I <sub>C</sub> = 5 mA, I <sub>B</sub> = 0.25 mA	Æ	-7(	0.15	V
Input voltage (ON)	V <sub>I (ON)</sub>	$V_{CE} = 0.2 \text{ V}, I_{C} = 5 \text{ mA}$	1.0	_	2.2	V
Input voltage (OFF)	V <sub>I (OFF)</sub>	$V_{CE} = 5 V, I_C = 0.1 mA$	0.8	_	1.5	V
Collector output capacitance	C <sub>ob</sub>	$V_{CB} = 10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		1.2	_	pF
Input resistor	R1	— ())*	8	10	12	kΩ
Resistor ratio	R1/R2		0.8	1.0	1.2	

### Electrical Characteristics (Ta = 25°C) (Q2)

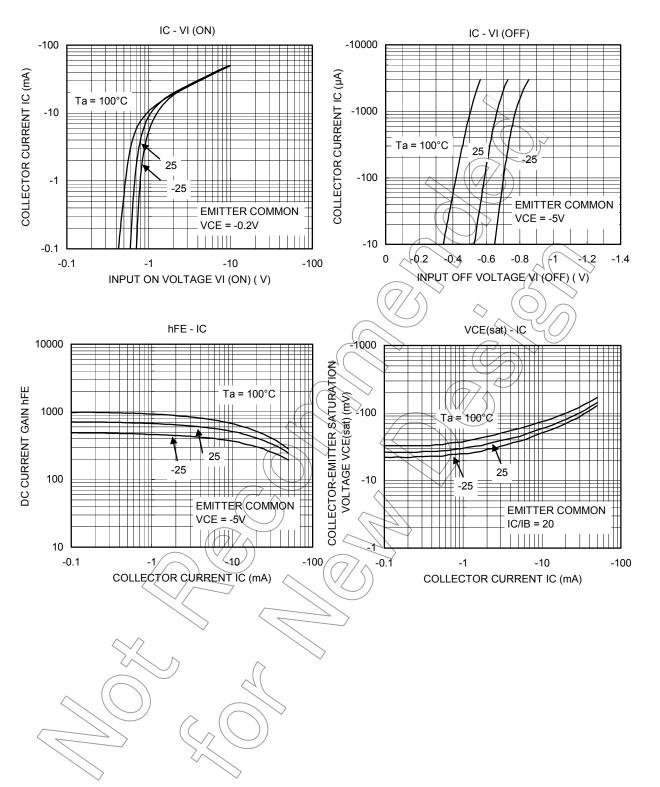
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cutoff current	I <sub>CBO</sub>	$V_{CB} = -20 V, I_E = 0$		$^{>}-$	-100	nA
Emitter cutoff current	I <sub>EBO</sub>	$V_{EB} = -5 V, I_{C} = 0$	7)	—	-100	mA
DC current gain	h <sub>FE</sub>	$V_{CE} = -5 V$ , $I_{C} = -1 \text{ mA}$	300	—	_	
Collector-emitter saturation voltage	V <sub>CE (sat)</sub>	$I_{C} = -5 \text{ mA}, I_{B} = -0.25 \text{ mA}$	/ _	—	-0.15	V
Collector output capacitance	C <sub>ob</sub>	V <sub>CB</sub> = -10 V, <i>I</i> <sub>E</sub> = 0, f = 1 MHz	_	1.2	_	pF
Input resistor	R1		8	10	12	kΩ

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Q2



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