

CD54HC541/3A CD54HCT541/3A

Switching Speed (Limits with black dots (•) are tested 100%.)

SWITCHING CHARACTERISTICS ($C_L = 50$ pF, Input $t_r, t_f = 6$ ns)

CHARACTERISTIC	SYMBOL	V_{CC} V	25°C				-55°C to +125°C				UNITS
			HC		HCT		54HC		54HCT		
			Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	
Propagation Delay Data to Outputs	t_{PLH}	2	—	115	—	—	—	175	—	—	ns
	t_{PHL}	4.5	—	23•	—	28•	—	35•	—	42•	
		6	—	20	—	—	—	30	—	—	
Propagation Delay Output Enable and Disable to Outputs	t_{PZH}, t_{PZL}	2	—	160	—	—	—	240	—	—	
	t_{PHZ}, t_{PLZ}	4.5	—	32•	—	35•	—	48•	—	53•	
		6	—	27	—	—	—	41	—	—	
Output Transition Time	t_{TLH}	2	—	60	—	—	—	90	—	—	
	t_{THL}	4.5	—	12	—	12	—	18	—	18	
		6	—	10	—	—	—	15	—	—	
Input Capacitance	C_I	—	—	10	—	10	—	10	—	10	
3-State Output Capacitance	C_O	—	—	20	—	20	—	20	—	20	

Burn-In Test-Circuit Connections (Use Static II for /3A burn-in and Dynamic for Life Test.)

Static	STATIC BURN-IN I			STATIC BURN-IN II		
	OPEN	GROUND	V_{CC} (6V)	OPEN	GROUND	V_{CC} (6V)
CD54HC/HCT541	11-18	1-10,19	20	11-18	10	1-9,19,20
Dynamic	OPEN	GROUND	$1/2 V_{CC}$ (3V)	V_{CC} (6V)	OSCILLATOR	
	—	10	11-18	20	50 kHz	25 kHz
CD54HC/HCT541	—	10	11-18	20	1,19	2-9

NOTE: Each pin except V_{CC} and Gnd will have a resistor of 2k-47k ohms.

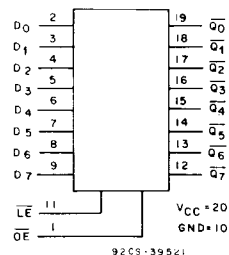
CD54HC563/3A CD54HCT563/3A

Octal Transparent Latch, 3-State, Inverting

The RCA CD54HC/HCT563 are high-speed Octal Transparent Latches manufactured with silicon gate CMOS technology. They possess the low power consumption of standard CMOS integrated circuits, as well as the ability to drive 15 LSTTL devices.

The outputs are transparent to the inputs when the latch enable (\overline{LE}) is high. When the latch enable (\overline{LE}) goes low the data is latched. The output enable (\overline{OE}) controls the three-state outputs. When the output enable (\overline{OE}) is high the outputs will be in the high impedance state. The latch operation is independent of the state of the output enable.

The CD54HC533 and CD54HCT533 are identical in function to the CD54HC563 and CD54HCT563 but have different pinouts. The CD54HC533 and CD54HCT533 are similar to the CD54HC373 and CD54HCT373; the latter are non-inverting types.



FUNCTIONAL DIAGRAM

Package Specifications

See Section 11, Fig. 13

CD54HC563/3A

CD54HCT563/3A

Static Electrical Characteristics (Limits with black dots (•) are tested 100%) — Bus Type

CHARACTERISTICS		TEST CONDITIONS								UNITS	
		HC/HCT				V _{IN}		LIMITS			
						V _{DD}	V _O				I _O
Output High (Source) Current I _{OH} Min. - TTL Load	25°C	4.5	3.98	—	—	0, 4.5	0, 4.5	-6•	—	mA	
	-55°C	4.5	3.70	—	—	0, 4.5	0, 4.5	-6•	—		
	+125°C	4.5	0.26	—	—	0, 4.5	0, 4.5	6•	—		
Output Low (Sink) Current I _{OL} Min. - TTL Load	25°C	4.5	0.26	—	—	0, 4.5	0, 4.5	6•	—		
	-55°C	4.5	0.40	—	—	0, 4.5	0, 4.5	6•	—		
	+125°C	4.5	—	-6	—	1.35, 3.15	0.8, 2.0	3.98•	—		
High Level Output Voltage V _{OH} - TTL Load	25°C	4.5	—	-6	—	1.35, 3.15	0.8, 2.0	3.70•	—	V	
	-55°C	4.5	—	6	—	1.35, 3.15	0.8, 2.0	—	0.26•		
	+125°C	4.5	—	6	—	1.35, 3.15	0.8, 2.0	—	0.40•		
Low Level Output Voltage V _{OL} - TTL Load	25°C	6	—	—	6, 0	—	—	—	8•		μA
	-55°C	6	—	—	6, 0	—	—	—	160•		
	+125°C	6	—	—	6, 0	—	—	—	160•		

The complete static electrical test specification consists of the above by-type static tests combined with the standard static tests in the beginning of this section.

HCT INPUT LOADING TABLE

INPUT	UNIT LOAD*
D0 - D7	0.15
\overline{LE}	0.30
\overline{OE}	0.55

Switching Speed (Limits with black dots (•) are tested 100%)

SWITCHING CHARACTERISTICS (C_L = 50 pF, Input t_r, t_f = 6 ns)

*Unit load is Δ l_{cc} limit specified in Static Characteristics Chart, e.g., 360 μA max. @ 25°C.

CHARACTERISTIC	SYMBOL	TEST CONDITIONS V _{CC} V	LIMITS								UNITS
			25°C				-55°C to +125°C				
			HC		HCT		54HC		54HCT		
Propagation Delay Data to Qn	t _{PLH} t _{PHL}	2	—	150	—	—	—	225	—	—	ns
		4.5	—	30•	—	30•	—	45•	—	45•	
		6	—	26	—	—	—	38	—	—	
\overline{LE} to Qn	t _{PZH} , t _{PZL} t _{PHZ} , t _{PLZ}	2	—	165	—	—	—	250	—	—	
		4.5	—	33•	—	35•	—	50•	—	53•	
		6	—	28	—	—	—	43	—	—	
Enable and Disable Times	t _{PZH} , t _{PZL} t _{PHZ} , t _{PLZ}	2	—	150	—	—	—	225	—	—	
		4.5	—	30•	—	35•	—	45•	—	53•	
		6	—	26	—	—	—	38	—	—	
Input Capacitance	C _I	—	—	10	—	10	—	10	—	10	pF
3-State Output Capacitance	C _O	—	—	20	—	20	—	20	—	20	

Burn-In Test-Circuit Connections (Use Static II for /3A burn-in and Dynamic for Life Test.)

Static	STATIC BURN-IN I			STATIC BURN-IN II		
	OPEN	GROUND	V _{CC} (6V)	OPEN	GROUND	V _{CC} (6V)
CD54HC/HCT563	12-19	1-11	20	12-19	10	1-9,11,20
Dynamic	OPEN	GROUND	1/2 V _{CC} (3V)	V _{CC} (6V)	OSCILLATOR	
	—	1,10	12-19	20	50 kHz	25 kHz
CD54HC/HCT563	—	1,10	12-19	20	11	2-9

NOTE: Each pin except V_{CC} and Gnd will have a resistor of 2k-47k ohms.