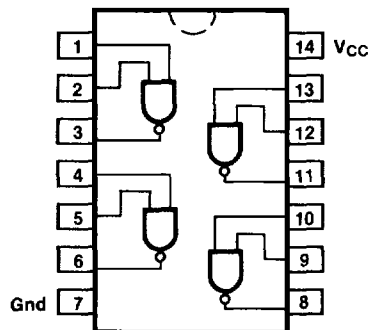


54F/74F38

Quad 2-Input NAND Buffer
(Open Collector)

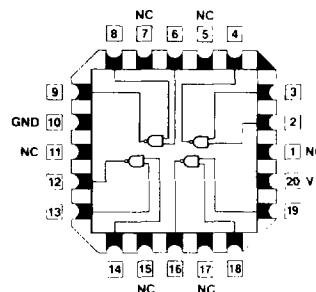
PRELIMINARY

Connection Diagrams



**Pin Assignment
for DIP and SOIC**

4



**Pin Assignment
for LCC and PCC**

Ordering Code: See Section 5

Input Loading/Fan-Out: See Section 3 for U.L. definitions

Pin Names	Description	54F/74F(U.L.) HIGH/LOW
A,B	Inputs	0.5/0.375
O	Output	OC*/12.5

*OC = Open Collector

Function Table

Inputs		Output
A	B	O
L	L	H
L	H	H
H	L	H
H	H	L

H = HIGH Voltage Level
L = LOW Voltage Level

DC Characteristics over Operating Temperature Range (unless otherwise specified)

Symbol	Parameter	54F/74F			Units	Conditions	
		Min	Typ	Max			
I_{CCH}	Power Supply Current			22	mA	$V_{CC} = \text{Max}$	$V_{IN} = \text{Gnd}$
I_{CCL}				22		$V_{IN} = \text{HIGH}$	

AC Characteristics: See Section 3 for waveforms and load configurations

Symbol	Parameter	54F/74F			54F		74F		Units	Fig. No.
		$T_A = +25^\circ\text{C}$ $V_{CC} = +5.0\text{ V}$ $C_L = 50\text{ pF}$			$T_A, V_{CC} = \text{Mil}$ $C_L = 50\text{ pF}$		$T_A, V_{CC} = \text{Com}$ $C_L = 50\text{ pF}$			
		Min	Typ	Max	Min	Max	Min	Max		
t_{PLH} t_{PHL}	Propagation Delay			9.0 6.0				ns	3-1 3-3	