

SILICON PLANAR EPITAXIAL TRANSISTORS

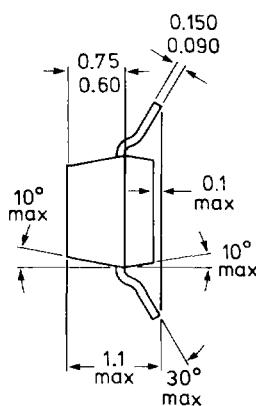
P-N-P transistors, in a SOT-23 plastic package.

QUICK REFERENCE DATA

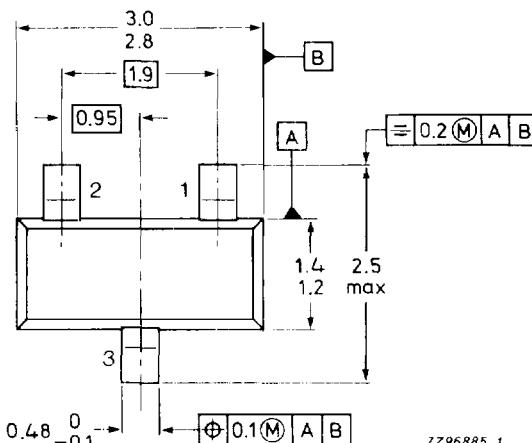
		BC856	BC857	BC858
Collector-emitter voltage ($+V_{BE} = 1$ V)	$-V_{CEX}$	max. 80	50	30 V
Collector-emitter voltage (open base)	$-V_{CEO}$	max. 65	45	30 V
Collector current (peak value)	$-I_{CM}$	max.	200	mA
Total power dissipation up to $T_{amb} = 60$ °C	P_{tot}	max.	250	mW
Junction temperature	T_j	max.	150	°C
DC current gain $-I_C = 2$ mA; $-V_{CE} = 5$ V	h_{fe}		75 to 800	
Transition frequency at $f = 100$ MHz $-I_C = 10$ mA; $-V_{CE} = 5$ V	f_T	>	100	MHz
Noise figure at $R_S = 2$ kΩ $-I_C = 200$ μA; $-V_{CE} = 5$ V $f = 1$ kHz; $B = 200$ Hz	F	<	10	dB

MECHANICAL DATA

Fig. 1 SOT-23.



Dimensions in mm

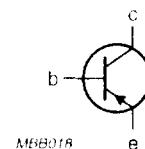


TOP VIEW

Reverse pinning types are available on request.

Marking code:
 BC856 = 3Dp
 BC856A = 3Ap
 BC856B = 3Bp
 BC857 = 3Hp
 BC857A = 3Ep
 BC857B = 3Fp
 BC857C = 3Gp
 BC858 = 3Mp
 BC858A = 3Jp
 BC858B = 3Kp
 BC858C = 3Lp

Pinning:
 1 = base
 2 = emitter
 3 = collector



RATINGS

Limiting values in accordance with the Absolute Maximum System (IEC 134)

		BC856	BC857	BC858
Collector-base voltage (open emitter)	-V _{CBO}	max. 80	50	30 V
Collector-emitter voltage ($+V_{BE} = 1\text{ V}$)	-V _{CEX}	max. 80	50	30 V
Collector-emitter voltage (open base)	-V _{CEO}	max. 65	45	30 V
Emitter-base voltage (open collector)	-V _{EBO}	max. 5	5	5 V
Collector current (d.c.)	-I _C	max.	100	mA
Collector current (peak value)	-I _{CM}	max.	200	mA
Emitter current (peak value)	I _{EM}	max.	200	mA
Base current (peak value)	-I _{BM}	max.	200	mA
Total power dissipation *	P _{tot}	max.	250	mW
up to $T_{amb} = 25\text{ }^{\circ}\text{C}$				
Storage temperature	T _{stg}		-65 to +150	°C
Junction temperature	T _j	max.	150	°C

THERMAL CHARACTERISTICS

Thermal resistance

From junction to ambient	R _{thj,a}	=	500	K/W
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CHARACTERISTICS

T_j = 25 °C unless otherwise specified

Collector cut-off current

I _E = 0; -V _{CB} = 30 V; T _j = 25 °C	-I _{CBO}	typ. <	1 15	nA nA
T _j = 150 °C	-I _{CBO}	<	4	μA

Base-emitter voltage ▲

-I _C = 2 mA; -V _{CE} = 5 V	-V _{BE}	typ.	650 600 to 750	mV mV
-I _C = 10 mA; -V _{CE} = 5 V	-V _{BE}	<	820	mV

▲ -V_{BE} decreases by about 2 mV/K with increasing temperature.

* Mounted on an FR4 printed-circuit board 8 mm x 10 mm x 0.7 mm.

Saturation voltages * $-I_C = 10 \text{ mA}; -I_B = 0,5 \text{ mA}$ $-V_{CEsat}$ typ. 75 mV
 $<$ 300 mV $-I_C = 100 \text{ mA}; -I_B = 5 \text{ mA}$ $-V_{BEsat}$ typ. 700 mV $-V_{CEsat}$ typ. 250 mV
 $<$ 650 mV $-V_{BEsat}$ typ. 850 mV **Knee voltage**Collector capacitance at $f = 1 \text{ MHz}$ $I_E = I_e = 0; -V_{CB} = 10 \text{ V}$ C_C typ. $4,5 \text{ pF}$ Transition frequency at $f = 100 \text{ MHz}$ $-I_C = 10 \text{ mA}; -V_{CE} = 5 \text{ V}$ f_T > 100 MHz Small-signal current gain at $f = 1 \text{ kHz}$ $-I_C = 2 \text{ mA}; -V_{CE} = 5 \text{ V}$ h_{fe} 125 to 800Noise figure at $R_S = 2 \text{ k}\Omega$ $-I_C = 200 \mu\text{A}; -V_{CE} = 5 \text{ V}$ F typ. 2 dB
 $<$ 10 dB $f = 1 \text{ kHz}; B = 200 \text{ Hz}$ **D.C. current gain** $-I_C = 2 \text{ mA}; -V_{CE} = 5 \text{ V}$ BC856/857

BC858

 h_{FE} 125 to 800

BC856A/857A/858A

 h_{FE} 125 to 250

BC856B/857B/858B

 h_{FE} 220 to 475

BC857C/858C

 h_{FE} 420 to 800* $-V_{BEsat}$ decreases by about $1,7 \text{ mV/K}$ with increasing temperature.