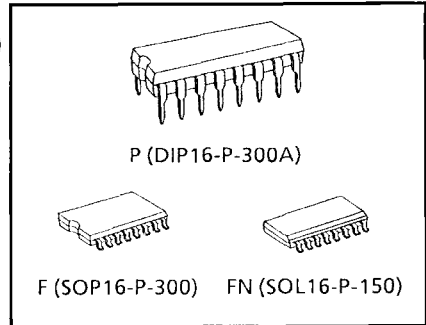


TC4049BP/BF/BFN TC4050BP/BF/BFN

TC4049B HEX BUFFER/CONVERTER(Inverting Type)
TC4050B HEX BUFFER/CONVERTER(Non-Inverting Type)

TC4049B, TC4050B contain six circuits of buffers. TC4049B is inverter type and TC4050B is non-inverter type. Since one TTL or DTL can be directly driven having large output current, these are useful for interfacing from CMOS to TTL or DTL. As voltage up to $V_{SS} + 18$ volts can be applied to the input regardless of V_{DD} , these can be also used as the level converter IC's which converts CMOS logical circuits of 15 volts or 10 volts system to CMOS/TTL logical circuits of 5 volts system. Ideal switching characteristic has been obtained by the circuit diagram of three stage inverters for TC4049B and two stage inverters for TC4050B.

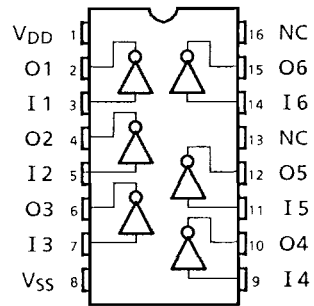


ABSOLUTE MAXIMUM RATINGS

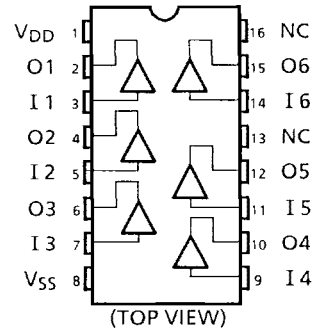
CHARACTERISTIC	SYMBOL	RATING	UNIT
DC Supply Voltage	V_{DD}	$V_{SS} - 0.5 \sim V_{SS} + 20$	V
Input Voltage	V_{IN}	$V_{SS} - 0.5 \sim V_{SS} + 20$	V
Output Voltage	V_{OUT}	$V_{SS} - 0.5 \sim V_{DD} + 0.5$	V
DC Input Current	I_{IN}	± 10	mA
Power Dissipation	P_D	300 (DIP) / 180 (SOIC)	mW
Operating Temperature Range	T_A	$-40 \sim 85$	$^{\circ}C$
Storage Temperature Range	T_{STG}	$-65 \sim 150$	$^{\circ}C$
Lead Temp./Time	T_{SOL}	$260^{\circ}C \cdot 10sec$	

PIN ASSIGNMENT

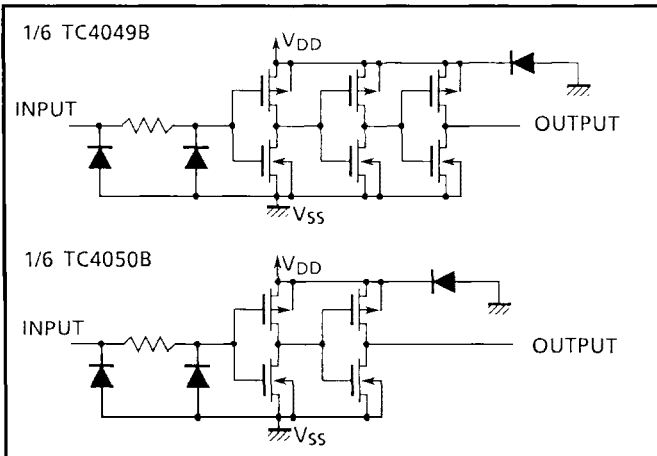
TC4049B



TC4050B



CIRCUIT DIAGRAM



TC4049BP/BF/BFN

TC4050BP/BF/BFN

RECOMMENDED OPERATING CONDITIONS ($V_{SS} = 0V$)

CHARACTERISTICS	SYMBOL	MIN.	TYP.	MAX.	UNITS
DC Supply Voltage	V_{DD}	3	–	18	V
Input Voltage	V_{IN}	0	–	18	V

STATIC ELECTRICAL CHARACTERISTICS ($V_{SS} = 0V$)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	V_{DD} (V)	–40°C		25°C			85°C		UNITS	
				MIN.	MAX.	MIN.	TYP.	MAX.	MIN.	MAX.		
High-Level Output Voltage	V_{OH}	$ I_{OUT} < 1\mu A$ $V_{IN} = V_{SS}, V_{DD}$	5	4.95	–	4.95	5.00	–	4.95	–	V	
			10	9.95	–	9.95	10.00	–	9.95	–		
			15	14.95	–	14.95	15.00	–	14.95	–		
Low-Level Output Voltage	V_{OL}	$ I_{OUT} < 1\mu A$ $V_{IN} = V_{SS}, V_{DD}$	5	–	0.05	–	0.00	0.05	–	0.05	V	
			10	–	0.05	–	0.00	0.05	–	0.05		
			15	–	0.05	–	0.00	0.05	–	0.05		
Output High Current	I_{OH}	$V_{OH} = 4.6V$ $V_{OH} = 2.5V$ $V_{OH} = 9.5V$ $V_{OH} = 13.5V$ $V_{IN} = V_{SS}, V_{DD}$	5	–0.73	–	–0.65	–1.2	–	–0.58	–	mA	
			5	–2.4	–	–2.1	–3.9	–	–1.9	–		
			10	–1.8	–	–1.65	–2.5	–	–1.35	–		
			15	–4.8	–	–4.3	–8.0	–	–3.5	–		
Output Low Current	I_{OL}	$V_{OL} = 0.4V$ $V_{OL} = 0.5V$ $V_{OL} = 1.5V$ $V_{IN} = V_{SS}, V_{DD}$	5	3.8	–	3.2	6.4	–	2.9	–	mA	
			10	9.6	–	8.0	16	–	6.6	–		
			15	25.0	–	24.0	48	–	20.0	–		
Input High Voltage	V_{IH}	$V_{OUT} = 0.5V, 4.5V$ $V_{OUT} = 1.0V, 9.0V$ $V_{OUT} = 1.5V, 13.5V$ $ I_{OUT} < 1\mu A$	5	3.5	–	3.5	2.75	–	3.5	–	V	
			10	7.0	–	7.0	5.5	–	7.0	–		
			15	11.0	–	11.0	8.25	–	11.0	–		
Input Low Voltage	V_{IL}	$V_{OUT} = 0.5V, 4.5V$ $V_{OUT} = 1.0V, 9.0V$ $V_{OUT} = 1.5V, 13.5V$ $ I_{OUT} < 1\mu A$	5	–	1.5	–	2.25	1.5	–	1.5	V	
			10	–	3.0	–	4.5	3.0	–	3.0		
			15	–	4.0	–	6.75	4.0	–	4.0		
Input Current	"H" Level	I_{IH}	$V_{IH} = 18V$	18	–	0.1	–	10^{-5}	0.1	–	1.0	μA
	"L" Level	I_{IL}	$V_{IL} = 0V$	18	–	–0.1	–	-10^{-5}	–0.1	–	–1.0	
Quiescent Device Current	I_{DD}	$V_{IN} = V_{SS}, V_{DD}^*$	5	–	1	–	0.002	1	–	30	μA	
			10	–	2	–	0.004	2	–	60		
			15	–	4	–	0.008	4	–	120		

* All valid input combinations.

DYNAMIC ELECTRICAL CHARACTERISTICS (Ta = 25°C, VSS = 0V, CL = 50pF)

CHARACTERISTICS		SYMBOL	TEST CONDITION	VDD (V)	MIN.	TYP.	MAX.	UNITS				
Output Transition Time (Low to High)	t _{TLH}			5	–	60	160	ns				
				10	–	30	80					
				15	–	25	60					
Output Transition Time (High to Low)	t _{THL}			5	–	120	60					
				10	–	10	40					
				15	–	8	30					
TC4049B	Propagation Delay Time (Low to High)	t _{pLH}										
										5	–	60
TC4049B	Propagation Delay Time (High to Low)	t _{pHL}										
										5	–	35
TC4050B	Propagation Delay Time (Low to High)	t _{pLH}										
									10	–	30	70
TC4050B	Propagation Delay Time (High to Low)	t _{pHL}										
									15	–	25	55
TC4050B	Propagation Delay Time (Low to High)	t _{pLH}										
									5	–	50	130
									10	–	30	70
TC4050B	Propagation Delay Time (High to Low)	t _{pHL}										
									5	–	30	70
									10	–	17	35
TC4050B	Propagation Delay Time (High to Low)	t _{pHL}										
									15	–	14	25
									15	–	14	25
Input Capacitance		C _{IN}			–	5	7.5	pF				

WAVEFORMS FOR MEASUREMENT OF DYNAMIC CHARACTERISTICS

