

**SN54ALS35A, SN74ALS35A
HEX NONINVERTERS WITH OPEN-COLLECTOR OUTPUTS**

D2861, DECEMBER 1983—REVISED MAY 1986

- Noninverters with Open-Collector Outputs
- Package Options Include Plastic "Small Outline" Packages, Ceramic Chip Carriers, and Standard Plastic and Ceramic 300-mil DIPs
- Dependable Texas Instruments Quality and Reliability

description

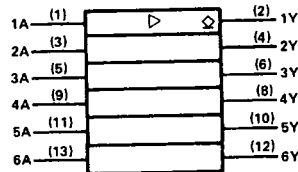
These devices contain six independent noninverters. They perform the Boolean functions $Y = A$. The open-collector outputs require pull-up resistors to perform correctly. They may be connected to other open-collector outputs to implement active-low wired-OR or active-high wired-AND functions. Open-collector devices are often used to generate higher V_{OH} levels.

The SN54ALS35A is characterized for operation over the full military temperature range of -55°C to 125°C . The SN74ALS35A is characterized for operation from 0°C to 70°C .

FUNCTION TABLE (each buffer)

| INPUT | OUTPUT |
|-------|--------|
| A | Y |
| H | H |
| L | L |

logic symbol†

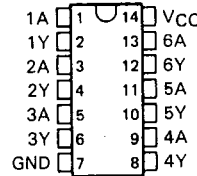


† This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12. Pin numbers shown are for D, J and N packages.

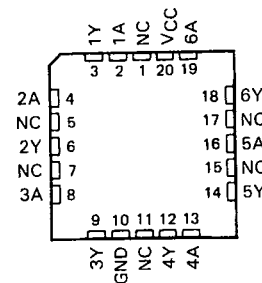
SN54ALS35A . . . J PACKAGE
SN74ALS35A . . . D OR N PACKAGE

(TOP VIEW)

T-43-15

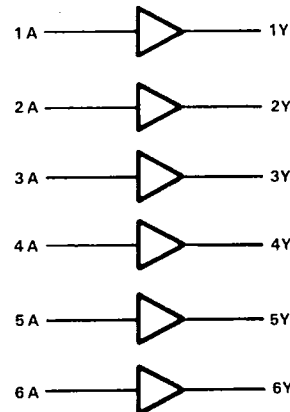


SN54ALS35A . . . FK PACKAGE
(TOP VIEW)



NC—No internal connection

logic diagram (positive logic)



PRODUCTION DATA documents contain information current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.



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**SN54ALS35A, SN74ALS35A
HEX NONINVERTERS WITH OPEN-COLLECTOR OUTPUTS**

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absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

| | |
|--|----------------|
| Supply voltage, VCC | 7 V |
| Input voltage | 7 V |
| Off-state output voltage | 7 V |
| Operating free-air temperature range: SN54ALS35A | -55°C to 125°C |
| SN74ALS35A | 0°C to 70°C |
| Storage temperature range | -65°C to 150°C |

recommended operating conditions

| | | SN54ALS35A | | | SN74ALS35A | | | UNIT | | |
|-----------------|--------------------------------|------------|-----|-----|------------|-----|-----|------|----|----|
| | | MIN | NOM | MAX | MIN | NOM | MAX | | | |
| VCC | Supply voltage | 4.5 | 5 | 5.5 | 4.5 | 5 | 5.5 | V | | |
| V _{IH} | High-level input voltage | 2 | | | 2 | | | V | | |
| V _{IL} | Low-level input voltage | 0.7 | | | 0.8 | | | V | | |
| V _{OH} | High-level output voltage | 5.5 | | | 5.5 | | | V | | |
| I _{OL} | Low-level output current | 4 | | | 8 | | | mA | | |
| T _A | Operating free-air temperature | -55 | | | 125 | | | 0 | 70 | °C |

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ALS and AS Circuits

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

| PARAMETER | TEST CONDITIONS | SN54ALS35A | | SN74ALS35A | | UNIT |
|------------------|--------------------------------------|------------|------|------------|-----|------|
| | | MIN | TYP† | MAX | MIN | |
| V _{IK} | VCC = 4.5 V, I _I = -18 mA | -1.2 | | -1.2 | | V |
| I _{OH} | VCC = 4.5 V, V _{OH} = 5.5 V | 0.1 | | 0.1 | | mA |
| V _{OL} | VCC = 4.5 V, I _{OL} = 4 mA | 0.25 | 0.4 | 0.25 | 0.4 | V |
| | VCC = 4.5 V, I _{OL} = 8 mA | | | 0.35 | 0.5 | |
| I _I | VCC = 5.5 V, V _I = 7 V | 0.1 | | 0.1 | | mA |
| I _{IH} | VCC = 5.5 V, V _I = 2.7 V | 20 | | 20 | | µA |
| I _{IL} | VCC = 5.5 V, V _I = 0.4 V | -0.1 | | -0.1 | | mA |
| I _{CCH} | VCC = 5.5 V, V _I = 4.5 V | 2.7 | 4.7 | 2.7 | 4.7 | mA |
| I _{CCL} | VCC = 5.5 V, V _I = 0 V | 4.1 | 6.3 | 4.1 | 6.3 | mA |

†All typical values are at VCC = 5 V, T_A = 25°C.

switching characteristics (see Note 1)

| PARAMETER | FROM (INPUT) | TO (OUTPUT) | VCC = 5 V, CL = 50 pF, RL = 2 kΩ, TA = 25°C | | VCC = 4.5 V to 5.5 V, CL = 60 pF, RL = 2 kΩ, TA = MIN to MAX | | UNIT | | |
|------------------|--------------|-------------|--|-----|---|-----|------|------------|--|
| | | | ALS35A | | SN54ALS35A | | | SN74ALS35A | |
| | | | TYP | MIN | MAX | MIN | | MAX | |
| t _{PLH} | A | Y | 34 | 20 | 60 | 20 | 50 | ns | |
| t _{PHL} | | | 9 | 2 | 17 | 2 | 14 | ns | |

NOTE 1: Load circuit and voltage waveforms are shown in Section 1.