TARGET SPECIFICATION

PERICOM

PI5B3253

3.3V, Bus Switch Dual 4:1 MUX/DEMUX

Product Features:

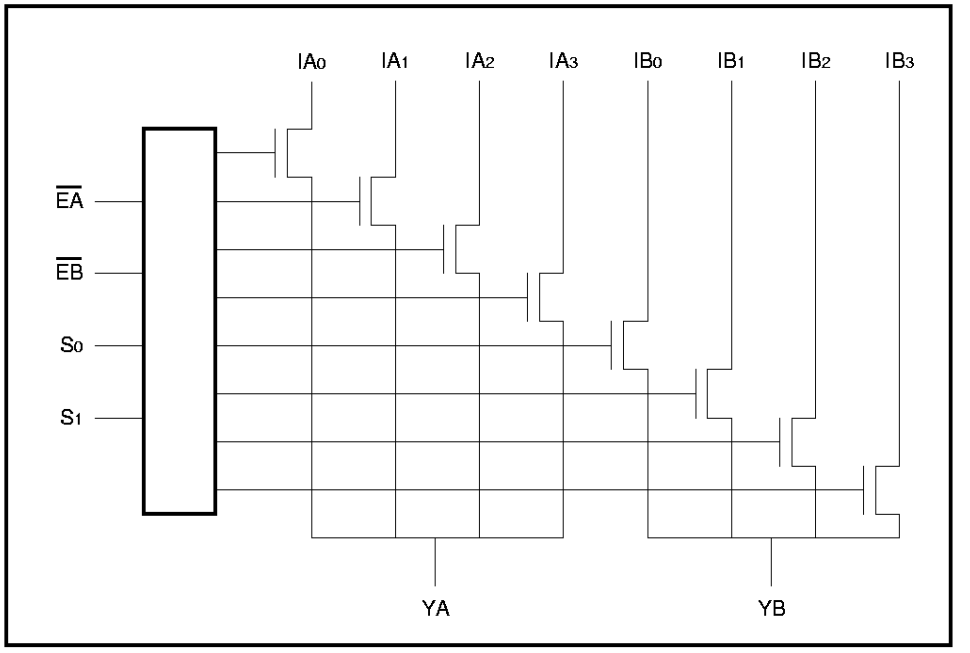
- Near zero propagation delay
- 5Ω switches connect inputs to outputs
- Direct bus connection when switches are ON
- Ultra Low Quiescent Power (0.2 µA Typical) – Ideally suited for notebook applications
- Pin compatible with 74 series 253 logic devices
- Packages available:
  – 16-pin 150 mil wide plastic QSOP (Q16)
  – 16-pin 150 mil wide plastic SOIC (W16)

Product Description:

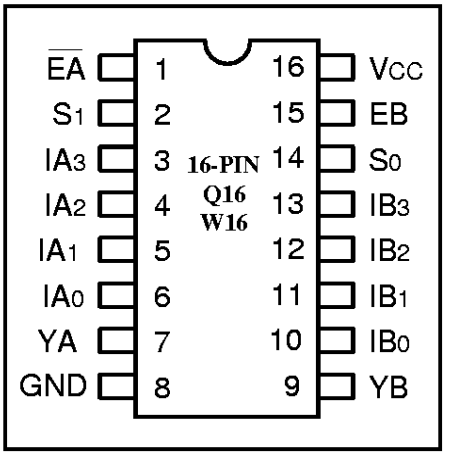
Pericom Semiconductor's PI5B series of logic circuits are produced in the Company's advanced 0.35 micron CMOS technology, achieving industry leading performance.

The PI5B3253 is a 3.3V, Dual 4:1 Multiplexer/demultiplexer with three-state outputs that is pinout compatible with the PI74FCT253T, 74F253, and 74ALS/AS/LS 253. Inputs can be connected to outputs with low on resistance (5Ω) with no additional ground bounce noise or propagation delay.

Logic Block Diagram



Product Pin Configuration



Truth Table[1]

Enable		Select				
$\overline{EA}$	$\overline{EB}$	S1	S0	YA	YB	Function
H	X	X	X	Hi-Z	X	Disable A
X	H	X	X	X	Hi-Z	Disable B
L	L	L	L	IA0	IB0	S1-0 = 0
L	L	L	H	IA1	IB1	S1-0 = 1
L	L	H	L	IA2	IB2	S1-0 = 2
L	L	H	H	IA3	IB3	S1-0 = 3

Note: 1. H = High Voltage Level
L = Low Voltage Level

Product Pin Description

Pin Name	Description
IAn, IBn	Data Inputs
S0–1	Select Inputs
$\overline{EA}$, $\overline{EB}$	Enable
YA, YB	Data Outputs
GND	Ground
Vcc	Power

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Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	–65°C to +150°C
Ambient Temperature with Power Applied	–40°C to +85°C
Supply Voltage to Ground Potential (Inputs & Vcc Only)	–0.5V to +4.6V
Supply Voltage to Ground Potential (Outputs & D/O Only)	–0.5V to +4.6V
DC Input Voltage	–0.5V to +4.6V
DC Output Current	120mA
Power Dissipation	0.5W

Note:
Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over the Operating Range, T_A = –40°C to +85°C, V_{CC} = 3.3V ±5%)

Parameters	Description	Test Conditions[1]	Min.	Typ[2]	Max.	Units
V_{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level	2.0			V
V_{IL}	Input LOW Voltage	Guaranteed Logic LOW Level	–0.5		0.8	V
I_{IH}	Input HIGH Current	V_{CC} = Max., V_{IN} = V_{CC}			±50	µA
I_{IL}	Input LOW Current	V_{CC} = Max., V_{IN} = GND			±50	µA
I_{OZH}	High Impedance Output Current	0 ≤ I_N, Y_N ≤ V_{CC}			±1	µA
V_{IK}	Clamp Diode Voltage	V_{CC} = Min., I_{IN} = –18mA			0.8	V
R_{ON}	Switch On Resistance[4]	V_{CC} = Min., V_{IN} = 0.0V, I_{ON} = 48 mA or 64mA V_{CC} = Min., V_{IN} = 2.4V, I_{ON} = 15mA		5 10	7 15	Ω

Capacitance (T_A = 25°C, f = 1 MHz)

Parameters[5]	Description	Test Conditions	Typ	Units
C_{IN}	Input Capacitance	V_{IN} = 0V	4	pF
C_{OFF}	I_A/I_B Capacitance, Switch Off	V_{IN} = 0V	4	pF
C_{ON}	I_A/I_B Capacitance, Switch On	V_{IN} = 0V	16	pF

Notes:
1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at Vcc = 3.3V, T_A = 25°C ambient and maximum loading.
3. Not more than one output should be shorted at one time. Duration of the test should not exceed one second.
4. Measured by the voltage drop between I and Y pin at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (I,Y) pins.
5. This parameter is determined by device characterization but is not production tested.

Power Supply Characteristics

Parameters	Description	Test Conditions[1]		Min.	Typ[2]	Max.	Units
I_{CC}	Quiescent Power Supply Current	V_{CC} = Max.	V_{IN} = GND or V_{CC}		0.1	3.0	µA
ΔI_{CC}	Supply Current per Input @ TTL HIGH	V_{CC} = Max.	V_{IN} = 3.0V[3]			750	µA

Notes:
1. For conditions shown as Max. or Min., use appropriate value specified under Electrical Characteristics for the applicable device.
2. Typical values are at Vcc = 3.3V, +25°C ambient.
3. Per TTL driven input (V_{IN} = 3.0V, control inputs only); I and Y pins do not contribute to Icc.
4. This current applies to the control inputs only and represent the current required to switch internal capacitance at the specified frequency. The I and Y inputs generate no significant AC or DC currents as they transition. This parameter is not tested, but is guaranteed by design.

Switching Characteristics over Operating Range

Parameters	Description	Conditions[1]	PI5B3253 Com. Min	PI5B3253 Com. Max	Unit
t_{IY}	Propagation Delay[2,3] I to Yn	C_L = 50pF, R_L = 500Ω		0.25	ns
t_{SY}	Bus Select Time Sn to Yn	C_L = 20pF, R_L = 500Ω	1	2	ns
		C_L = 50pF, R_L = 500Ω	1	3.5	
t_{PZH} t_{PZL}	Bus Enable Time $\overline{E}$ to Yn	C_L = 20pF, R_L = 500Ω	1	4	ns
t_{PHZ} t_{PLZ}	Bus Disable Time $\overline{E}$n to Y	C_L = 50pF, R_L = 500Ω	1	4	ns

Notes:
1. See test circuit and wave forms.
2. This parameter is guaranteed but not tested on Propagation Delays.
3. The bus switch contributes no propagational delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25 ns for 50 pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.