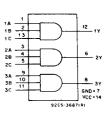
## CD54HC11/3A CD54HCT11/3A

# **Triple 3-Input AND Gate**

The RCA-CD54HC11 and CD54HCT11 logic gates utilize silicon-gate CMOS technology to achieve operating speeds similar to LSTTL gates with the low power consumption of standard CMOS integrated circuits. All devices have the ability to drive 10 LSTTL loads. The 54HCT logic family is functionally as well as pin compatible with the standard 54LS logic family.



### **Package Specifications**

See Section 11, Fig. 10

#### **FUNCTIONAL DIAGRAM**

### Static Electrical Characteristics (Limits with black dots (•) are tested 100%)

			TEST CONDITIONS							
		нс/нст				V <sub>IN</sub>		1		
			HC	HCI		НС	HCT	LIMITS		UNITS
CHARACTERI	STICS	V <sub>DD</sub>	<b>v</b> o	I <sub>o</sub>	V <sub>cc</sub> or GND	V <sub>IL</sub> or V <sub>IH</sub>	V <sub>IL</sub> or V <sub>IH</sub>	MIN. MAX.		
Quiescent	25° C	6	_		6, 0		l -	I	2•	
Device Current	-55°C	6			6.0		_	l _	40•	μΑ
lcc	+125°C	·			0,0				100	

The complete static electrical test specification consists of the above by-type static tests combined with the standard static tests in the beginning of this section.

#### HCT INPUT LOADING TABLE

INPUT	UNIT LOAD*					
All	0.50					

<sup>\*</sup>Unit load is  $\Delta I_{CC}$  limit specified in Static Characteristics Chart, e.g., 360  $\mu$ A max. @ 25° C.

#### Switching Speed (Limits with black dots (•) are tested 100%.)

SWITCHING CHARACTERISTICS (CL = 50 pF, Input t, t, = 6 ns)

CHARACTERISTIC	SYMBOL	V <sub>cc</sub>	25° C				-55°C to +125°C				
			нс		HCT		54HC		54HCT		UNITS
			Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	
Propagation Delay Input to Output	t <sub>PLH</sub> t <sub>PHL</sub>	2	I —	100	_	_	_	150	_		
		4.5		20∙	<u> </u>	28•	l —	30∙	_	42•	
		6	_	17	<b>—</b>	_	l –	26	_	_	
Transition Times		2	_	75	_	_	I —	110	_	_	ns
	t <sub>TLH</sub>	4.5		15	<b>—</b>	15	l —	22	_	22	
	t <sub>THL</sub>	6	_	13	_	-	-	19	_		
Input Capacitance	Cı			10	_	10	_	10	_	10	pF

#### Burn-In Test-Circuit Connections (Use Static II for /3A burn-in and Dynamic for Life Test.)

Ctatio		STATIC BURN-	N I	STATIC BURN-IN II				
Static	OPEN	GROUND	V <sub>cc</sub> (6V)	OPEN	GROUND	V <sub>cc</sub> (6V)		
CD54HC/HCT11	6,8,12	1-5,7,9-11,13	14	6,8,12	7	1-5,9-11,13,14		
Dumamia	OPEN	COCUND	1/0 1/ (010	V (6)0	OSCILLATOR			
Dynamic	OPEN	GROUND	1/2 V <sub>cc</sub> (3V)	V <sub>cc</sub> (6V)	50 kHz	25 kHz		
CD54HC/HCT11	_	7	6,8,12	14	1-5,9-11,13	_		

NOTE: Each pin except Vcc and Gnd will have a resistor of 2k-47k ohms.