



# High Speed CMOS Quad 2 Input Multiplexers

QS54/74FCT157T  
QS54/74FCT158T  
QS54/74FCT257T  
QS54/74FCT258T\*

QS54/74FCT2157T  
QS54/74FCT2158T  
QS54/74FCT2257T  
QS54/74FCT2258T\*

## FEATURES/BENEFITS

- Pin and function compatible to the 74F1/257/8 74FCT157/8 and 74FCT257/8
- CMOS power levels: <7.5 mW static
- Available in DIP, SOIC, QSOP, ZIP, HQSOP
- Undershoot clamp diodes on all inputs
- TTL-compatible input and output levels
- Ground bounce controlled outputs
- Reduced output swing of 0-3.5V
- Military product compliant to MIL-STD-883



### FCT-T 157T, 158T, 257T, 258T

- JEDEC-FCT spec compatible
- Fastest CMOS logic family available
- Std, A and C speed grades with 4.3 ns for C
- $I_{OL} = 48$  mA Com., 32 mA Mil.

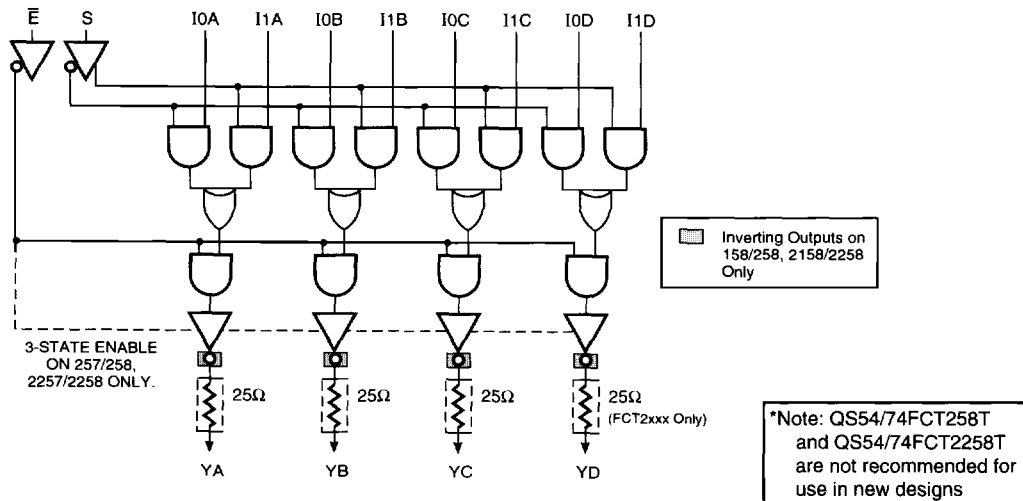
### FCT-T 2157T, 2158T, 2257T, 2258T

- Built-in  $25\Omega$  series resistor outputs reduce reflection and other system noise
- Std, A and C speed grades with 4.3 ns for C
- $I_{OL} = 12$  mA Com.

## DESCRIPTION

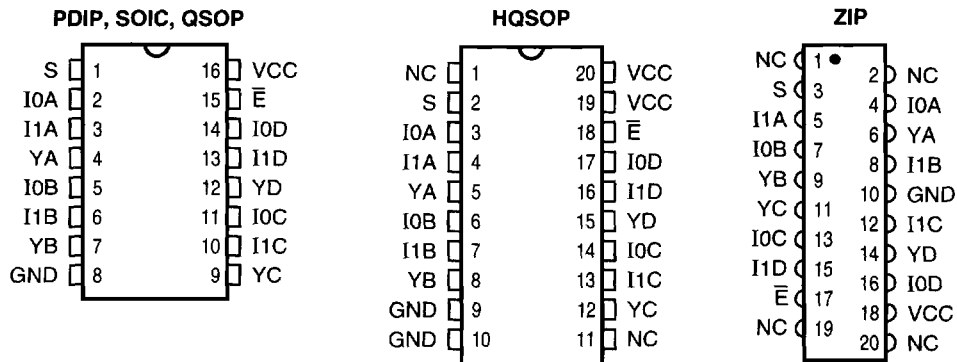
The QSFCT157/8T and QSFCT257/8T are high speed CMOS TTL-compatible 8-input multiplexers. The 157/257 parts are non-inverting; the 158/258 are inverting. The 157/8 has TTL outputs; the 257/8 has 3-state outputs. The QSFCT2157/8T and QSFCT2257/8T are  $25\Omega$  resistor output versions useful for driving transmission lines and reducing system noise. All inputs have clamp diodes for undershoot noise suppression. All outputs have ground bounce suppression (see QSI Application Note AN-001). Outputs will not load an active bus when  $V_{CC}$  is removed from the device.

## FUNCTIONAL BLOCK DIAGRAM



# QSFCT157/8T, 257/8T, 2157/8T, 2257/8T

## PIN CONFIGURATIONS (All Pins Top View)



**Note:**

Available in both 150 mil wide SOIC (package code S1) and 300 mil SOIC (package code SO).

## PIN DESCRIPTION

Name	I/O	Description
Ixx	I	Data Inputs
S	I	Select Input
$\bar{E}$	I	Enable Input
YA-YD	O	Data Outputs

## FUNCTION TABLES

		FCT157				FCT158				
$\bar{E}$	S	YA	YB	YC	YD	YA	YB	YC	YD	Function
H	X	L	L	L	L	H	H	H	H	Disable
L	L	I0A	I0B	I0C	I0D	$\bar{I0A}$	$\bar{I0B}$	$\bar{I0C}$	$\bar{I0D}$	Select 0
L	H	I1A	I1B	I1C	I1D	$\bar{I1A}$	$\bar{I1B}$	$\bar{I1C}$	$\bar{I1D}$	Select 1

		FCT257				FCT258				
$\bar{E}$	S	YA	YB	YC	YD	YA	YB	YC	YD	Function
H	X	Hi-Z	Hi-Z	Hi-Z	Hi-Z	Hi-Z	Hi-Z	Hi-Z	Hi-Z	Disable
L	L	I0A	I0B	I0C	I0D	$\bar{I0A}$	$\bar{I0B}$	$\bar{I0C}$	$\bar{I0D}$	Select 0
L	H	I1A	I1B	I1C	I1D	$\bar{I1A}$	$\bar{I1B}$	$\bar{I1C}$	$\bar{I1D}$	Select 1

**ABSOLUTE MAXIMUM RATINGS**

Supply Voltage to Ground .....	-0.5V to +7.0V
DC Output Voltage $V_{OUT}$ .....	-0.5V to +7.0V
DC Input Voltage $V_{IN}$ .....	-0.5V to +7.0V
AC Input Voltage (for a pulse width $\leq 20$ ns) .....	-3.0V
DC Input Diode Current with $V_{IN} < 0$ .....	-20 mA
DC Output Diode Current with $V_{OUT} < 0$ .....	-50 mA
DC Output Current Max. Sink Current/Pin .....	120 mA
Maximum Power Dissipation .....	0.5 watts
$T_{STG}$ Storage Temperature .....	-65° to +150°C

**Note:** Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to QSI devices that result in functional or reliability type failures.

**CAPACITANCE**

$T_A = 25^\circ\text{C}$ ,  $f = 1$  MHz,  $V_{IN} = 0\text{V}$ ,  $V_{OUT} = 0\text{V}$

Pins	SOIC	QSOP	PDIP	ZIP	Unit
1-3, 5, 6, 10, 11, 13-15	4	4	5	7	pF
4, 7, 9, 12	6	6	7	9	pF
—	8	8	9	10	pF

**Note:** Capacitance is characterized but not tested.

**POWER SUPPLY CHARACTERISTICS**

Symbol	Parameter	Test Conditions <sup>(1)</sup>	Min	Max	Unit
$I_{CC}$	Quiescent Power Supply Current	$V_{CC} = \text{Max.}$ , $\text{freq} = 0$ $0\text{V} \leq V_{IN} \leq 0.2\text{V}$ or $V_{CC} - 0.2\text{V} \leq V_{IN} \leq V_{CC}$	—	1.5	mA
$\Delta I_{CC}$	Supply Current per Input @ TTL HIGH	$V_{CC} = \text{Max.}$ , $V_{IN} = 3.4\text{V}$ , $\text{freq} = 0$ <sup>(2)</sup>	—	2.0	mA
$Q_{CCD}$	Supply Current per Input per MHz	$V_{CC} = \text{Max.}$ , Outputs Open and Enabled One Bit Toggling @ 50% Duty Cycle Other Inputs at GND or $V_{CC}$ <sup>(3,4)</sup>	—	0.25	mA/ MHz

**Notes:**

- For conditions shown as Min. or Max., use the appropriate values specified under DC specifications.
- Per TTL driven input ( $V_{IN} = 3.4\text{V}$ ).
- For flip-flops,  $Q_{CCD}$  is measured by switching one of the data input pins so that the output changes every clock cycle. This is a measurement of device power consumption only and does not include power to drive load capacitance or tester capacitance. This parameter is guaranteed by design but not tested.
- $I_C$  can be computed using the above parameters as explained in the Technical Overview section.

**QSFCT157/8T, 257/8T, 2157/8T, 2257/8T**

**DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE**

Commercial  $T_A = 0^\circ\text{C}$  to  $70^\circ\text{C}$ ,  $V_{CC} = 5.0\text{V} \pm 5\%$

Military  $T_A = -55^\circ\text{C}$  to  $125^\circ\text{C}$ ,  $V_{CC} = 5.0\text{V} \pm 10\%$

Symbol	Parameter	Test Conditions	Min	Typ <sup>(1)</sup>	Max	Unit
$V_{IH}$	Input HIGH Voltage	Logic HIGH for All Inputs	2.0	—	—	V
$V_{IL}$	Input LOW Voltage	Logic LOW for All Inputs	—	—	0.8	V
$\Delta V_T$	Input Hysteresis	$V_{TLH} - V_{THL}$ for All Inputs	—	0.2	—	V
$ I_{IH} $ $ I_{IL} $	Input Current Input HIGH or LOW	$V_{CC} = \text{Max.}$ , $0 \leq V_{IN} < V_{CC}$	—	—	5	$\mu\text{A}$
$ I_{OZ} $	Off-State Output Current (Hi-Z) (257/8, 2257/8)	$V_{CC} = \text{Max.}$ , $0 \leq V_{IN} \leq V_{CC}$	—	—	5	$\mu\text{A}$
$I_{OS}$	Short Circuit Current (FCTXXX)	$V_{CC} = \text{Max.}$ , $V_{OUT} = \text{GND}^{(2,3)}$	-60	—	—	mA
$I_{OR}$	Current Drive (FCT2XXX)	$V_{CC} = \text{Max.}$ , $V_{OUT} = 2.0\text{V}^{(3)}$	50	—	—	mA
$V_{IC}$	Input Clamp Voltage	$V_{CC} = \text{Min.}$ , $I_{IN} = -18 \text{ mA}$ , $T_A = 25^\circ\text{C}^{(3)}$	—	-0.7	-1.2	V
$V_{OH}$	Output HIGH Voltage	$V_{CC} = \text{Min.}$ $I_{OH} = -12 \text{ mA (MIL)}$ $I_{OH} = -15 \text{ mA (COM)}$	2.4 2.4	— —	— —	V
$V_{OL}$	Output LOW Voltage (FCTXXX)	$V_{CC} = \text{Min.}$ $I_{OL} = 32 \text{ mA (MIL)}$ $I_{OL} = 48 \text{ mA (COM)}$	— —	— —	0.50 0.50	V
$V_{OL}$	Output LOW Voltage (FCT2XXX- 25 $\Omega$ )	$V_{CC} = \text{Min.}$ $I_{OL} = 12 \text{ mA (MIL)}$ $I_{OL} = 12 \text{ mA (COM)}$	— —	— —	0.50 0.50	V
$R_{OUT}$	Output Resistance (FCT2XXX- 25 $\Omega$ )	$V_{CC} = \text{Min.}$ $I_{OL} = 12 \text{ mA (MIL)}$ $I_{OL} = 12 \text{ mA (COM)}$	— 20	25 28	— 40	$\Omega$

**Notes:**

1. Typical values indicate  $V_{CC} = 5.0\text{V}$  and  $T_A = 25^\circ\text{C}$ .
2. Not more than one output should be shorted and the duration is  $\leq 1$  second.
3. These parameters are guaranteed by design but not tested.

**SWITCHING CHARACTERISTICS OVER OPERATING RANGE**

Commercial T<sub>A</sub> = 0°C to 70°C, V<sub>CC</sub> = 5.0V ± 5%

Military T<sub>A</sub> = -55°C to 125°C, V<sub>CC</sub> = 5.0V ± 10%

C<sub>LOAD</sub> = 50 pF, R<sub>LOAD</sub> = 500Ω unless otherwise noted.

Symbol	Description <sup>(1)</sup>		157/8 257/8 2157/8 2257/8		157/8A 257/8A 2157/8A 2257/8A		157/8C 257/8C 2157/8C 2257/8C		Unit
			Min	Max	Min	Max	Min	Max	
t <sub>PLH</sub>	Propagation Delay	Com	1.5	6.0	1.5	5.0	1.5	4.3	ns
t <sub>PHL</sub>	In to Y, 157/8/257/8	Mil	1.5	7.0	1.5	5.8	1.5	5.0	
t <sub>PLH</sub>	Propagation Delay	Com	1.5	6.0	1.5	5.0	1.5	4.3	ns
t <sub>PHL</sub>	In to Y, 2157/8/2257/8	Mil	1.5	7.0	1.5	5.8	1.5	5.0	
t <sub>PLH</sub>	Propagation Delay	Com	1.5	10.5	1.5	7.0	1.5	5.2	ns
t <sub>PHL</sub>	S to Y, 157/8/257/8	Mil	1.5	12	1.5	8.1	1.5	6.0	
t <sub>PLH</sub>	Propagation Delay	Com	1.5	10.5	1.5	7.0	1.5	5.2	ns
t <sub>PHL</sub>	S to Y, 2157/8/2257/8	Mil	1.5	12	1.5	8.1	1.5	6.0	
t <sub>PZH</sub>	Output Enable Time	Com	1.5	10.5	1.5	6.0	1.5	4.8	ns
t <sub>PZL</sub>	$\bar{E}$ to Y <sub>i</sub> , 157/8	Mil	1.5	12	1.5	7.4	1.5	5.9	
t <sub>PZH</sub>	Output Enable Time	Com	1.5	10.5	1.5	6.0	1.5	4.8	ns
t <sub>PZL</sub>	$\bar{E}$ to Y <sub>i</sub> , 2157/8	Mil	1.5	12	1.5	7.4	1.5	5.9	
t <sub>PZH</sub>	Output Enable Time	Com	1.5	8.5	1.5	7.0	1.5	6.0	ns
t <sub>PZL</sub>	$\bar{E}$ to Y <sub>i</sub> , 257/8	Mil	1.5	10	1.5	8.0	1.5	6.8	
t <sub>PZH</sub>	Output Enable Time	Com	1.5	8.5	1.5	7.0	1.5	6.0	ns
t <sub>PZL</sub>	$\bar{E}$ to Y <sub>i</sub> , 2257/8	Mil	1.5	10	1.5	8.0	1.5	6.8	
t <sub>PHZ</sub>	Output Disable Time	Com <sup>(2)</sup>	1.5	6.0	1.5	5.5	1.5	5.0	ns
t <sub>PLZ</sub>	$\bar{E}$ to Y <sub>i</sub> , 257/8/2257/8	Mil <sup>(2)</sup>	1.5	8.0	1.5	5.8	1.5	5.3	

**Notes:**

1. Minimums guaranteed but not tested.
2. This parameter is guaranteed by design but not tested.
3. See Test Circuit and Waveforms.

**3**