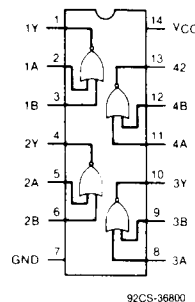


# CD54HC02/3A CD54HCT02/3A

## Quad 2-Input NOR Gate

The RCA-CD54HC02 and CD54HCT02 logic gates utilize silicon-gate CMOS technology to achieve operating speeds similar to LSTTL gates with the low power consumption of standard CMOS integrated circuits. All devices have the ability to drive 10 LSTTL loads. The 54HCT logic family is functionally as well as pin compatible with the standard 54LS logic family.



92CS-36800

FUNCTIONAL DIAGRAM

### Package Specifications

See Section 11, Fig. 10

### Static Electrical Characteristics (Limits with black dots (•) are tested 100%)

CHARACTERISTICS	TEST CONDITIONS							LIMITS		UNITS
	V <sub>DD</sub>	V <sub>O</sub>	I <sub>O</sub>	V <sub>CC</sub> or GND	HC/HCT		MIN.	MAX.		
					HC V <sub>IL</sub> or V <sub>IH</sub>	HCT V <sub>IL</sub> or V <sub>IH</sub>				
Quiescent Device Current I <sub>CC</sub>	25°C	6	—	—	6, 0	—	—	—	2•	μA
	-55°C	6	—	—	6, 0	—	—	—	40•	
	+125°C	6	—	—	6, 0	—	—	—	40•	

The complete static electrical test specification consists of the above by-type static tests combined with the standard static tests in the beginning of this section.

#### HCT INPUT LOADING TABLE

INPUT	UNIT LOAD*
All	1.5

\*Unit load is ΔI<sub>CC</sub> limit specified in Static Characteristics Chart, e.g., 360 μA max. @ 25°C.

### Switching Speed (Limits with black dots (•) are tested 100%.)

#### SWITCHING CHARACTERISTICS (C<sub>L</sub> = 50 pF, Input t<sub>r</sub>, t<sub>f</sub> = 6 ns)

CHARACTERISTIC	SYMBOL	V <sub>CC</sub> V	25°C				-55°C to +125°C				UNITS
			HC		HCT		54HC		54HCT		
			Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	
Propagation Delay Input to Output	t <sub>PLH</sub> t <sub>PHL</sub>	2 4.5 6	— — —	90 18• 15	— — —	— 21• —	— — —	135 27• 23	— — —	32• — —	ns
Transition Times	t <sub>TLH</sub> t <sub>THL</sub>	2 4.5 6	— — —	75 15 13	— — —	— 15 —	— — —	110 22 19	— — —	22 — —	
Input Capacitance	C <sub>I</sub>	—	—	10	—	10	—	10	—	10	

### Burn-In Test-Circuit Connections (Use Static II for /3A burn-in and Dynamic for Life Test.)

Static	STATIC BURN-IN I			STATIC BURN-IN II		
	OPEN	GROUND	V <sub>CC</sub> (6V)	OPEN	GROUND	V <sub>CC</sub> (6V)
CD54HC/HCT02	1,4,10,13	2,3,5-9,11,12	14	1,4,10,13	7	2,3,5,6,8,9,11,12,14
Dynamic	OPEN	GROUND	1/2 V <sub>CC</sub> (3V)	V <sub>CC</sub> (6V)	OSCILLATOR 50 kHz 25 kHz	
CD54HC/HCT02	—	7	1,4,10,13	14	2,3,5,6,8,9,11,12	

NOTE: Each pin except V<sub>CC</sub> and Gnd will have a resistor of 2k-47k ohms.