National Semiconductor

ADVANCE INFORMATION

74LCX2952
Low-Voltage Octal Registered Transceiver with 5V Tolerant Inputs and Outputs

General Description

The LCX2952 is an octal registered transceiver. Two 8-bit back to back registers store data flowing in both directions between two bidirectional buses. Separate clock, clock enable and TRI-STATE® output enable signals are provided for each register.

The LCX2952 is designed for low voltage (3.3V) V_{CC} applications with capability of interfacing to a 5V signal environment.

The LCX2952 is fabricated with an advanced CMOS technology to achieve high speed operation while maintaining CMOS low power dissipation.

Features

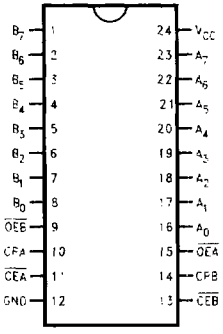
- 5V tolerant inputs and outputs
- Power down high impedance inputs and outputs
- Supports live insertion/withdrawal
- 2.0V–3.6V V_{CC} supply operation
- ±24 mA output drive
- Implements patented Quiet Series™ noise/EMI reduction circuitry
- Functionally compatible with the 74 series 2952
- Latch-up performance exceeds 500 mA
- ESD performance:
  Human body model > 2000V
  Machine model > 200V

Pin Descriptions

Pin Names	Description
A_0–A_7	A-Register Inputs/B-Register TRI-STATE Outputs
B_0–B_7	B-Register Inputs/A-Register TRI-STATE Outputs
$\overline{OEA}$	Output Enable A-Register
CPA	A-Register Clock
$\overline{CEA}$	A-Register Clock Enable
$\overline{OEB}$	Output Enable B-Register
CPB	B-Register Clock
$\overline{CEB}$	B-Register Clock Enable

Connection Diagram

Pin Assignment for SOIC, SSOP II and TSSOP



TL/F/12650–1

	SOIC JEDEC	SSOP Type II	TSSOP
Order Number	74LCX2952WM 74LCX2952WMX	74LCX2952MSA 74LCX2952MSAX	74LCX2952MTC 74LCX2952MTCX
See NS Package Number	M24B	MSA24	MTC24

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