

SN54ALS1034, SN54AS1034A, SN74ALS1034, SN74AS1034A HEX DRIVERS

SDAS053B – APRIL 1982 – REVISED JANUARY 1995

- 'AS1034A Offer High Capacitive-Drive Capability
- Noninverting Drivers
- Package Options Include Plastic Small-Outline (D) Packages, Ceramic Chip Carriers (FK), and Standard Plastic (N) and Ceramic (J) 300-mil DIPs

description

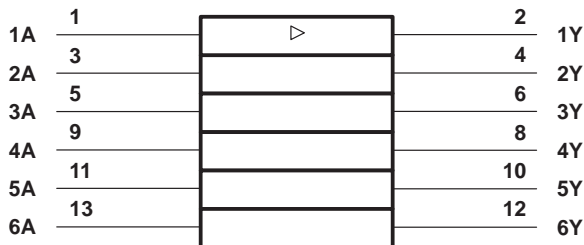
These devices contain six independent noninverting drivers. They perform the Boolean function $Y = A$.

The SN54ALS1034 and SN54AS1034A are characterized for operation over the full military temperature range of -55°C to 125°C . The SN74ALS1034 and SN74AS1034A are characterized for operation from 0°C to 70°C .

FUNCTION TABLE
(each buffer)

INPUT A	OUTPUT Y
H	H
L	L

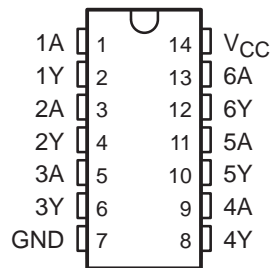
logic symbol†



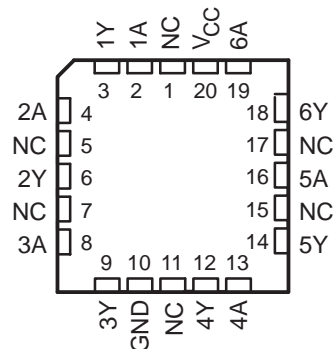
† This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

Pin numbers shown are for the D, J, and N packages.

SN54ALS1034, SN54AS1034A . . . J PACKAGE
SN74ALS1034, SN74AS1034A . . . D OR N PACKAGE
(TOP VIEW)

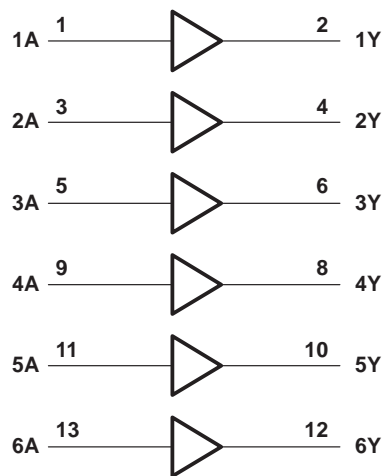


SN54ALS1034, SN54AS1034A . . . FK PACKAGE
(TOP VIEW)



NC – No internal connection

logic diagram (positive logic)



SN54ALS1034, SN54AS1034A, SN74ALS1034, SN74AS1034A HEX DRIVERS

SDAS053B – APRIL 1982 – REVISED JANUARY 1995

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage, V_{CC}	7 V
Input voltage, V_I	7 V
Operating free-air temperature range, T_A : SN54ALS1034	-55°C to 125°C
SN74ALS1034	0°C to 70°C
Storage temperature range	-65°C to 150°C

† Stresses beyond those listed under “absolute maximum ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

recommended operating conditions

		SN54ALS1034			SN74ALS1034			UNIT
		MIN	NOM	MAX	MIN	NOM	MAX	
V_{CC}	Supply voltage	4.5	5	5.5	4.5	5	5.5	V
V_{IH}	High-level input voltage	2			2			V
V_{IL}	Low-level input voltage			0.7			0.8	V
I_{OH}	High-level output current			-12			-15	mA
I_{OL}	Low-level output current			12			24	mA
T_A	Operating free-air temperature	-55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS	SN54ALS1034			SN74ALS1034			UNIT
		MIN	TYP‡	MAX	MIN	TYP‡	MAX	
V_{IK}	$V_{CC} = 4.5\text{ V}$, $I_I = -18\text{ mA}$			-1.2			-1.2	V
V_{OH}	$V_{CC} = 4.5\text{ V to } 5.5\text{ V}$, $I_{OH} = -0.4\text{ mA}$	$V_{CC} - 2$			$V_{CC} - 2$			V
	$V_{CC} = 4.5\text{ V}$	$I_{OH} = -3\text{ mA}$	2.4	3.2	2.4	3.2		
		$I_{OH} = -12\text{ mA}$	2					
V_{OL}	$V_{CC} = 4.5\text{ V}$	$I_{OH} = -15\text{ mA}$	2					
		$I_{OL} = 12\text{ mA}$	0.25	0.4			V	
					0.35	0.5		
I_I	$V_{CC} = 5.5\text{ V}$, $V_I = 7\text{ V}$			0.1			0.1	mA
I_{IH}	$V_{CC} = 5.5\text{ V}$, $V_I = 2.7\text{ V}$			20			20	μA
I_{IL}	$V_{CC} = 5.5\text{ V}$, $V_I = 0.4\text{ V}$			-0.1			-0.1	mA
I_O^{\S}	$V_{CC} = 5.5\text{ V}$, $V_O = 2.25\text{ V}$	-20		-112	-30		-112	mA
I_{CCH}	$V_{CC} = 5.5\text{ V}$, $V_I = 4.5\text{ V}$		3	6	3	6		mA
I_{CCL}	$V_{CC} = 5.5\text{ V}$, $V_I = 0$		8	14	8	14		mA

‡ All typical values are at $V_{CC} = 5\text{ V}$, $T_A = 25^\circ\text{C}$.

§ The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, I_{OS} .



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SDAS053B – APRIL 1982 – REVISED JANUARY 1995

switching characteristics (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V _{CC} = 4.5 V to 5.5 V, C _L = 50 pF, R _L = 500 Ω, T _A = MIN to MAX†				UNIT
			SN54ALS1034		SN74ALS1034		
			MIN	MAX	MIN	MAX	
t _{PLH}	A	Y	1	11	1	8	ns
t _{PHL}			1	13	1	8	

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)‡

Supply voltage, V _{CC}	7 V
Input voltage, V _I	7 V
Operating free-air temperature range, T _A : SN54AS1034A	–55°C to 125°C
SN74AS1034A	0°C to 70°C
Storage temperature range	–65°C to 150°C

‡ Stresses beyond those listed under “absolute maximum ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

recommended operating conditions§

	SN54AS1034A			SN74AS1034A			UNIT
	MIN	NOM	MAX	MIN	NOM	MAX	
V _{CC} Supply voltage	4.5	5	5.5	4.5	5	5.5	V
V _{IH} High-level input voltage	2			2			V
V _{IL} Low-level input voltage			0.8			0.8	V
I _{OH} High-level output current			–40			–48	mA
I _{OL} Low-level output current			40			48	mA
T _A Operating free-air temperature	–55		125	0		70	°C

§ These high sink- or source-current devices are not recommended for use above 40 MHz.



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electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS	SN54AS1034A		SN74AS1034A		UNIT		
		MIN	TYP†	MAX	MIN		TYP†	MAX
V_{IK}	$V_{CC} = 4.5\text{ V}$, $I_I = -18\text{ mA}$			-1.2		-1.2	V	
V_{OH}	$V_{CC} = 4.5\text{ V to } 5.5\text{ V}$, $I_{OH} = -2\text{ mA}$			$V_{CC} - 2$		$V_{CC} - 2$	V	
	$V_{CC} = 4.5\text{ V}$			2.4	3.2	2.4		3.2
		$I_{OH} = -3\text{ mA}$			2			
						2		
V_{OL}	$V_{CC} = 4.5\text{ V}$	$I_{OL} = 40\text{ mA}$		0.25	0.5		V	
		$I_{OL} = 48\text{ mA}$				0.35		0.5
I_I	$V_{CC} = 5.5\text{ V}$, $V_I = 7\text{ V}$			0.1		0.1	mA	
I_{IH}	$V_{CC} = 5.5\text{ V}$, $V_I = 2.7\text{ V}$			20		20	μA	
I_{IL}	$V_{CC} = 5.5\text{ V}$, $V_I = 0.4\text{ V}$			-0.5		-0.5	mA	
$I_{O\ddagger}$	$V_{CC} = 5.5\text{ V}$, $V_O = 2.25\text{ V}$			-50		-200	mA	
I_{CCH}	$V_{CC} = 5.5\text{ V}$, $V_I = 4.5\text{ V}$			9	15	9	15	mA
I_{CCL}	$V_{CC} = 5.5\text{ V}$, $V_I = 0$			21	35	21	35	mA

† All typical values are at $V_{CC} = 5\text{ V}$, $T_A = 25^\circ\text{C}$.

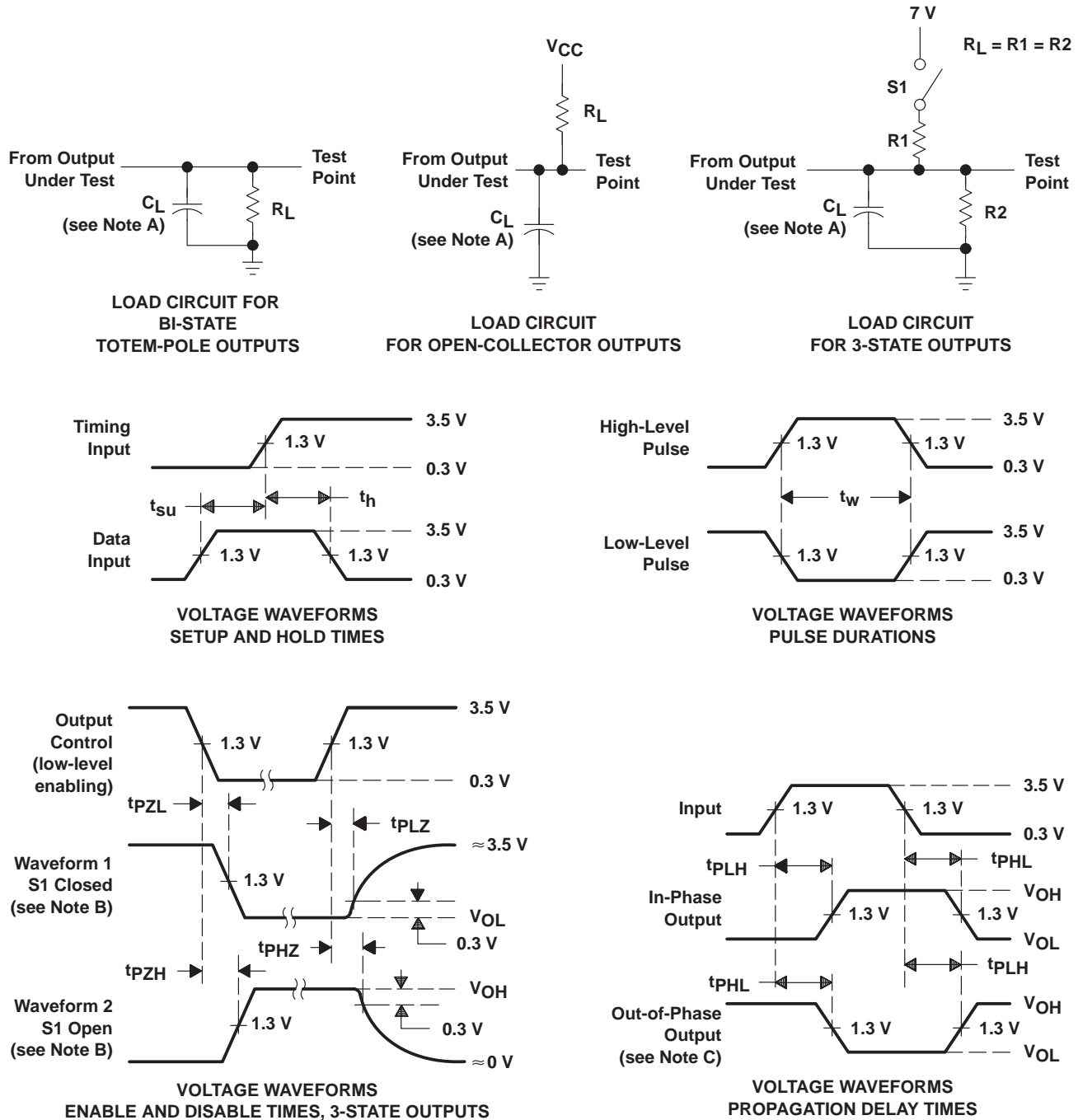
‡ The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, I_{OS} .

switching characteristics (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	$V_{CC} = 4.5\text{ V to } 5.5\text{ V}$, $C_L = 50\text{ pF}$, $R_L = 500\ \Omega$, $T_A = \text{MIN to MAX}\S$				UNIT
			SN54AS1034A		SN74AS1034A		
			MIN	MAX	MIN	MAX	
t_{PLH}	A	Y	1	6.5	1	6	ns
t_{PHL}			1	6.5	1	6	

§ For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

PARAMETER MEASUREMENT INFORMATION
 SERIES 54ALS/74ALS AND 54AS/74AS DEVICES



- NOTES: A. C_L includes probe and jig capacitance.
 B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
 C. When measuring propagation delay items of 3-state outputs, switch S1 is open.
 D. All input pulses have the following characteristics: $PRR \leq 1$ MHz, $t_r = t_f = 2$ ns, duty cycle = 50%.
 E. The outputs are measured one at a time with one transition per measurement.

Figure 1. Load Circuits and Voltage Waveforms

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PRODUCT SUPPORT: [TRAINING](#)

SN54AS1034A, Hex Drivers

DEVICE STATUS: **ACTIVE**

PARAMETER NAME	SN54AS1034A	SN74AS1034A
Voltage Nodes (V)	5	5
Vcc range (V)	4.5 to 5.5	4.5 to 5.5
Input Level	TTL	TTL
Output Level	TTL	TTL
Output Drive (mA)		-48/48
tpd max (ns)		6
Static Current		25

FEATURES

[▲Back to Top](#)

- AS1034A Offer High Capacitive-Drive Capability
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DESCRIPTION

[▲Back to Top](#)

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TECHNICAL DOCUMENTS

[▲Back to Top](#)

To view the following documents, [Acrobat Reader 4.0](#) is required.

To download a document to your hard drive, right-click on the link and choose 'Save'.

DATASHEET

[▲Back to Top](#)

Full datasheet in Acrobat PDF: [sn54as1034a.pdf](#) (94 KB,Rev.B) (Updated: 01/01/1995)

APPLICATION NOTES

[▲Back to Top](#)

View Application Notes for [Digital Logic](#)

- [Advanced Schottky \(ALS and AS\) Logic Families](#) (SDAA010 - Updated: 08/01/1995)
- [Advanced Schottky Load Management](#) (SDYA016 - Updated: 02/01/1997)
- [Designing With Logic \(Rev. C\)](#) (SDYA009C - Updated: 06/01/1997)

- [Evaluation of Nickel/Palladium/Gold-Finished Surface-Mount Integrated Circuits](#) (SZZA026 - Updated: 06/20/2001)
- [Input and Output Characteristics of Digital Integrated Circuits](#) (SDYA010 - Updated: 10/01/1996)
- [Live Insertion](#) (SDYA012 - Updated: 10/01/1996)
- [TI IBIS File Creation, Validation, and Distribution Processes](#) (SZZA034 - Updated: 08/29/2002)
- [Understanding and Interpreting Texas Instruments Standard-Logic Products Data Sh \(Rev. A\)](#) (SZZA036A - Updated: 02/27/2003)

MORE LITERATURE

[▲Back to Top](#)

- [Enhanced Plastic Portfolio Brochure](#) (SGZB004, 387 KB - Updated: 08/19/2002)
- [Logic Reference Guide](#) (SCYB004, 1032 KB - Updated: 10/23/2001)
- [MicroStar Junior BGA Design Summary](#) (SCET004, 167 KB - Updated: 07/28/2000)
- [Military Brief](#) (SGYN138, 803 KB - Updated: 10/10/2000)
- [Overview of IEEE Std 91-1984, Explanation of Logic Symbols Training Booklet \(Rev. A\)](#) (SDYZ001A, 138 KB - Updated: 07/01/1996)
- [Palladium Lead Finish User's Manual](#) (SDYV001, 2041 KB - Updated: 11/01/1996)
- [QML Class V Space Products Military Brief \(Rev. A\)](#) (SGZN001A, 257 KB - Updated: 10/07/2002)

USER GUIDES

[▲Back to Top](#)

- [LOGIC Pocket Data Book](#) (SCYD013, 4837 KB - Updated: 12/05/2002)

PRICING/AVAILABILITY/PKG

[▲Back to Top](#)

DEVICE INFORMATION
Updated Daily

ORDERABLE DEVICE	STATUS	PACKAGE TYPE PINS	TEMP (°C)	DSCC NUMBER	PRODUCT CONTENT	BUDGETARY PRICING QTY \$US	STD PACK QTY
5962-88731012A	ACTIVE	LCCC (FK) 20	-55 TO 125		View Contents	1KU 9.81	1
5962-8873101CA	ACTIVE	CDIP (J) 14	-55 TO 125		View Contents	1KU 5.09	1
5962-8873101DA	ACTIVE	CFP (W) 14	-55 TO 125		View Contents	1KU 7.87	1
SN54AS1034AJ	ACTIVE	CDIP (J) 14	-55 TO 125		View Contents	1KU 4.32	1
SNJ54AS1034AFK	ACTIVE	LCCC (FK) 20	-55 TO 125	5962-88731012A	View Contents	1KU 9.81	1
SNJ54AS1034AJ	ACTIVE	CDIP (J) 14	-55 TO 125	5962-8873101CA	View Contents	1KU 5.09	1
SNJ54AS1034AW	ACTIVE	CFP (W) 14	-55 TO 125	5962-8873101DA	View Contents	1KU 7.87	1

TI INVENTORY STATUS

As Of 09:00 AM GMT, 17 Apr 2003

IN STOCK	IN PROGRESS QTY DATE	LEAD TIME
<u>6</u> *	3978 20 May	8 WKS
	> 10k 27 May	
<u>78</u> *	> 10k 20 May	8 WKS
<u>0</u> *	> 10k 20 May	8 WKS
<u>67</u> *	> 10k 20 May	8 WKS
<u>30</u> *	3889 20 May	8 WKS
	> 10k 27 May	
<u>0</u> *	> 10k 20 May	8 WKS
<u>0</u> *	> 10k 20 May	8 WKS

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As Of 09:00 AM GMT, 17 Apr 2003

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None Reported View Distributors		
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