

High Speed CMOS 3.3V 16-Bit Bus Registered Transceiver with Zero Hold Time

QS74LCX16Z646

FEATURES/BENEFITS

- 5V tolerant inputs and outputs
- Industry standard pinouts
- $10\mu\text{A}$ I_{CCQ} quiescent power supply current
- Hot insertable
- 2.0V–3.6V V_{CC} supply operation
- $\pm 24\text{mA}$ balanced output drive
- Meets or exceeds JEDEC Standard 36 specifications
- $t_{\text{PD}} = 5.0\text{ns}$
- Zero hold time
- Input hysteresis for noise immunity
- Multiple power and ground pins for low noise
- Operating temperature range:
–40°C to 85°C
- Latch-up performance exceeds 500mA
- ESD performance:
Human body model > 2000V
Machine model > 200V
- Packages available:
56-pin TSSOP
56-pin SSOP

DESCRIPTION

The LCX16Z646 is a 16-bit bus registered transceiver with three-state outputs and zero hold time that is ideal for driving address and data buses. The LXC16Z646 is organized for transmission of data between A bus and B bus either directly or from the internal storage registers. The 3.3V LCX family features low power, low switching noise, and fast switching speeds for low power portable applications as well as high-end advanced workstation applications. 5V tolerant inputs and outputs allow this LCX product to be used in mixed 5V and 3.3V applications. Easy board layout is facilitated by the use of flow-through pinouts and byte enable controls provide architectural flexibility for systems designers. To accommodate hot-plug or live insertion applications, this product is designed not to load an active bus when V_{CC} is removed.

Figure 1. Functional Block Diagram

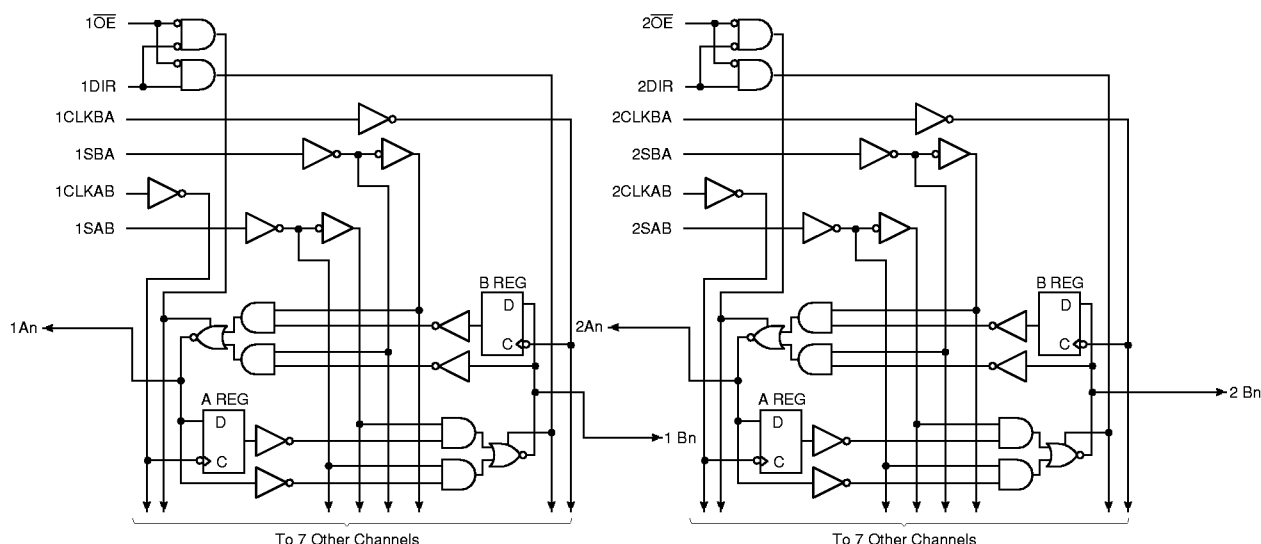


Figure 2. Pin Configuration
(All Pins Top View)
SSOP, TSSOP

1DIR	1	56	1 \overline{OE}
1CLKAB	2	55	1CLKBA
1SAB	3	54	1SBA
GND	4	53	GND
1A1	5	52	1B1
1A2	6	51	1B2
V _{CC}	7	50	V _{CC}
1A3	8	49	1B3
1A4	9	48	1B4
1A5	10	47	1B5
GND	11	46	GND
1A6	12	45	1B6
1A7	13	44	1B7
1A8	14	43	1B8
2A1	15	42	2B1
2A2	16	41	2B2
2A3	17	40	2B3
GND	18	39	GND
2A4	19	38	2B4
2A5	20	37	2B5
2A6	21	36	2B6
V _{CC}	22	35	V _{CC}
2A7	23	34	2B7
2A8	24	33	2B8
GND	25	32	GND
2SAB	26	31	2SBA
2CLKAB	27	30	2CLKBA
2DIR	28	29	2 \overline{OE}

Table 1. Pin Description

Name	Description
xAx	Data Register A Inputs Data Register B Outputs
xBx	Data Register B Inputs Data Register A Outputs
xCLKAB, xCLKBA	Clock Inputs
xSAB, xSBA	Output Source Select Inputs
xDIR, x \overline{OE}	Output Enable Inputs

Table 2. Function Table

Inputs						Data I/O ⁽¹⁾		Operation or Function
x \overline{OE}	xDIR	xCLKAB	xCLKBA	xSAB	xSBA	xAx	xBx	
H	X	H or L	H or L	x	x	Input	Input	Isolation
H	X	↑	↑	X	X			Store A and B Data
L	L	X	X	X	L	Output	Input	Real Time B Data to A Bus
L	L	X	H or L	X	H			Stored B Data to A Bus
L	H	X	X	L	X	Input	Output	Real Time A Data to B Bus
L	H	H or L	X	H	X			Stored A Data to B Bus

Notes:

- The data output functions may be enabled or disabled by various signals at the xOE or xDIR inputs. Data input functions are always enabled, i.e. data at the bus pins will be stored on every LOW-to-HIGH transition on the clock inputs.
- H = HIGH Voltage Level
L = LOW Voltage Level
X = Don't Care
↑ = LOW-to-HIGH Transition

Table 3. Capacitance

Symbol	Pins	Typ	Unit	Conditions
C_{IN}	Input Capacitance	7	pF	$V_{IN} = 0V$, $V_{OUT} = 0V$, $f = 1MHz$
$C_{I/O}$	I/O Capacitance	8	pF	$V_{IN} = 0V$, $V_{OUT} = 0V$, $f = 1MHz$
C_{PD}	Power Dissipation Capacitance	25	pF	$V_{CC} = 3.3V$, $V_{IN} = 0$ or V_{CC} $f = 10MHz$

Note: Capacitance is characterized but not production tested.

Table 4. Absolute Maximum Ratings

Supply Voltage to Ground	-0.5V to 7.0V
DC Output Voltage V_{OUT}	
Outputs HIGH-Z	-0.5V to 7.0V
Outputs Active	-0.5V to $V_{CC} - 0.5V$
DC Input Voltage V_{IN}	-0.5V to + 7.0V
DC Input Diode Current with $V_{IN} < 0$	-50mA
DC Output Diode Current	
$V_O < 0$	-50mA
$V_O > V_{CC}$	50mA
DC Output Source/Sink Current (I_{OH}/I_{OL})	$\pm 50mA$
DC Supply Current per Supply Pin	$\pm 100mA$
DC Ground Current per Ground Pin	$\pm 100mA$
T_{STG} Storage Temperature	-65° to 150°C

Note: Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to this device resulting in functional or reliability type failures.

Table 5. Recommended Operating Conditions

Symbol	Parameter	Min	Max	Unit
V_{CC}	Supply Voltage, Operating	2.0	3.6	V
	Supply Voltage, Data Retention Only	1.5	3.6	
V_{IN}	Input Voltage	0	5.5	V
V_{OUT}	Output Voltage in Active State	0	V_{CC}	V
	Output Voltage in "OFF" State	0	5.5	
I_{OH}/I_{OL}	Output Current	$V_{CC} = 3.0 - 3.6V$	± 24	mA
		$V_{CC} = 2.7V$	± 12	
$\Delta t/\Delta v$	Input Transition Slew Rate	—	10	ns/V
T_A	Operating Free Air Temperature	-40	85	°C

Table 6. DC Electrical Characteristics Over Operating Range

Industrial Temperature Range, $T_A = -40^{\circ}\text{C}$ to 85°C

Symbol	Parameter	Test Conditions	Min	Typ ⁽¹⁾	Max	Unit
V_{IH}	Input HIGH Voltage	Logic HIGH for All Inputs	2.0	—	—	V
V_{IL}	Input LOW Voltage	Logic LOW for All Inputs	—	—	0.8	V
V_{OH}	Output HIGH Voltage	$V_{CC} = 2.7\text{V}$, $I_{OH} = -100\mu\text{A}$ $V_{CC} = 2.7\text{V}$, $I_{OH} = -12\text{mA}$ $V_{CC} = 3.0\text{V}$, $I_{OH} = -18\text{mA}$ $V_{CC} = 3.0\text{V}$, $I_{OH} = -24\text{mA}$	$V_{CC} - 0.2$ 2.2 2.4 2.2	— — — —	— — — —	V
V_{OL}	Output LOW Voltage	$V_{CC} = 2.7\text{V}$, $I_{OL} = 100\mu\text{A}$ $V_{CC} = 2.7\text{V}$, $I_{OL} = 12\text{mA}$ $V_{CC} = 3.0\text{V}$, $I_{OL} = 16\text{mA}$ $V_{CC} = 3.0\text{V}$, $I_{OL} = 24\text{mA}$	— — — —	— — — —	0.2 0.4 0.4 0.5	V
ΔV_T	Input Hysteresis ⁽²⁾	$V_{TLH} - V_{THL}$ for All Inputs	—	150	—	mV
I_I	Input Leakage Current	$V_I = 0\text{V}$, $V_I = 5.5\text{V}$	—	—	± 1.0	μA
I_{OZ}	High-Z I/O Leakage	$V_O = 0\text{V}$, $V_O = 5.5\text{V}$ $V_I = V_{IH}$ or V_{IL}	—	—	± 1.0	μA
I_{OS}	Short Circuit Current ^(2,3)	$V_{CC} = 3.6\text{V}$, $V_O = \text{GND}$	-60	—	-240	mA
I_{OFF}	Power Off Leakage	$V_{CC} = 0\text{V}$, V_I or $V_O = 5.5\text{V}$	—	—	10	μA
V_{IK}	Input Clamp Voltage	$V_{CC} = 2.7\text{V}$, $I_{IN} = -18\text{mA}$	—	-0.7	-1.2	V

- Notes:** 1. Typical values indicate $V_{CC} = 3.3\text{V}$, and $T_A = 25^{\circ}\text{C}$.
2. These parameters are guaranteed by characterization but not production tested.
3. Not more than one output should be tested at one time. Duration of test should not exceed one second.

Table 7. Power Supply Characteristics

Symbol	Parameter	Test Conditions ⁽¹⁾	Typ ⁽²⁾	Max	Unit
I_{CC}	Quiescent Power Supply Current	$V_{CC} = 3.6V$, Freq = 0 $V_{IN} = GND$ or V_{CC}	0.1	10	μA
ΔI_{CC}	Supply Current per Input @ TTL HIGH	$V_{CC} = 3.6V$, $V_{IN} = V_{CC} - 0.6V^{(3)}$	2.0	30	μA
I_{CCD}	Supply Current per Input per MHz ⁽⁴⁾	$V_{CC} = 3.6V$, Outputs Open One Bit Toggling @ 50% Duty Cycle $xDIR = x\overline{OE} = GND$ $V_{IN} = V_{CC}$ $V_{IN} = GND$	65	100	$\mu A/MHz$
I_C	Total Power Supply Current ⁽⁶⁾	$V_{CC} = 3.6V$, Outputs Open One Bit Toggling @ 50% Duty Cycle $f = 5MHz$, $f_{CP} = 10MHz$ (xCLKBA) $xDIR = x\overline{OE} = GND$, $V_{IN} = V_{CC} - 0.6V$ $V_{IN} = GND$	0.7 ⁽⁵⁾	1.05 ⁽⁵⁾	mA
		$V_{CC} = 3.6V$, Outputs Open Sixteen Bits Toggling @ 50% Duty Cycle $f = 2.5MHz$, $f_{CP} = 10MHz$ (xCLKBA) $xDIR = x\overline{OE} = GND$ $V_{IN} = V_{CC} - 0.6V$ $V_{IN} = GND$	2.7 ⁽⁵⁾	4.3 ⁽⁵⁾	mA

Notes:

- For conditions shown as Min. or Max., use the appropriate values specified under Recommended Operating Conditions for applicable device type.
- Typical values are at $V_{CC} = 3.3V$, 25°C ambient.
- Per TTL driven input. All Other Inputs at V_{CC} or GND.
- This parameter is not directly testable, but is derived for use in Total Power Supply Calculations.
- Values for these conditions are examples of the I_{CC} formula. These limits are guaranteed by design but not tested.
- $I_C = I_{CCQ} + \Delta I_{CC} D_H N_T + I_{CCD} f N_O$
 I_{CCQ} = Quiescent Current (I_{CCL} , I_{CCH} , and I_{CCZ}).
 ΔI_{CC} = Power Supply Current for a TTL-High Input ($V_{IN} = V_{CC} - 0.6V$).
 D_H = Duty Cycle for TTL High Inputs.
 N_T = Number of TTL High Inputs.
 I_{CCD} = Dynamic Current Caused by an Input Transition Pair (HLH or LHL).
 f = Average Switching Frequency per Output.
 N_O = Number of Outputs Switching.

Table 8. Dynamic Switching Characteristics⁽¹⁾

Symbol	Parameter	Conditions	V_{CC} (V)	$T_A = 25^\circ C$	Units
				Typical	
V_{OLP}	Quiet Output Dynamic Peak V_{OL}	$C_L = 50pF$, $V_{IH} = 3.3V$, $V_{IL} = 0V$	3.3	0.8	V
V_{OLV}	Quiet Output Dynamic Valley V_{OL}	$C_L = 50pF$, $V_{IH} = 3.3V$, $V_{IL} = 0V$	3.3	0.8	V

Note:

- Characterized but not production tested.

Table 9. Switching Characteristics Over Operating Range

Industrial Temperature Range, $T_A = -40^{\circ}\text{C}$ to 85°C .

$C_{\text{LOAD}} = 50\text{pF}$, $R_{\text{LOAD}} = 500\Omega$ unless otherwise noted.

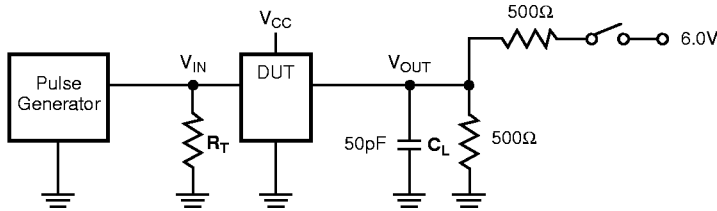
Symbol	Description ⁽¹⁾	$V_{\text{CC}} = 3.3 \pm 0.3\text{V}$		$V_{\text{CC}} = 2.7\text{V}^{(2)}$		Unit
		Min	Max	Min	Max	
f_{MAX}	Clock Pulse Frequency ⁽²⁾	170	—	—	—	MHz
t_{PHL} t_{PLH}	Propagation Delay Bus to Bus	2.0	5.0	2.0	6.0	ns
t_{PZH} t_{PZL}	Output Enable Time xDIR or xOE to Bus	2.0	7.5	2.0	8.5	ns
t_{PHZ} t_{PLZ}	Output Disable Time ⁽²⁾ xDIR or xOE to Bus	2.0	6.0	2.0	7.0	ns
t_{PHL} t_{PLH}	Propagation Delay Clock to Bus	2.0	6.0	2.0	7.0	ns
t_{PHL} t_{PLH}	Propagation Delay xSAB or xSBA to Bus	2.0	6.0	2.0	7.0	ns
t_{SU}	Setup Time HIGH or LOW Bus to Clock	3.0	—	3.5	—	ns
t_{H}	Hold Time HIGH or LOW Bus to Clock	0	—	0	—	ns
t_{W}	Clock Pulse Width ⁽²⁾ LOW or HIGH	3.0	—	3.0	—	ns
$t_{\text{SK}(0)}$	Output Skew ⁽³⁾	—	0.5	—	—	ns

Notes:

1. Minimums guaranteed but not tested on propagation delays. See Test Circuit and Waveforms.
2. Guaranteed by characterization.
3. Skew between any two outputs of the same package switching in the same direction.
This parameter is guaranteed by characterization but not production tested.

TEST CIRCUIT AND WAVEFORMS

Figure 3. Test Circuit



SWITCH POSITION

Test	Switch
Open Drain	
Disable LOW	6V
Enable LOW	
Disable HIGH	GND
Enable HIGH	
All Other Inputs	Open

DEFINITIONS:

C_L = Load capacitance: includes jig and probe capacitance.

R_T = Termination resistance: should be equal to Z_{OUT} of the Pulse generator.

Figure 4. Setup, Hold, and Release Timing

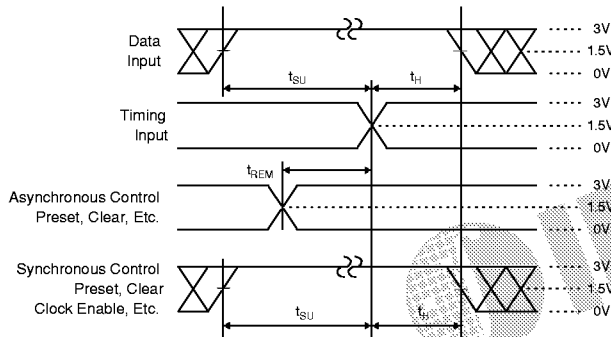


Figure 6. Pulse Width

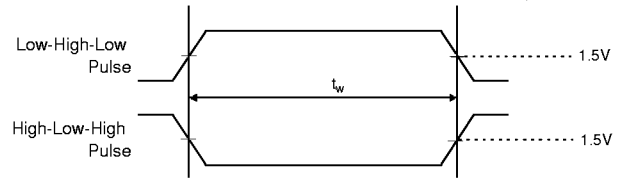


Figure 5. Enable and Disable Timing

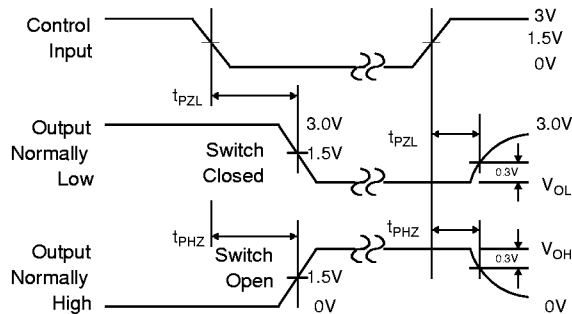
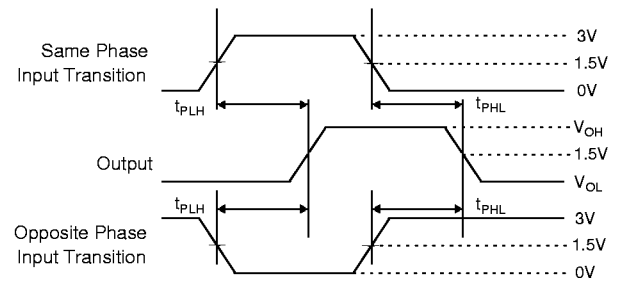


Figure 7. Propagation Delay



Notes:

1. Input Control Enable = LOW and Input Control Disable = HIGH.
2. Pulse Generator for All Pulses: Rate $\leq 1.0\text{MHz}$;
 $Z_{OUT} \leq 50\Omega$; $t_F, t_R \leq 2.5\text{ns}$.