

SN54F30, SN74F30 8-INPUT POSITIVE-NAND GATES

D2932, MARCH 1987

- Package Options Include Plastic "Small Outline" Packages, Ceramic Chip Carriers, and Standard Plastic and Ceramic 300-mil DIPs
- Dependable Texas Instruments Quality and Reliability

description

These devices contain a single 8-input NAND gate and perform the following Boolean functions in positive logic:

$$Y = \overline{A \cdot B \cdot C \cdot D \cdot E \cdot F \cdot G \cdot H} \text{ or}$$

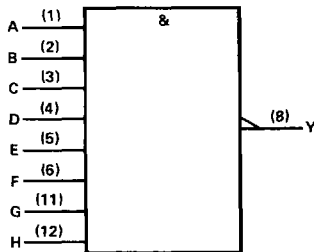
$$Y = \overline{A} + \overline{B} + \overline{C} + \overline{D} + \overline{E} + \overline{F} + \overline{G} + \overline{H}$$

The SN54F30 is characterized for operation over the full military temperature range of -55°C to 125°C . The SN74F30 is characterized for operation from 0°C to 70°C .

FUNCTION TABLE

INPUTS A THRU H	OUTPUT Y
All inputs H	L
One or more inputs L	H

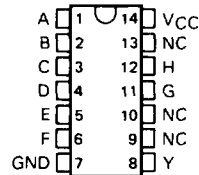
logic symbol†



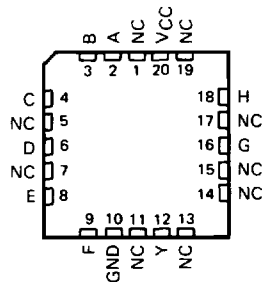
† This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

Pin numbers shown are for D, J, and N packages.

SN54F30 . . . J PACKAGE
SN74F30 . . . D OR N PACKAGE
(TOP VIEW)

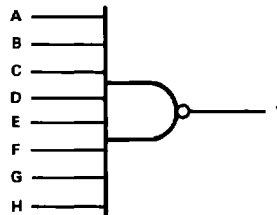


SN54F30 . . . FK PACKAGE
(TOP VIEW)



NC—No internal connection.

logic diagram (positive logic)



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Data Sheets

SN54F30, SN74F30

8-INPUT POSITIVE-NAND GATES

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, V_{CC}	-0.5 V to 7 V
Input voltage [†]	-1.2 V to 7 V
Input current	-30 mA to 5 mA
Voltage applied to any output in the high state	-0.5 V to V_{CC}
Current into any output in the low state	40 mA
Operating free-air temperature range: SN54F30	-55°C to 125°C
SN74F30	0°C to 70°C
Storage temperature range	-65°C to 150°C

[†]The input voltage ratings may be exceeded provided the input current ratings are observed.

recommended operating conditions

	SN54F30			SN74F30			UNIT
	MIN	NOM	MAX	MIN	NOM	MAX	
V_{CC} Supply voltage	4.5	5	5.5	4.5	5	5.5	V
V_{IH} High-level input voltage	2			2			V
V_{IL} Low-level input voltage			0.8			0.8	V
I_{IK} Input clamp current			-18			-18	mA
I_{OH} High-level output current			-1			-1	mA
I_{OL} Low-level output current			20			20	mA
T_A Operating free-air temperature	-55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS		SN54F30			SN74F30			UNIT
			MIN	TYP [§]	MAX	MIN	TYP [§]	MAX	
V_{IK}	$V_{CC} = 4.5 V$,	$I_I = -18 mA$			-1.2			-1.2	V
$V_{OH}^{\#}$	$V_{CC} = 4.5 V$,	$I_{OH} = -1 mA$	2.5	3.4		2.5	3.4		V
V_{OL}	$V_{CC} = 4.5 V$,	$I_{OL} = 20 mA$		0.30	0.5		0.30	0.5	V
I_I	$V_{CC} = 5.5 V$,	$V_I = 7 V$			0.1			0.1	mA
I_{IH}	$V_{CC} = 5.5 V$,	$V_I = 2.7 V$			20			20	μA
I_{IL}	$V_{CC} = 5.5 V$,	$V_I = 0.5 V$			-0.6			-0.6	mA
I_{OS}^{\ddagger}	$V_{CC} = 5.5 V$,	$V_O = 0$	-60		-150	-60		-150	mA
I_{CCH}	$V_{CC} = 5.5 V$,	$V_I = 0$		0.7	1.5		0.7	1.5	mA
I_{CCL}	$V_{CC} = 5.5 V$,	$V_I = 4.5 V$		2.2	4		2.2	4	mA

switching characteristics (see Note 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	$V_{CC} = 5 V$, $C_L = 50 pF$, $R_L = 500 \Omega$, $T_A = 25^\circ C$			$V_{CC} = 4.5 V$ to $5.5 V$, $C_L = 50 pF$, $R_L = 500 \Omega$, $T_A = MIN$ to MAX^{\ddagger}			UNIT	
			F30			SN54F30		SN74F30		
			MIN	TYP	MAX	MIN	MAX	MIN		MAX
t_{PLH}	A or B	Y	1	3.1	5	1	6	1	5.5	ns
t_{PHL}	A or B	Y	1	2.6	4.5	1	6	1	5	ns

[‡] For conditions shown as MIN or MAX, use the appropriate value specified under Recommended Operating Conditions.

[§] All typical values are at $V_{CC} = 5 V$, $T_A = 25^\circ C$.

[¶] Not more than one output should be shorted at a time and the duration of the short circuit should not exceed one second.

[#] For the SN74F30 at $V_{CC} = 4.75 V$ and $I_{OH} = -1 mA$, $V_{OH} min = 2.7 V$.

NOTE 1: See General Information for load circuits and waveforms.