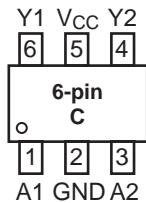


Features

- High-speed: $t_{PD} = 2.5\text{ns}$ typical into 50pF @ 5V V_{CC}
- Unbuffered Output
- Broad Operating Range: $V_{CC} = 1.65\text{V}$ to 5.5V
- Balanced Output Drive: $\pm 8\text{mA}$ at 4.5V V_{CC}
- Package: 6-pin space saving SC70 (C)

PI74STX2GU04

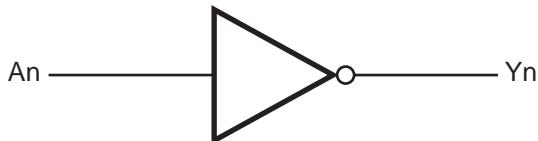


Description

PI74STX2GU04 is a dual unbuffered inverter that operates over the 1.65V to 5.5V V_{CC} operating range. The internal design of a single stage inverter makes this device especially suitable for crystal oscillator or analog applications.

Pericom's PI74STX series of products are produced using the Company's advanced submicron technology.

Block Diagram



Pin Description

Pin Names	Description
An	Inputs
Yn	Outputs

Function Table

Inputs	Output
A	Y
L	H
H	L

Note:

H = HIGH Logic Level
L = LOW Logic Level

Recommended Operating Conditions⁽¹⁾

Parameter	Condition	Min.	Max.	Units
Supply Voltage (V_{CC})	Input Voltage (V_{IN})	0	5.5	V
Output Voltage (V_{OUT})				
Operating Temperature				
Input Rise and Fall Time (t_r, t_f)	$V_{CC} = 1.8\text{V}, 2.5\text{V}\pm 0.2\text{V}$	0	20	ns/V
	$V_{CC} = 3.3\text{V}\pm 0.3\text{V}$	0	10	
	$V_{CC} = 5.0\text{V}\pm 0.5\text{V}$	0	5	

Note:

1. Unused inputs must be held HIGH or LOW. They may not float.

Absolute Maximum Ratings

Supply Voltage (V _{CC})	-0.5V to +7V	DC Output Current (I _{OUT})	±50mA
DC Input Voltage (V _{IN})	-0.5V to +7V	DC V _{CC} /GND Current (I _{CC} /I _{GND})	±100mA
DC Output Voltage (V _{OUT})	-0.5V to V _{CC} +0.5V	Storage Temperature (T _{STG})	-65°C to +150°C
DC Input Diode Current (I _{IK})	-50mA to 20mA	Junction Lead Temperature (IOS)	260°C
DC Output Diode Current (I _{OK})	-50mA to 20mA	Power Dissipation SC70	180mW

Note:

Absolute maximum ratings are DC values beyond which the device may be damaged or have its useful life impaired. The datasheet specifications should be met, without exception, to ensure that the system design is reliable over its power supply, temperature, and output/input loading variables. Pericom does not recommend operation outside datasheet specifications.

DC Electrical Characteristics (Over supply voltage and operating temperature ranges, unless otherwise specified)

Symbol	Parameters	V _{CC} (V)	Conditions	T _A = +25°C			T _A = -40 to +85°C		Units
				Min.	Typ.	Max.	Min.	Max.	
V _{IH}	HIGH level Input Voltage	1.8 - 2.7 3.0 - 5.5		0.85 V _{CC} 0.8 V _{CC}			0.85 V _{CC} 0.8 V _{CC}		
V _{IL}	LOW level Input Voltage	1.8 - 2.7 3.0 - 5.5					0.15 V _{CC} 0.2 V _{CC}		0.15 V _{CC} 0.2 V _{CC}
V _{OH}	HIGH level Output Voltage	1.65 1.8 2.3 3.0 4.5	V _{IN} = V _{IL}	I _{OH} = -100µA	1.55	1.65		1.55	
					1.6	1.8		1.6	
					2.1	2.3		2.1	
					2.7	3.0		2.7	
					4.0	4.5		4.0	
	LOW level Output Voltage	1.65 2.3 3.0 3.0 4.5	V _{IN} = V _{IH}	I _{OL} = 100µA	I _{OH} = -2mA	1.26	1.44		1.29
					I _{OH} = -2mA	1.9	2.19		1.9
					I _{OH} = -4mA	2.4	2.82		2.4
					I _{OH} = -6mA	2.3	2.73		2.3
					I _{OH} = -8mA	3.8	4.24		3.8
V _{OL}	LOW level Output Voltage	1.65 1.8 2.3 3.0 4.5	V _{IN} = V _{IH}	I _{OL} = 100µA		0.0	0.2		0.2
						0.0	0.2		0.2
						0.0	0.2		0.2
						0.0	0.3		0.3
						0.0	0.5		0.5
	HIGH level Output Voltage	1.65 2.3 3.0 3.0 4.5	V _{IN} = V _{IL}	I _{OL} = 2mA I _{OL} = 2mA I _{OL} = 4mA I _{OL} = 6mA I _{OL} = 8mA	I _{OL} = 2mA	0.09	0.24		0.24
					I _{OL} = 2mA	0.07	0.3		0.3
					I _{OL} = 4mA	0.11	0.4		0.4
					I _{OL} = 6mA	0.17	0.55		0.55
					I _{OL} = 8mA	0.19	0.55		0.55
I _{IN}	Input Leakage Current	0 to 5.5	V _{IN} = 5.5V or GND				±1.0		±10
I _{CC}	Quiescent Supply Current	1.65 - 5.5	V _{IN} = 5.5V or GND				1.0		10
I _{CCPEAK}	Peak Supply Current in Analog Operation	1.8 2.5 3.3 5.0	V _{OUT} = Open V _{IN} = Adjust for Peak I _{CC} Current				0.3 2.5 7 22		mA

AC Electrical Characteristics

Symbol	Parameter	V _{CC} (V)	Conditions	T _A = +25°C			T _A = -40°C to +85°C		Units	Fig. No.
				Min.	Typ.	Max.	Min.	Max.		
t _{PLH} , t _{PHL}	Propagation Delay	1.65	C _L = 15pF, R _L = 1M ohm	1.5		8.5	1.5	8.9	ns	1 3
		1.8		1.5		7.0	1.5	7.4		
		2.5 ±0.2		1.2		4.6	1.2	5.0		
		3.3 ±0.3		0.5		3.3	0.5	3.7		
		5.0 ±0.5		0.5		2.6	0.5	2.9		
t _{PLH} , t _{PHL}	Propagation Delay	3.3 ±0.3	C _L = 50pF, R _L = 500 ohms	1.2		6.0	1.2	6.6	ns	1 3
		5.0 ±0.5		0.8		4.7	0.8	5.2		
C _{IN}	Input Capacitance	0			3.5				pF	2
C _{PD}	Power Dissipation Capacitance ⁽³⁾	3.3 5.0			5 11					

Notes:

3. C_{PD} is defined as the value of the internal equivalent capacitance which is derived from dynamic operating current consumption (I_{CCD}) at no output loading and operating at 50% duty cycle (see Figure 2). C_{PD} is related to I_{CCD} dynamic operating current by the expression: I_{CCD} = (C_{PD})(V_{CC})(f_{IN}) + (I_{CC} static).

AC Loading and Waveforms

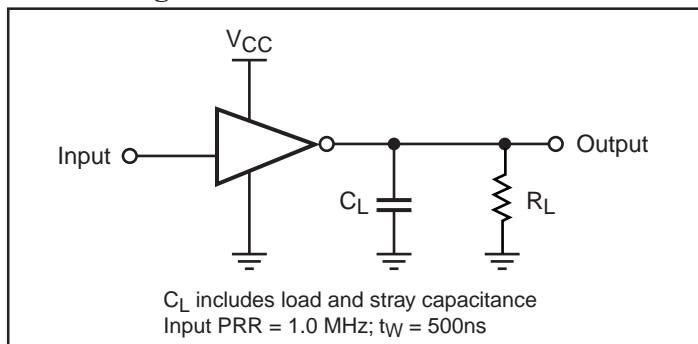


Figure 1. AC Test Circuit

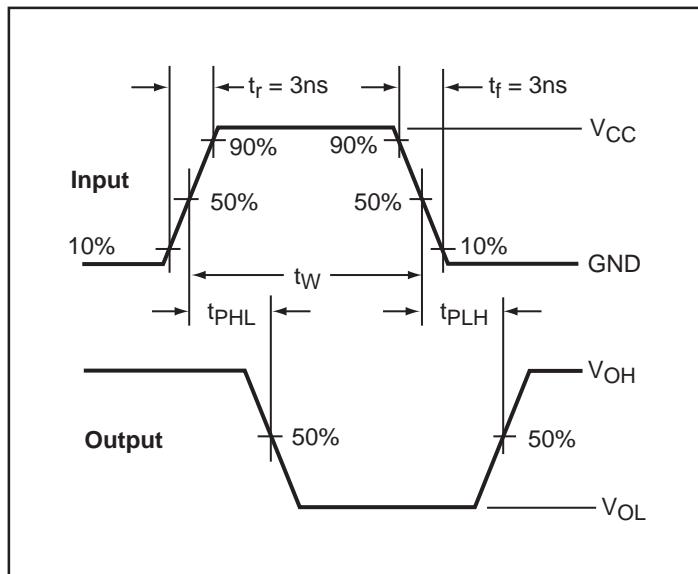


Figure 3. AC Waveforms

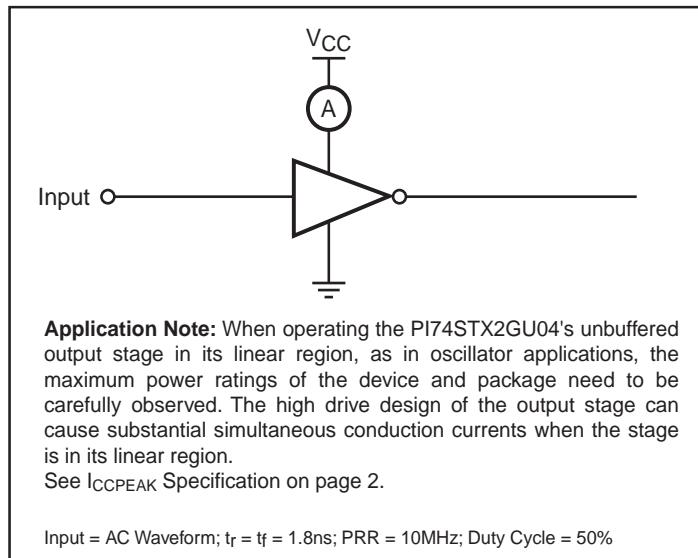
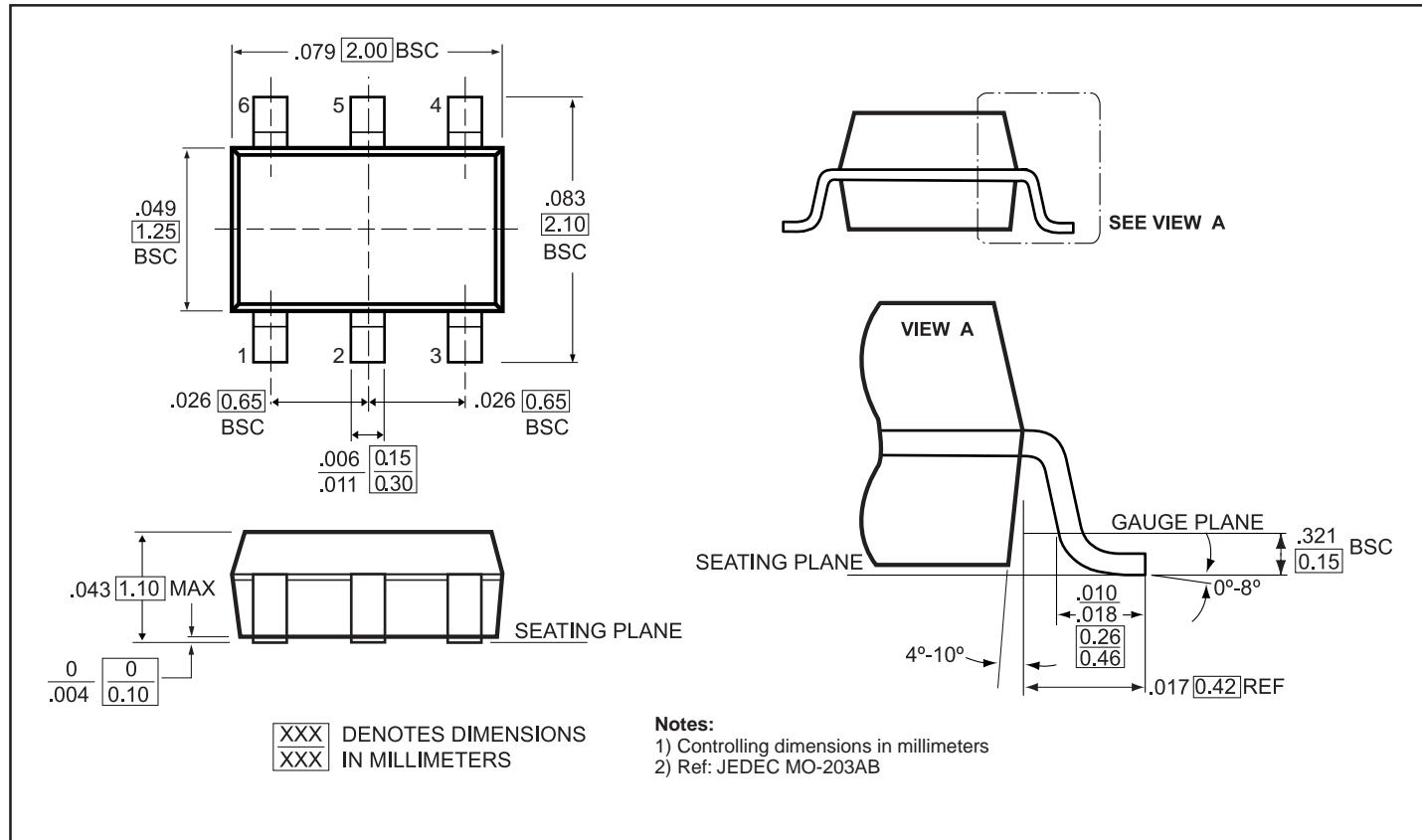


Figure 2. ICCD Test Circuit

Packaging Mechanical: 6-Pin SC70 (C)



Thermal Characteristics can be found on the web: <http://www.pericom.com/packaging/mechanicals.php>

Ordering Information

Part	Pin-Package	Top Marking	Operating Range
PI74STX2GU04CX	6-Pin - SC70 (C)	NU4	-40°C to 85°C