

3.3V Hex inverter Schmitt trigger

74LVT14

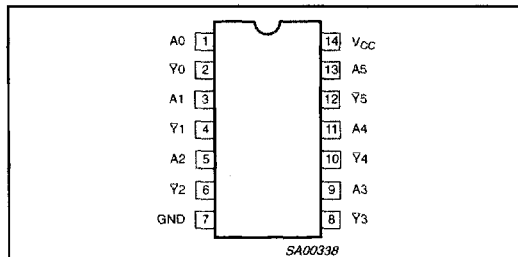
DESCRIPTION

The 74LVT14 is a high-performance BiCMOS product designed for V_{CC} operation at 3.3V. They are capable of transforming slowly changing input signals into sharply defined, jitter free output signals. In addition, they have greater noise margin than conventional inverters. Each circuit contains a Schmitt trigger followed by a Darlington level shifter and a phase splitter driving a TTL totem-pole output. The Schmitt trigger uses positive feedback to effectively speed-up slow input transitions, and provide different input threshold voltages for positive-going and negative-going input threshold (typically 600mV) is determined internally by resistor ratios and is insensitive to temperature and supply voltage variations.

QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS $T_{amb} = 25^{\circ}C$; $GND = 0V$	TYPICAL	UNIT
t_{PLH} t_{PHL}	Propagation delay A_n to \bar{Y}_n	$C_L = 50pF$; $V_{CC} = 3.3V$	3.2 3.0	ns
C_{IN}	Input capacitance	$V_I = 0V$ or $3.0V$	3	pF
I_{CCL}	Total supply current	Outputs low; $V_{CC} = 3.6V$	1.5	mA

PIN CONFIGURATION



PIN DESCRIPTION

PIN NUMBER	SYMBOL	NAME AND FUNCTION
1, 3, 5, 9, 11, 13	A_n	Data inputs
2, 4, 6, 8, 10, 12	\bar{Y}_n	Data outputs
7	GND	Ground (0V)
14	V_{CC}	Positive supply voltage

ORDERING INFORMATION

PACKAGES	TEMPERATURE RANGE	OUTSIDE NORTH AMERICA	NORTH AMERICA	DWG NUMBER
14-Pin Plastic SO	-40°C to +85°C	74LVT14 D	74LVT14 D	SOT108-1
14-Pin Plastic SSOP	-40°C to +85°C	74LVT14 DB	74LVT14 DB	SOT337-1
14-Pin Plastic TSSOP	-40°C to +85°C	74LVT14 PW	74LVT14 PWDH	SOT402-1

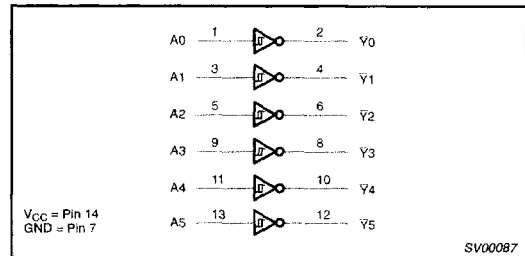
FUNCTION TABLE

INPUTS	OUTPUT
D_n	\bar{Q}_n
L	H
H	L

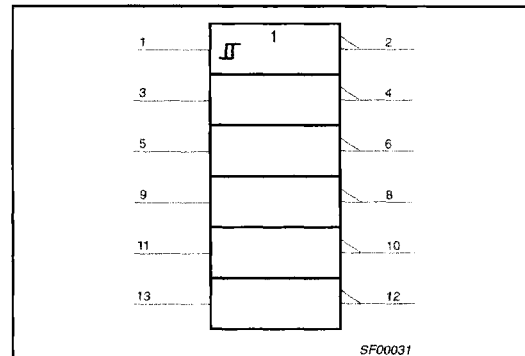
NOTES:

H = High voltage level
L = Low voltage level

LOGIC DIAGRAM



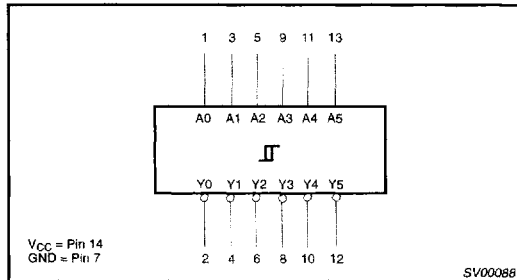
IEC/IEEE SYMBOL



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LOGIC SYMBOL

ABSOLUTE MAXIMUM RATINGS^{1, 2}

SYMBOL	PARAMETER	CONDITIONS	RATING	UNIT
V _{CC}	DC supply voltage		-0.5 to +4.6	V
I _{IK}	DC input diode current	V _I < 0	-50	mA
V _I	DC input voltage ³		-0.5 to +7.0	V
I _{OK}	DC output diode current	V _O < 0	-50	mA
V _{OUT}	DC output voltage ³	Output in Off or High state	-0.5 to +7.0	V
I _{OUT}	DC output current	Output in High state	-32	mA
		Output in Low state	64	
T _{stg}	Storage temperature range		-65 to 150	°C

NOTES:

- Stresses beyond those listed may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
- The performance capability of a high-performance integrated circuit in conjunction with its thermal environment can create junction temperatures which are detrimental to reliability. The maximum junction temperature of this integrated circuit should not exceed 150°C.
- The input and output negative voltage ratings may be exceeded if the input and output clamp current ratings are observed.

RECOMMENDED OPERATING CONDITIONS

SYMBOL	PARAMETER	LIMITS		UNIT
		MIN	MAX	
V _{CC}	DC supply voltage	2.7	3.6	V
V _I	Input voltage	0	5.5	V
V _{IH}	High-level input voltage	2.0		V
V _{IL}	Low-level Input voltage		0.8	V
I _{OH}	High-level output current		-20	mA
I _{OL}	Low-level output current		32	mA
Δt/Δv	Input transition rise or fall rate; Outputs enabled		10	ns/V
T _{amb}	Operating free-air temperature range	-40	+85	°C

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DC ELECTRICAL CHARACTERISTICS

Over recommended operating conditions

Voltages are referenced to GND (ground = 0V)

SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			UNIT
			Temp = -40°C to +85°C			
			MIN	TYP ¹	MAX	
V_{T+}	Positive-going threshold	$V_{CC} = 3.3V$	1.5	1.7	2.0	V
V_{T-}	Negative-going threshold	$V_{CC} = 3.3V$	0.9	1.1	1.3	V
ΔV_T	Hysteresis	$V_{CC} = 3.3V$	0.4	0.6		V
V_{IK}	Input clamp voltage	$V_{CC} = 2.7V; I_{IK} = -18mA$			-1.2	V
V_{OH}	High-level output voltage	$V_{CC} = 2.7$ to $3.6V; I_{OH} = -100\mu A$	$V_{CC}-0.2$			V
		$V_{CC} = 2.7V; I_{OH} = -6mA$	2.4			
		$V_{CC} = 3.0V; I_{OH} = -20mA$	2.0			
V_{OL}	Low-level output voltage	$V_{CC} = 2.7V; I_{OL} = 100\mu A$	0.2			V
		$V_{CC} = 2.7V; I_{OL} = 24mA$	0.5			
		$V_{CC} = 3.0V; I_{OL} = 32mA$	0.5			
I_I	Input leakage current	$V_{CC} = 0$ or $3.6V; V_I = 5.5V$	10			μA
		$V_{CC} = 3.6V; V_I = V_{CC}$ or GND	± 1			
I_{OFF}	Output off current	$V_{CC} = 0V; V_I$ or $V_O = 0$ to $4.5V$	± 100			μA
I_{CCH}	Quiescent supply current	$V_{CC} = 3.6V; \text{Outputs High, } V_I = \text{GND or } V_{CC}, I_O = 0$	0.02			mA
I_{CCL}		$V_{CC} = 3.6V; \text{Outputs Low, } V_I = \text{GND or } V_{CC}, I_O = 0$	1.5			
ΔI_{CC}	Additional supply current per input pin ²	$V_{CC} = 3V$ to $3.6V; \text{One input at } V_{CC}-0.6V, \text{Other inputs at } V_{CC}$ or GND	0.2			μA
C_I	Input capacitance	$V_I = 3V$ or 0	3			pF

NOTES:

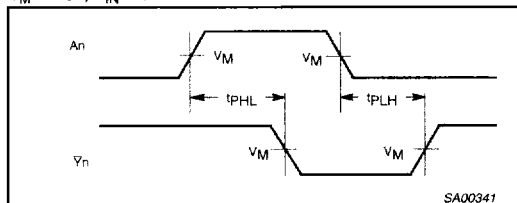
1. All typical values are at $V_{CC} = 3.3V$ and $T_{amb} = 25^\circ C$.
2. This is the increase in supply current for each input at the specified voltage level other than V_{CC} or GND

AC ELECTRICAL CHARACTERISTICS

SYMBOL	PARAMETER	TEST CONDITION	LIMITS				UNIT
			$V_{CC} = 3.3V \pm 0.3V$			$V_{CC} = 2.7V$	
			MIN	TYP	MAX	MAX	
t_{PLH} t_{PHL}	Propagation delay A_n to \bar{Y}_n	Waveform 1	1.0	3.8	5.7	6.9	ns
			1.0	3.2	4.5	4.1	

AC WAVEFORMS

$V_M = 1.5V, V_{IN} = \text{GND to } 2.7V$



Waveform 1. Propagation delay for inverting outputs

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TEST CIRCUIT AND WAVEFORMS

Test Circuit for Outputs

$V_M = 1.5V$
Input Pulse Definition

DEFINITIONS

R_L = Load resistor; see AC CHARACTERISTICS for value.

C_L = Load capacitance includes jig and probe capacitance; see AC CHARACTERISTICS for value.

R_T = Termination resistance should be equal to Z_{OUT} of pulse generators.

SV00022

FAMILY	INPUT PULSE REQUIREMENTS				
	Amplitude	Rep. Rate	t_w	t_r	t_f
74LVT	2.7V	$\leq 10\text{MHz}$	500ns	$\leq 2.5\text{ns}$	$\leq 2.5\text{ns}$