TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7PG17FU

#### **Dual Schmitt Buffer**

#### **Features**

High output current : ±8 mA (min) at V<sub>CC</sub> = 3 V

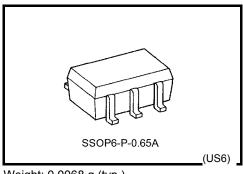
• Super high speed operation : t<sub>pd</sub> = 3.7 ns (typ.)

at  $V_{CC} = 3.3 \text{ V}, 15 \text{pF}$ 

• Operating voltage range :V<sub>CC</sub> = 0.9 to 3.6 V

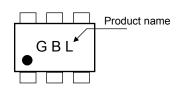
• 5.5-V tolerant inputs

• 3.6-V power down protection outputs

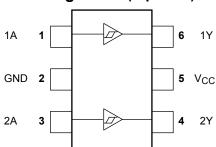


Weight: 0.0068 g (typ.)

## Marking



#### Pin Assignment (top view)



## **Absolute Maximum Ratings (Ta = 25°C)**

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	-0.5 to 4.6	V
DC input voltage	V <sub>IN</sub>	-0.5 to 7.0	V
DC output voltage	\/	-0.5 to 4.6 (Note 1)	V
	V <sub>OUT</sub>	$-0.5 \text{ to V}_{CC} + 0.5  \text{(Note 2)}$	\ \ \
Input diode current	l <sub>IK</sub>	-20	mA
Output diode current	lok	-20 (Note 3)	mA
DC output current	lout	±25	mA
DC V <sub>CC</sub> /GND current	Icc	±100	mA
Power dissipation	P <sub>D</sub>	200	mW
Storage temperature	T <sub>stg</sub>	-65 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1:  $V_{CC} = 0 V$ 

Note 2: High or Low State. Do not exceed I<sub>OUT</sub> of absolute maximum ratings.

Note 3: V<sub>OUT</sub> < GND

# **IEC Logic Symbol**



### **Truth Table**

А	Y
L	L
Н	Н

## **Operating Ranges**

Characteristics	Symbol	Rating	Unit			
Supply voltage	V <sub>CC</sub>	0.9 to 3.6	V			
Input voltage	V <sub>IN</sub>	0 to 5.5	V			
Output voltage	V	0 to 3.6 (Note 4)	V			
	V <sub>OUT</sub>	0 to V <sub>CC</sub> (Note 5)	V			
Output Current	IOH / IOL	±8.0 (Note 6)				
		±4.0 (Note 7)				
		±3.0 (Note 8)	mA			
		±1.7 (Note 9)	IIIA			
		±0.3 (Note 10)				
		±0.02 (Note 11)				
Operating temperature	T <sub>opr</sub>	-40 to 85	°C			

Note 4:  $V_{CC} = 0V$ 

Note 5: High or Low State. I<sub>OUT</sub> absolute maximum rating must be observed.

Note 6:  $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$ 

Note 7:  $V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$ 

Note 8:  $V_{CC} = 1.65 \text{ to } 1.95 \text{ V}$ 

Note 9:  $V_{CC} = 1.4 \text{ to } 1.6 \text{ V}$ 

Note 10:  $V_{CC} = 1.1 \text{ to } 1.3 \text{ V}$ 

Note 11:  $V_{CC} = 0.9 \text{ V}$ 

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## **Electrical Characteristics**

### **DC Characteristics**

Characteristics Symbol Test Condition		T4	Took Oom diking		7	Га = 25°(	)	Ta = -40 to 85°C		11-4	
		V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit			
Positive					0.9	_	_	0.73	_	0.80	_
				1.1	_	_	0.86	_	0.93		
				1.4	_	_	1.07	_	1.12	-	
	threshold voltage	V <sub>P</sub>	_		1.65	_	_	1.23	_	1.25	
					2.3	_	_	1.66	_	1.68	
Threshold				3.0		_	2.14	_	2.15	V	
Voltage					0.9	0.18			0.07		v
					1.1	0.26	_	_	0.18	_	
	Negative	V/			1.4	0.36	_	_	0.31	_	
	threshold voltage	V <sub>N</sub>	_		1.65	0.45	_	_	0.41	_	
					2.3	0.69	_	_	0.64		
			3.0	0.96	_	_	0.91				
						0.20	_	0.38	0.15	0.53	
					1.1	0.25		0.41	0.21	0.53	
Hysteresis Vol	tane	V <sub>H</sub>	_		1.4	0.35		0.48	0.34	0.57	V
Trysteresis voi	lage	VН			1.65	0.42		0.56	0.40	0.60	
					2.3	0.60		0.74	0.60	0.76	
					3.0	0.79		0.93	0.79	0.94	
				I <sub>OH</sub> =-0.02 mA	0.9	0.75	_	_	0.75	_	
			$I_{OH} = -0.3 \text{ mA}$	1.1 to 1.3	V <sub>CC</sub> × 0.75	_	_	V <sub>CC</sub> × 0.75	_		
Output voltage	High level	ligh level V <sub>OH</sub>	V <sub>IN</sub> = V <sub>IH</sub>	I <sub>OH</sub> = -1.7 mA	1.4 to 1.6	V <sub>CC</sub> × 0.75	_	_	V <sub>CC</sub> × 0.75	_	
			$I_{OH} = -3.0 \text{ mA}$	1.65 to 1.95	V <sub>CC</sub> -0.45	_	_	V <sub>CC</sub> -0.45	_		
		$I_{OH} = -4.0 \text{ mA}$	2.3 to 2.7	2.0	_	_	2.0	_	V		
		$I_{OH} = -8.0 \text{ mA}$	3.0 to 3.6	2.48	_	_	2.48	_			
			$I_{OL} = 0.02 \text{ mA}$	0.9	_	_	0.1	_	0.1		
			I <sub>OL</sub> = 0.3 mA	1.1 to 1.3	_	_	V <sub>CC</sub> × 0.25		V <sub>CC</sub> × 0.25		
	Low level	v level V <sub>OL</sub> V <sub>IN</sub> =	$V_{IN} = V_{IL}$	I <sub>OL</sub> = 1.7 mA	1.4 to 1.6	_	_	V <sub>CC</sub> × 0.25	_	V <sub>CC</sub> × 0.25	
			I <sub>OL</sub> = 3.0 mA	1.65 to 1.95		1	0.45		0.45		
				I <sub>OL</sub> = 4.0 mA	2.3 to 2.7	_	_	0.4	_	0.4	]
				$I_{OL} = 8.0 \text{ mA}$	3.0 to 3.6	_	_	0.4	_	0.4	
Input leakage current I <sub>IN</sub> V <sub>IN</sub> = 0 to 5.5V		0 to 3.6	_	_	±0.1	_	±1.0	μА			
Power off leak	Power off leakage current I <sub>OFF</sub> V <sub>IN</sub> or N		V <sub>IN</sub> = 5.5V or V <sub>OUT</sub> =	$V_{IN} = 5.5V$ or $V_{OUT} = 3.6V$		_	_	1.0	_	10.0	μА
Quiescent sup	ply current	I <sub>CC</sub>	$V_{IN} = V_{CC}$	$V_{IN} = V_{CC}$ or GND 3.6		_	_	1.0	_	10.0	μА

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## AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition		Ta = 25°C		Ta = -40 to 85°C		Unit	
Characteristics			V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit
		$C_L = 10 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	27.3	_	_	_	ns
			1.1 to 1.3	_	13.0	24.5	1.0	39.2	
			1.4 to 1.6	_	7.5	11.8	1.0	12.7	
			1.65 to 1.95		6.0	8.5	1.0	9.0	
			2.3 to 2.7		4.3	5.4	1.0	5.8	
			3.0 to 3.6		3.5	4.4	1.0	4.6	
		$C_L$ = 15 pF, $R_L$ = 1 M $\Omega$	0.9		29.5	_	_	_	
Propagation delay time	<sup>t</sup> pLH <sup>t</sup> pHL		1.1 to 1.3		14.3	26.7	1.0	44.7	
			1.4 to 1.6		8.0	12.7	1.0	14.0	
			1.65 to 1.95		6.3	9.1	1.0	9.5	
			2.3 to 2.7		4.6	5.7	1.0	6.1	
			3.0 to 3.6		3.7	4.6	1.0	5.0	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9		40.5	_	_	_	
			1.1 to 1.3		19.6	35.7	1.0	58.1	
			1.4 to 1.6		10.7	15.8	1.0	17.6	
			1.65 to 1.95		7.8	10.7	1.0	11.7	
			2.3 to 2.7		5.4	6.9	1.0	8.1	
			3.0 to 3.6		4.3	5.2	1.0	6.1	
Input capacitance	C <sub>IN</sub>	_	3.6		3	_	_	_	pF
Power dissipation capacitance	C <sub>PD</sub>	(Note 12)	0.9 to 3.6	_	9	_	_	_	pF

Note 12: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

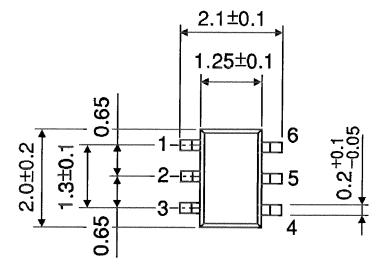
 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$ 

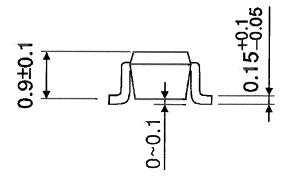
# **Package Dimensions**

SSOP6-P-0.65A

Unit: mm

TC7PG17FU





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Weight: 0.0068 g (typ.)

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