

TYPES SN54ALS1620 THRU SN54ALS1623 SN74ALS1620 THRU SN74ALS1623 OCTAL BUS TRANSCEIVERS

D2661, DECEMBER 1982—REVISED DECEMBER 1983

- Bus Transceivers in High-Density 20-Pin DIPs and the New Plastic and Ceramic Chip Carriers Packages
- Local Bus Latch Capability
- Choice of True or Inverting Logic
- Dependable Texas Instruments Quality and Reliability
- Choice of 3-State or Open-Collector Outputs

DEVICE	OUTPUT	LOGIC
'ALS1620	3-State	Inverting
'ALS1621	Open-Collector	True
'ALS1622	Open-Collector	Inverting
'ALS1623	3-State	True

description

These octal bus transceivers are designed for asynchronous two-way communication between data buses. The control function implementation allows for maximum flexibility in timing.

These devices allow data transmission from A bus to the B bus or from the B bus to the A bus depending upon the logic levels at the enable inputs ($\overline{\text{GBA}}$ and GAB).

The enable inputs can be used to disable the device so that the buses are effectively isolated.

The dual-enable configuration gives the 'ALS1620 thru 'ALS1623 the capability to store data by simultaneous enabling of $\overline{\text{GBA}}$ and GAB. Each output reinforces its input in this transceiver configuration. Thus, when both control inputs are enabled and all other data sources to the two sets of bus lines are at high impedance, both sets of bus lines (16 in all) will remain at their last states. The 8-bit codes appearing on the two sets of buses will be identical for the 'ALS1621 and 'ALS1623 or complementary for the 'ALS1620 and 'ALS1622.

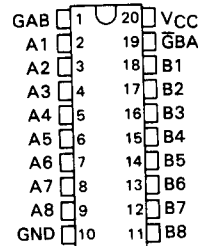
The -1 versions of the SN74ALS' parts are identical to the standard versions except that the recommended maximum $|I_{OL}|$ is increased to 24 mA. There are no -1 versions of the SN54ALS' parts.

The SN54ALS1620 thru SN54ALS1623 are characterized for operation over the full military temperature range of -55°C to 125°C . The SN74ALS1620 thru SN74ALS1623 are characterized for operation from 0°C to 70°C .

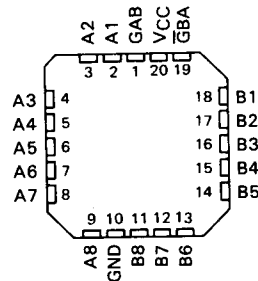
FUNCTION TABLE

ENABLE INPUTS		OPERATION	
GBA	GAB	'ALS1620, 'ALS1622	'ALS1621, 'ALS1623
L	L	$\overline{\text{B}}$ data to A bus	B data to A bus
H	H	$\overline{\text{A}}$ data to B bus	A data to B bus
H	L	Isolation	Isolation
L	H	$\overline{\text{B}}$ data to A bus, $\overline{\text{A}}$ data to B bus	B data to A bus, A data to B bus

SN54ALS' . . . J PACKAGE
SN74ALS' . . . N PACKAGE
(TOP VIEW)



SN54ALS' . . . FH PACKAGE
SN74ALS' . . . FN PACKAGE
(TOP VIEW)

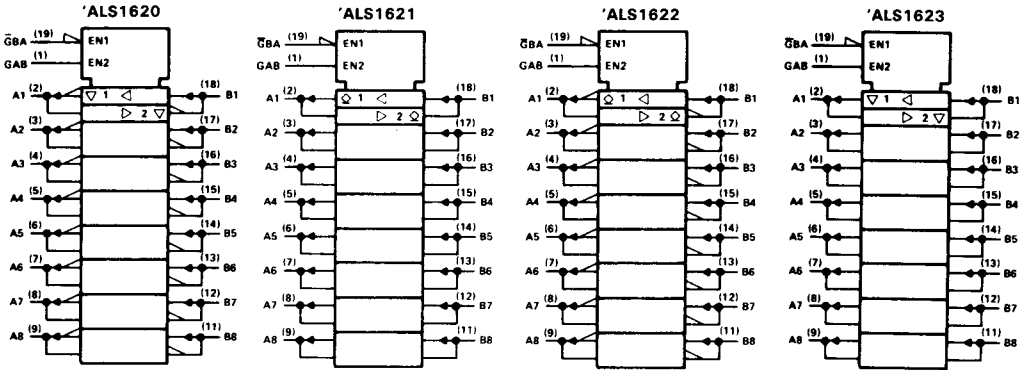


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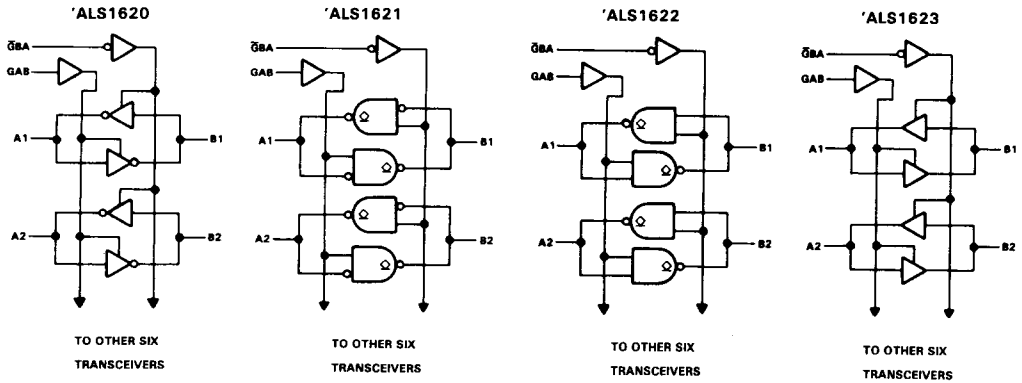
ALS AND AS CIRCUITS

TYPES SN54ALS1620 THRU SN54ALS1623 SN74ALS1620 THRU SN74ALS1623 OCTAL BUS TRANSCEIVERS

logic symbols



functional block diagrams (positive logic)



Pin numbers shown are for J and N packages.

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**TYPES SN54ALS1620 THRU SN54ALS1623
SN74ALS1620 THRU SN74ALS1623
OCTAL BUS TRANSCEIVERS**

'ALS1620 switching characteristics (see Note 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V,}$ $C_L = 50 \text{ pF,}$ $R_1 = 500 \Omega,$ $R_2 = 500 \Omega,$ $T_A = \text{MIN to MAX}$						UNIT
			SN54ALS1620			SN74ALS1620			
			MIN	TYP†	MAX	MIN	TYP†	MAX	
t _{PLH}	A	B	9			9			ns
t _{PHL}			6			6			
t _{PLH}	B	A	9			9			ns
t _{PHL}			6			6			
t _{PZH}	\bar{G} BA	A	14			14			ns
t _{PZL}			17			17			
t _{PHZ}	\bar{G} BA	A	7			7			ns
t _{PLZ}			11			11			
t _{PZH}	GAB	B	14			14			ns
t _{PZL}			17			17			
t _{PHZ}	GAB	B	7			7			ns
t _{PLZ}			11			11			

'ALS1623 switching characteristics (see Note 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V,}$ $C_L = 50 \text{ pF,}$ $R_1 = 500 \Omega,$ $R_2 = 500 \Omega,$ $T_A = \text{MIN to MAX}$						UNIT
			SN54ALS1623			SN74ALS1623			
			MIN	TYP†	MAX	MIN	TYP†	MAX	
t _{PLH}	A	B	8			8			ns
t _{PHL}			8			8			
t _{PLH}	B	A	8			8			ns
t _{PHL}			8			8			
t _{PZH}	\bar{G} BA	A	18			18			ns
t _{PZL}			21			21			
t _{PHZ}	\bar{G} BA	A	12			12			ns
t _{PLZ}			13			13			
t _{PZH}	GAB	B	18			18			ns
t _{PZL}			21			21			
t _{PHZ}	GAB	B	12			12			ns
t _{PLZ}			13			13			

†All typical values are at $V_{CC} = 5 \text{ V, } T_A = 25^\circ\text{C.}$

NOTE 1: For load circuit and voltage waveforms, see page 1-12.

Additional information on these products can be obtained from the factory as it becomes available.

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ALS AND AS CIRCUITS

**TYPES SN54ALS1620 THRU SN54ALS1623
SN74ALS1620 THRU SN74ALS1623
OCTAL BUS TRANSCEIVERS**

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, V_{CC}	7 V
Input voltage: All inputs and I/O ports	7 V
Operating free-air temperature range: SN54ALS1621, SN54ALS1622	-55°C to 125°C
SN74ALS1621, SN74ALS1622	0°C to 70°C
Storage temperature range	-65°C to 150°C

recommended operating conditions

		SN54ALS1621			SN74ALS1621			UNIT	
		SN54ALS1622			SN74ALS1622				
		MIN	NOM	MAX	MIN	NOM	MAX		
V_{CC}	Supply voltage	4.5	5	5.5	4.5	5	5.5	V	
V_{IH}	High-level input voltage	2			2			V	
V_{IL}	Low-level input voltage				0.8			V	
V_{OH}	High-level output voltage				5.5			mV	
I_{OL}	Low-level output current				8			mA	
					16				
T_A	Operating free-air temperature				24 [†]			°C	
		-55			125				
					0				
					70				

[†]The extended limits apply only if V_{CC} is maintained between 4.75 V and 5.25 V.
The 24-mA limit applies for the SN74ALS1621-1 and SN74ALS1622-1 only.

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS	SN54ALS1621		SN74ALS1621		UNIT
		SN54ALS1622		SN74ALS1622		
		MIN	TYP [‡]	MAX	MIN	
V_{IK}	$V_{CC} = 4.5$ V, $I_I = -18$ mA			-1.5		V
I_{OH}	$V_{CC} = 4.5$ V, $V_{OH} = 5.5$ V			0.1		mA
V_{OL}	$V_{CC} = 4.5$ V, $I_{OL} = 8$ mA	0.25		0.4		V
	$V_{CC} = 4.5$ V, $I_{OL} = 16$ mA ($I_{OL} = 24$ mA for -1 versions)			0.35		
I_I	Control inputs	$V_{CC} = 5.5$ V, $V_I = 7$ V		0.1		mA
	A or B ports	$V_{CC} = 5.5$ V, $V_I = 5.5$ V		0.1		
I_{IH}	Control inputs	$V_{CC} = 5.5$ V, $V_I = 2.7$ V		20		μA
	A or B ports [§]			20		
I_{IL}	Control inputs	$V_{CC} = 5.5$ V, $V_I = 0.4$ V		-0.1		mA
	A or B ports [§]			-0.1		
I_{CC}	'ALS1621	$V_{CC} = 5.5$ V	Outputs high	11		mA
			Outputs low	16		
			Outputs high	13		
			Outputs low	18		
'ALS1622	$V_{CC} = 5.5$ V	Outputs high	11		mA	
		Outputs low	16			
		Outputs high	13			
		Outputs low	18			

[‡]All typical values are at $V_{CC} = 5$ V, $T_A = 25$ °C.

[§]For I/O ports, the parameters I_{IH} and I_{IL} include the off-state output current.

Additional information on these products can be obtained from the factory as it becomes available.

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**TYPES SN54ALS1620 THRU SN54ALS1623
SN74ALS1620 THRU SN74ALS1623
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'ALS1621 switching characteristics (see Note 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V,}$ $C_L = 50 \text{ pF,}$ $R_L = 680 \Omega,$ $T_A = \text{MIN to MAX}$						UNIT
			SN54ALS1621			SN74ALS1621			
			MIN	TYP [‡]	MAX	MIN	TYP [‡]	MAX	
t _{PLH}	A	B	22			22			ns
t _{PHL}			14			14			
t _{PLH}	B	A	22			22			ns
t _{PHL}			14			14			
t _{PLH}	\bar{G} BA	A	33			33			ns
t _{PHL}			24			24			
t _{PLH}	GAB	B	33			33			ns
t _{PHL}			24			24			

'ALS1622 switching characteristics (see Note 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V,}$ $C_L = 50 \text{ pF,}$ $R_L = 680 \Omega,$ $T_A = \text{MIN to MAX}$						UNIT
			SN54ALS1622			SN74ALS1622			
			MIN	TYP [‡]	MAX	MIN	TYP [‡]	MAX	
t _{PLH}	A	B	25			25			ns
t _{PHL}			13			13			
t _{PLH}	B	A	25			25			ns
t _{PHL}			13			13			
t _{PLH}	\bar{G} BA	A	31			31			ns
t _{PHL}			28			28			
t _{PLH}	GAB	B	31			31			ns
t _{PHL}			28			28			

[‡]All typical values are at $V_{CC} = 5 \text{ V, } T_A = 25^\circ\text{C.}$

NOTE 1: For load circuit and voltage waveforms, see page 1-12.

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