

**OCTAL BUFFER/LINE DRIVER WITH
3-STATE OUTPUTS**

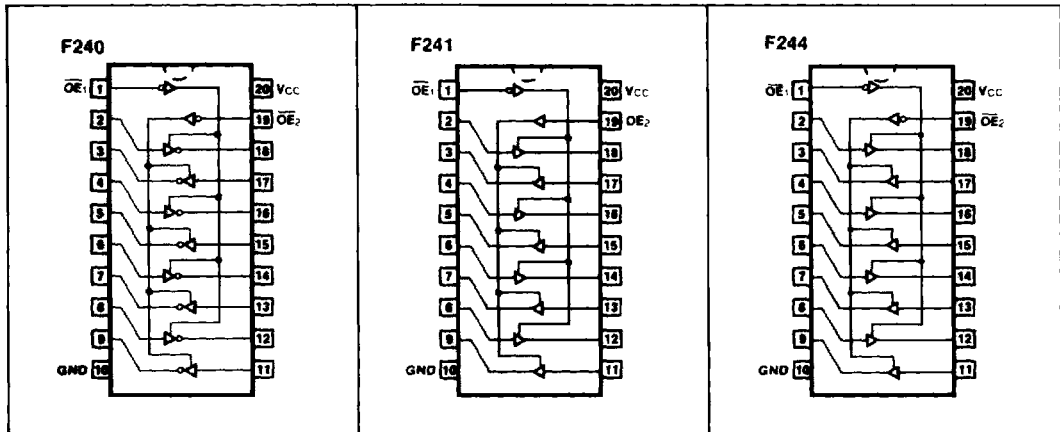
DESCRIPTION — The F240, F241 and F244 are octal buffers and line drivers designed to be employed as memory address drivers, clock drivers and bus oriented transmitters/receivers which provide improved PC board density.

- 3-STATE OUTPUTS DRIVE BUS LINES OR BUFFER MEMORY ADDRESS REGISTERS
- OUTPUTS SINK 64 mA
- 15 mA SOURCE CURRENT
- INPUT CLAMP DIODES LIMIT HIGH-SPEED TERMINATION EFFECTS

MC54F/74F240
MC54F/74F241
MC54F/74F244

**OCTAL BUFFER/LINE DRIVER
with 3-STATE OUTPUTS**

FAST™ SCHOTTKY TTL

CONNECTION DIAGRAMS


J Suffix — Case 732-03 (Ceramic)
N Suffix — Case 738-03 (Plastic)
DW Suffix — Case 751D-03 (SOIC)

TRUTH TABLES

F240

INPUTS			OUTPUT
\overline{OE}_1	\overline{OE}_2	D	
L	L	H	H
L	H	L	L
H	X	Z	Z

F241

INPUTS			OUTPUT
\overline{OE}_1	\overline{OE}_2	D	
L	H	L	L
L	H	H	H
H	L	X	Z

F244

INPUTS			OUTPUT
\overline{OE}_1	\overline{OE}_2	D	
L	L	L	L
L	H	H	H
H	X	Z	Z

H = HIGH Voltage Level

L = LOW Voltage Level

X = Immaterial

Z = High Impedance

GUARANTEED OPERATING RANGES

SYMBOL	PARAMETER		MIN	TYP	MAX	UNIT
V _{CC}	Supply Voltage	54, 74	4.50	5.0	5.50	V
T _A	Operating Ambient Temperature Range	54 74	-55 0	25 25	125 70	°C
I _{OH}	Output Current — High	54 74			-12 -15	mA
I _{OL}	Output Current — Low	54 74			48 64	mA

DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

SYMBOL	PARAMETER		LIMITS			UNITS	TEST CONDITIONS	
			MIN	TYP	MAX			
V _{IH}	Input HIGH Voltage		2.0			V	Guaranteed Input HIGH Voltage	
V _{IL}	Input LOW Voltage				0.8	V	Guaranteed Input LOW Voltage	
V _{IK}	Input Clamp Diode Voltage				-1.2	V	I _{IN} = -18 mA	V _{CC} = MIN
V _{OH}	Output HIGH Voltage	54, 74	2.4	3.4		V	I _{OH} = -3.0 mA	V _{CC} = 4.50 V
		74	2.7	3.4		V	I _{OH} = -3.0 mA	V _{CC} = 4.75 V
		54	2.0			V	I _{OH} = -12 mA	V _{CC} = 4.50 V
		74	2.0			V	I _{OH} = -15 mA	V _{CC} = 4.5 V
V _{OL}	Output LOW Voltage	54			0.55	V	I _{OL} = 48 mA	V _{CC} = MIN
		74			0.55	V	I _{OL} = 64 mA	
I _{OZH}	Output Off Current HIGH				50	μA	V _{OUT} = 2.7 V	V _{CC} = MAX
I _{OZL}	Output Off Current LOW				-50	μA	V _{OUT} = 0.5 V	V _{CC} = MAX
I _{IH}	Input HIGH Current				20	μA	V _{IN} = 2.7 V	V _{CC} = MAX
					100		V _{IN} = 7.0 V	
I _{IL}	Input LOW Current	Data Inputs F241, F244			-1.6	mA	V _{IN} = 0.5 V	V _{CC} = MAX
		Other			-1.0			
I _{OS}	Output Drive Current Note 2	54	-100		-275	mA	V _{OUT} = GND	V _{CC} = MAX
		74	-100		-275			
I _{CCH}	Power Supply Current HIGH	F240			35	mA	V _{CC} = MAX	
		F241, F244			60			
I _{CCL}	Power Supply Current LOW	F240			75			
		F241, F244			90			
I _{CCZ}	Power Supply Current OFF	F240			75			
		F241, F244			90			

NOTES:

1. For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions for the applicable device type.
2. Not more than one output should be shorted at a time, nor for more than 1 second.

**F240
AC CHARACTERISTICS**

SYMBOL	PARAMETER	54/74F T _A = +25°C V _{CC} = +5.0 V C _L = 50 pF			54F T _A = -55 to +125°C V _{CC} = 5.0 V ± 10% C _L = 50 pF		74F T _A = 0 to +70°C V _{CC} = 5.0 V ± 10% C _L = 50 pF		UNITS
		MIN	TYP	MAX	MIN	MAX	MIN	MAX	
^t PLH ^t PHL	Propagation Delay, Data to Output	2.5 1.5	5.1 3.5	7.0 4.7	2.5 1.5	9.0 6.0	2.5 1.5	8.0 5.7	ns
^t PZH ^t PZL	Output Enable Time	2.0 4.0	3.5 6.9	5.2 9.0	2.0 4.0	6.5 13.5	2.0 4.0	5.7 10	ns
^t PHZ ^t PLZ	Output Disable Time	2.0 2.0	4.0 6.0	5.3 8.0	2.0 2.0	6.5 12.5	2.0 2.0	6.3 9.5	ns

**F241
AC CHARACTERISTICS**

^t PLH ^t PHL	Propagation Delay, Data to Output	2.5 2.5	4.0 4.0	5.2 5.2	2.0 2.0	6.5 7.0	2.5 2.5	6.2 6.5	ns
^t PZH ^t PZL	Output Enable Time	2.0 2.0	4.3 5.4	5.7 7.0	2.0 2.0	7.0 8.5	2.0 2.0	6.7 8.0	ns
^t PHZ ^t PLZ	Output Disable Time	2.0 2.0	4.5 4.5	6.0 6.5	2.0 2.0	7.0 12.5	2.0 2.0	7.0 7.5	ns

**F244
AC CHARACTERISTICS**

^t PLH ^t PHL	Propagation Delay, Data to Output	2.5 2.5	4.0 4.0	5.2 5.2	2.5 2.5	6.5 7.0	2.5 2.5	6.2 6.5	ns
^t PZH ^t PZL	Output Enable Time	2.0 2.0	4.3 5.4	5.7 7.0	2.0 2.0	7.0 8.5	2.0 2.0	6.7 8.0	ns
^t PHZ ^t PLZ	Output Disable Time	2.0 2.0	4.5 4.5	6.0 6.0	2.0 2.0	7.0 10.0	2.0 2.0	7.0 7.0	ns