

Digital Circuits

930 Series DTL

Type ¹ Number	Description	Fanout Function	TYPICAL CHARACTERISTICS			Available Packages							
			Tpd (ns) or Toggle Rate (Min)	Avg. Pwr. Function (mW) 50% Duty	DC Noise Margin (V)	14 Pin							
						C	D	DB	DC	DP	7		
RM/RC930	Dual 4 NAND/NOR gate with nodes ³	8	30	8.5/gate	0.5	X	X	X	X	X			
RM/RC932	Dual 4 NAND/NOR buffer	22	40	28/gate	0.5	X	X	X	X	X			
RM/RC933	Dual four expander	NA	NA	NA	NA	X	X	X	X	X			
RM/RC934	Hex Inverter ³	8	30	8.5/gate	0.5	X	X	X	X	X			
RM/RC935	Hex Inverter ³	8	30	7/gate	0.5	X	X	X		X			
RM/RC936	Hex Inverter ²	8	30	8.5/gate	0.5	X	X	X		X			
RM/RC937	Hex Inverter ⁴	8	30	8.5/gate	0.5	X	X	X	X	X			
RM/RC940	Hex Inverter	8	30	8.5/gate	0.5	X	X	X	X	X			
RM/RC941	Hex Inverter	8	30	8.5/gate	0.5	X	X	X		X			
RM/RC944	Dual 4 NAND/NOR power gate	25	27	22/gate	0.5	X	X	X		X			
RM/RC945	Clocked Flip-Flop ³	9	52	35	0.5	X	X	X		X			
RM/RC946	Quad two NAND/NOR gate ³	8	30	8.5/gate	0.5	X	X	X	X	X			
RM/RC948	Clocked Flip-Flop ⁴	9	50	40	0.5	X	X	X		X			
RM/RC949	Quad two NAND/NOR gate ⁴	7	25	8.5/gate	0.5	X	X	X		X			
RM/RC950	Pulse triggered binary flip-flop	8	30	22	0.5	X	X	X	X	X			
RM/RC951	Monostable multivibrator	9	50	35	—	X	X	X	X	X			
RM/RC957	Quad 2 input buffers	22	40	28/gate	0.5	X	X	X		X			
RM/RC958	Quad 2 NAND power gates	25	27	22/gate	0.5	X	X	X		X			
RM/RC961	Dual 4 NAND/NOR gate with nodes ⁴	7	25	8.5/gate	0.5	X	X	X		X			
RM/RC962	Triple three NAND/NOR gate ³	8	30	8.5/gate	0.5	X	X	X	X	X			
RM/RC963	Triple three NAND/NOR gate ⁴	7	25	8.5/gate	0.5	X	X	X	X	X			
RM/RC988	Monostable multivibrator	10	—	—	1.0	X	X	X		X			
RM/RC993	Dual RM945 ³ } separate clock, separate	9	52	70	0.5	X	X	X	X	X			
RM/RC994	Dual RM948 ⁴ } direct set, no direct clear	9	50	80	0.5	X	X	X		X			
RM/RC997	Dual RM948 ⁴ } common clock, common	9	50	80	0.5	X	X	X		X			
RM/RC999	Dual RM945 ³ } direct clear, direct set	9	52	70	0.5	X	X	X		X			

NOTES

1. Operating temperature range; RM types: -55°C to +125°C; RC types: 0°C to +75°C
2. Without collector pull-up resistor, R_c
3. 6KΩ pull-up resistor
4. 2KΩ pull-up resistor