

# SL22-L THRU SL24-L

## List

List.....	1
Package outline.....	2
Features.....	2
Mechanical data.....	2
Maximum ratings .....	2
Rating and characteristic curves.....	3
Pinning information.....	4
Marking.....	4
Suggested solder pad layout.....	4
Packing information.....	5
Reel packing.....	6
Suggested thermal profiles for soldering processes.....	6
High reliability test capabilities.....	7

# SL22-L THRU SL24-L

## 2.0A Low VF Surface Mount Schottky Barrier Rectifiers - 20V-40V

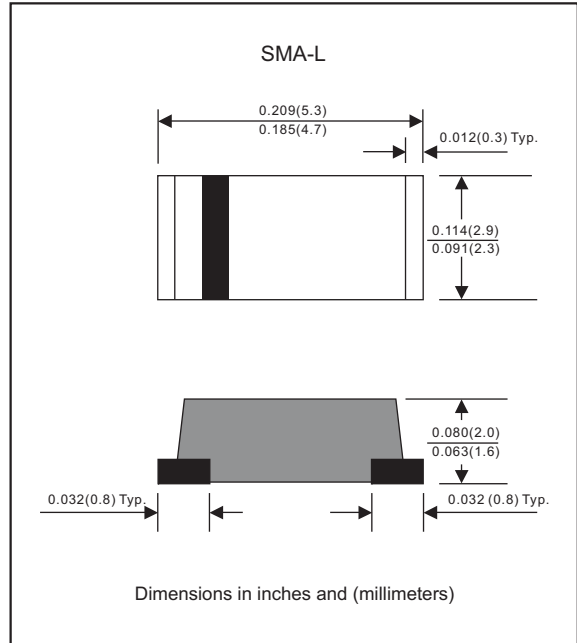
### Features

- Batch process design, excellent power dissipation offers better reverse leakage current and thermal resistance.
- Low profile surface mounted application in order to optimize board space.
- Low power loss, high efficiency.
- High current capability, low forward voltage drop.
- High surge capability.
- Guardring for overvoltage protection.
- Ultra high-speed switching.
- Silicon epitaxial planar chip, metal silicon junction.
- Lead-free parts meet environmental standards of MIL-STD-19500 /228
- Suffix "-H" indicates Halogen-free part, ex.SL22-L-H.

### Mechanical data

- Epoxy:UL94-V0 rated flame retardant
- Case : Molded plastic, DO-214AC / SMA-L
- Terminals : Solder plated, solderable per MIL-STD-750, Method 2026
- Polarity : Indicated by cathode band
- Mounting Position : Any
- Weight : Approximated 0.05 gram

### Package outline



### Maximum ratings and Electrical Characteristics (AT T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER	CONDITIONS	Symbol	MIN.	TYP.	MAX.	UNIT
Forward rectified current	See Fig.2	I <sub>O</sub>			2.0	A
Forward surge current	8.3ms single half sine-wave (JEDEC methode)	I <sub>FSM</sub>			50	A
Reverse current	V <sub>R</sub> = V <sub>RRM</sub> T <sub>J</sub> = 25°C	I <sub>R</sub>			1.0	mA
Diode junction capacitance	f=1MHz and applied 4V DC reverse voltage	C <sub>J</sub>		160		pF
Storage temperature		T <sub>STG</sub>	-65		+175	°C

SYMBOLS	V <sub>RRM</sub> <sup>*1</sup> (V)	V <sub>RMS</sub> <sup>*2</sup> (V)	V <sub>R</sub> <sup>*3</sup> (V)	V <sub>F</sub> <sup>*4</sup> (V)	Operating temperature T <sub>J</sub> (°C)
SL22-L	20	14	20	0.38	-55 to +100
SL23-L	30	21	30	0.40	
SL24-L	40	28	40	0.40	

- \*1 Repetitive peak reverse voltage
- \*2 RMS voltage
- \*3 Continuous reverse voltage
- \*4 Maximum forward voltage@I<sub>F</sub>=2.0A

## Rating and characteristic curves (SL22-L THRU SL24-L)

FIG.1-TYPICAL FORWARD CHARACTERISTICS

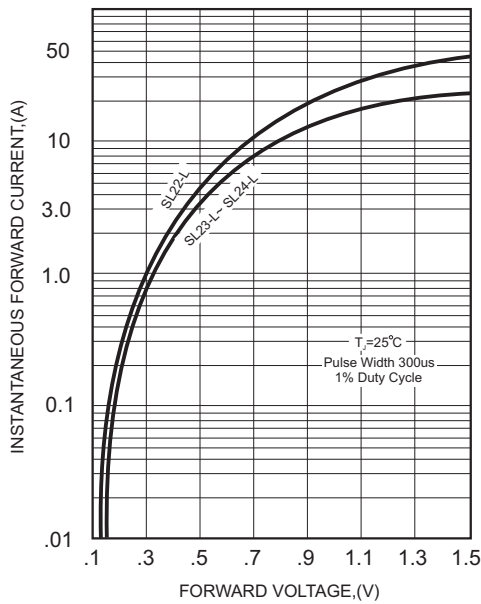


FIG.2-TYPICAL FORWARD CURRENT DERATING CURVE

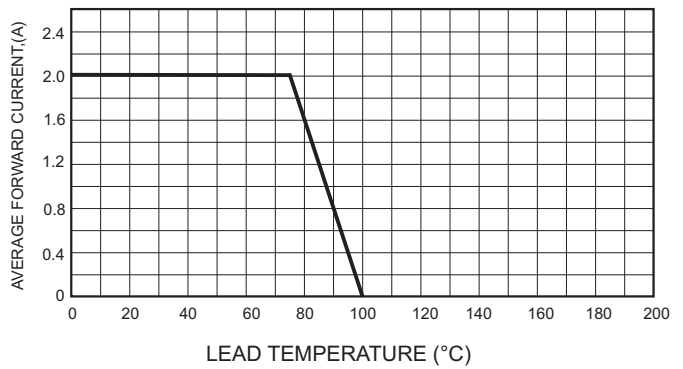


FIG.4-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

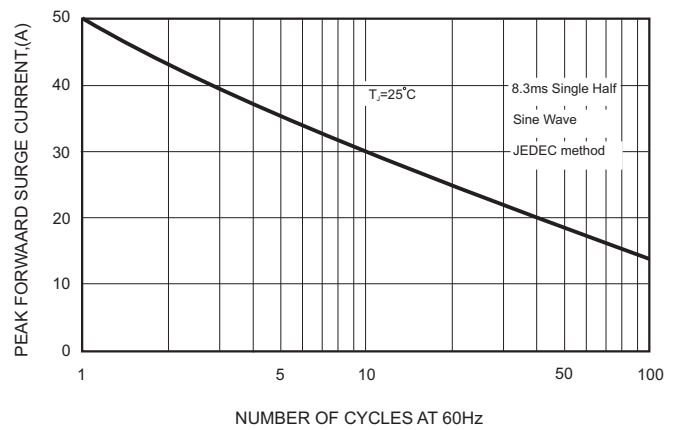


FIG.3 - TYPICAL REVERSE CHARACTERISTICS

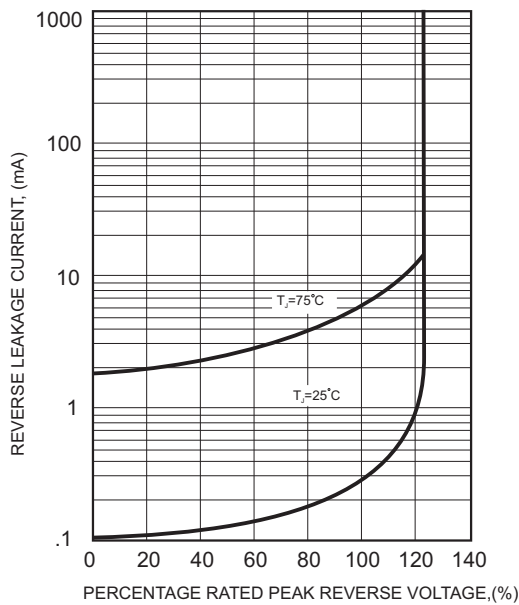
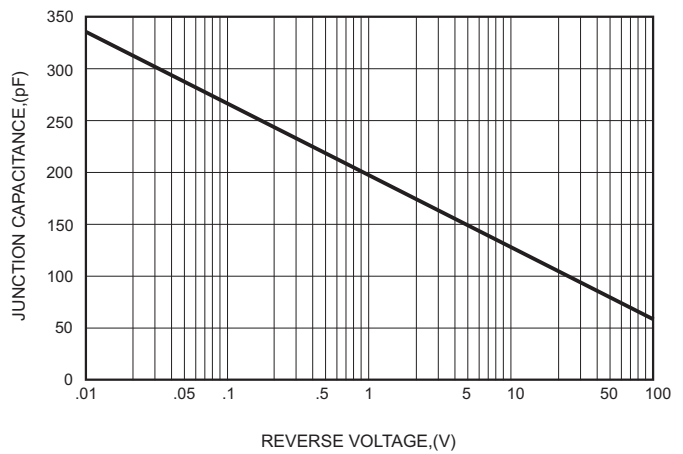




FIG.5-TYPICAL JUNCTION CAPACITANCE



# SL22-L THRU SL24-L

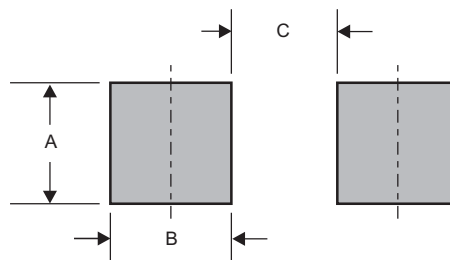
## Pinning information

Pin	Simplified outline	Symbol
Pin1 cathode Pin2 anode		

## Marking

Type number	Marking code
SL22-L	SL22
SL23-L	SL23
SL24-L	SL24

## Suggested solder pad layout

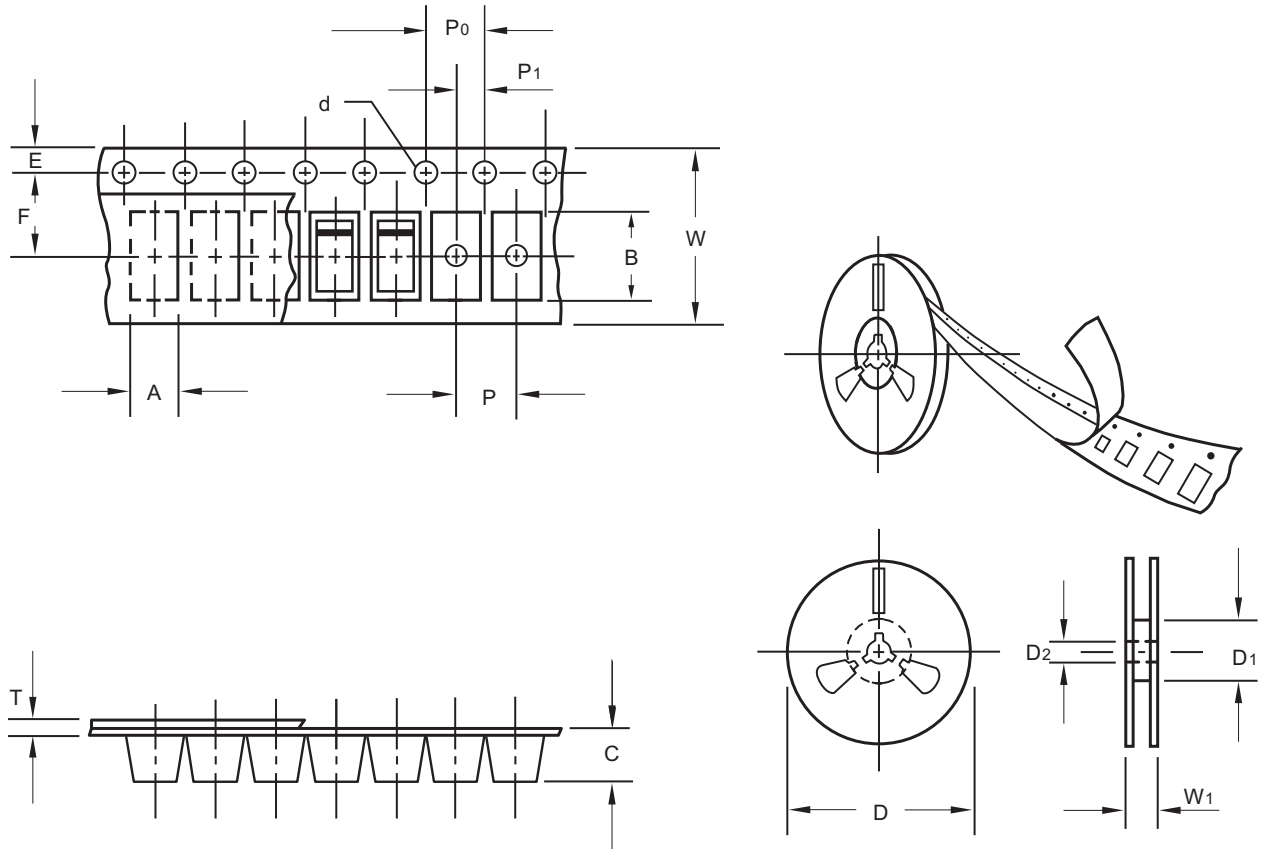


Dimensions in inches and (millimeters)

PACKAGE	A	B	C
SMA-L	0.110 (2.80)	0.059 (1.50)	0.110 (2.80)

# SL22-L THRU SL24-L

## Packing information



unit:mm

Item	Symbol	Tolerance	SMA-L
Carrier width	A	0.1	2.90
Carrier length	B	0.1	5.50
Carrier depth	C	0.1	2.10
Sprocket hole	d	0.1	1.50
13" Reel outside diameter	D	2.0	330.00
13" Reel inner diameter	D1	min	50.00
7" Reel outside diameter	D	2.0	178.00
7" Reel inner diameter	D1	min	62.00
Feed hole diameter	D2	0.5	13.00
Sprocket hole position	E	0.1	1.75
Punch hole position	F	0.1	5.50
Punch hole pitch	P	0.1	4.00
Sprocket hole pitch	P0	0.1	4.00
Embossment center	P1	0.1	2.00
Overall tape thickness	T	0.1	0.23
Tape width	W	0.3	12.00
Reel width	W1	1.0	18.00

Note: Devices are packed in accordance with EIA standard RS-481-A and specifications listed above.

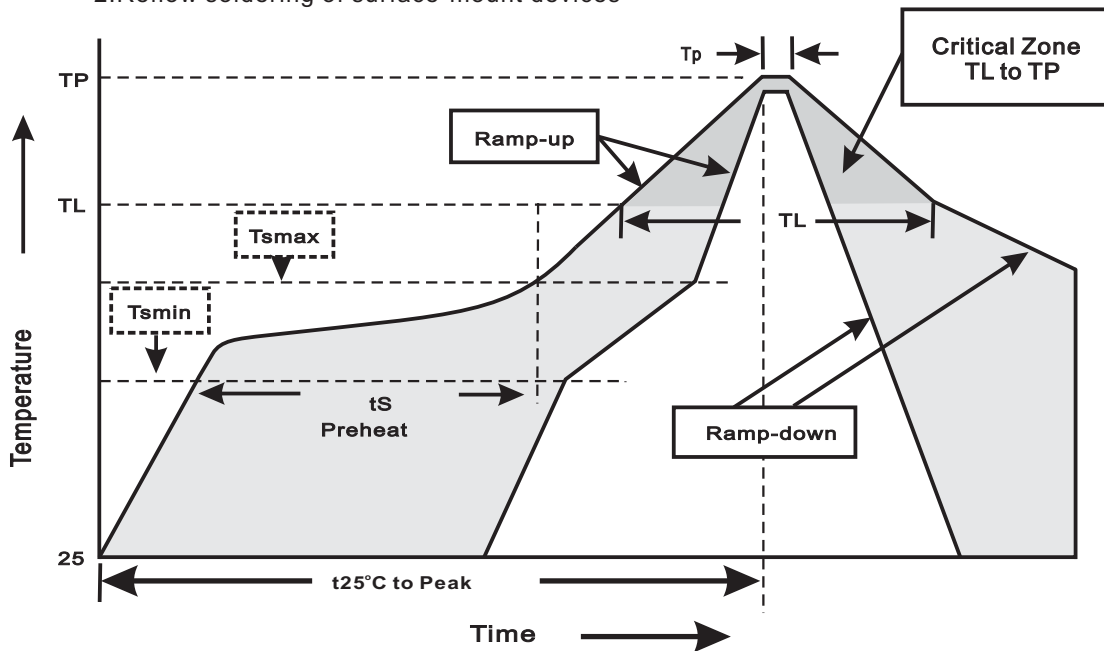
# SL22-L THRU SL24-L

## Reel packing

PACKAGE	REEL SIZE	REEL (pcs)	COMPONENT SPACING (m/m)	BOX (pcs)	INNER BOX (m/m)	REEL DIA, (m/m)	CARTON SIZE (m/m)	CARTON (pcs)	APPROX. GROSS WEIGHT (kg)
SMA-L	7"	2,000	4.0	20,000	183*155*183	178	382*356*392	160,000	15.5
	7"	2,000	4.0	10,000	180*180*80	178	440*410*220	100,000	13.0

## Suggested thermal profiles for soldering processes

- 1.Storage environment: Temperature=5°C~40°C Humidity=55%±25%
- 2.Reflow soldering of surface-mount devices



### 3.Reflow soldering

Profile Feature	Soldering Condition
Average ramp-up rate(T <sub>L</sub> to T <sub>P</sub> )	<3°C/sec
Preheat -Temperature Min(T <sub>smin</sub> ) -Temperature Max(T <sub>smax</sub> ) -Time(min to max)(t <sub>s</sub> )	150°C 200°C 60~120sec
T <sub>smax</sub> to T <sub>L</sub> -Ramp-upRate	<3°C/sec
Time maintained above: -Temperature(T <sub>L</sub> ) -Time(t <sub>L</sub> )	217°C 60~260sec
Peak Temperature(T <sub>P</sub> )	255°C-0/+5°C
Time within 5°C of actual Peak Temperature(t <sub>P</sub> )	10~30sec
Ramp-down Rate	<6°C/sec
Time 25°C to Peak Temperature	<6minutes

# SL22-L THRU SL24-L

## High reliability test capabilities

Item Test	Conditions	Reference
1. Solder Resistance	at $260 \pm 5^\circ\text{C}$ for $10 \pm 2$ sec. immerse body into solder $1/16" \pm 1/32"$	MIL-STD-750D METHOD-2031
2. Solderability	at $245 \pm 5^\circ\text{C}$ for 5 sec.	MIL-STD-202F METHOD-208
3. High Temperature Reverse Bias	$V_R = 80\%$ rate at $T_J = 100^\circ\text{C}$ for 168 hrs.	MIL-STD-750D METHOD-1038
4. Forward Operation Life	Rated average rectifier current at $T_A = 25^\circ\text{C}$ for 500hrs.	MIL-STD-750D METHOD-1027
5. Intermittent Operation Life	$T_A = 25^\circ\text{C}$ , $I_F = I_O$ On state: power on for 5 min. off state: power off for 5 min. on and off for 500 cycles.	MIL-STD-750D METHOD-1036
6. Pressure Cooker	$15P_{SIG}$ at $T_A = 121^\circ\text{C}$ for 4 hrs.	JESD22-A102
7. Temperature Cycling	$-55^\circ\text{C}$ to $+125^\circ\text{C}$ dwelled for 30 min. and transferred for 5min. total 10 cycles.	MIL-STD-750D METHOD-1051
8. Forward Surge	8.3ms single half sine-wave , one surge.	MIL-STD-750D METHOD-4066-2
9. Humidity	at $T_A = 85^\circ\text{C}$ , RH=85% for 1000hrs.	MIL-STD-750D METHOD-1021
10. High Temperature Storage Life	at $175^\circ\text{C}$ for 1000 hrs.	MIL-STD-750D METHOD-1031