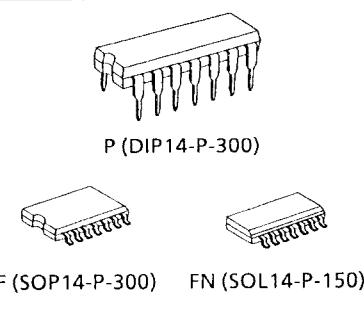


TC4030BP/BF/BFN

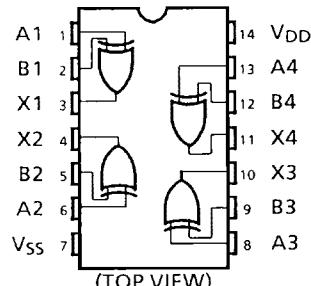
TC4030B QUAD EXCLUSIVE-OR GATE

TC4030B contains four circuits of exclusive OR gates. Since the buffers of two stage inverters are provided for all the outputs, the input / output voltage characteristic has been improved and the noise immunity has been also improved. And increase of transmission time due to load capacity increase is kept minimum.

Wide variety of applications are offered, such as digital comparators and parity circuits.



PIN ASSIGNMENT

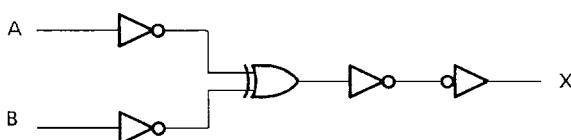


ABSOLUTE MAXIMUM RATINGS

CHARACTERISTIC	SYMBOL	RATING	UNIT
DC Supply Voltage	V _{DD}	V _{SS} - 0.5 ~ V _{SS} + 20	V
Input Voltage	V _{IN}	V _{SS} - 0.5 ~ V _{DD} + 0.5	V
Output Voltage	V _{OUT}	V _{SS} - 0.5 ~ V _{DD} + 0.5	V
DC Input Current	I _{IN}	± 10	mA
Power Dissipation	P _D	300 (DIP) / 180 (SOIC)	mW
Operating Temperature Range	T _A	- 40 ~ 85	°C
Storage Temperature Range	T _{STG}	- 65 ~ 150	°C
Lead Temp./Time	T _{SOL}	260°C · 10sec	

CIRCUIT DIAGRAM

1/4 TC4030B



TRUTH TABLE

INPUTS		OUTPUT
A	B	X
L	L	L
L	H	H
H	L	H
H	H	L

RECOMMENDED OPERATING CONDITIONS ($V_{SS} = 0V$)

CHARACTERISTICS	SYMBOL		MIN.	TYP.	MAX.	UNITS
DC Supply Voltage	V_{DD}		3	—	18	V
Input Voltage	V_{IN}		0	—	V_{DD}	V

STATIC ELECTRICAL CHARACTERISTICS ($V_{SS} = 0V$)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	V_{DD} (V)	- 40°C		25°C			85°C		UNITS
				MIN.	MAX.	MIN.	TYP.	MAX.	MIN.	MAX.	
High-Level Output Voltage	V_{OH}	$ I_{OUT} < 1\mu A$ $V_{IN} = V_{SS}, V_{DD}$	5 10 15	4.95 9.95 14.95	— — —	4.95 9.95 14.95	5.00 10.00 15.00	— — —	4.95 9.95 14.95	— — —	V
Low-Level Output Voltage	V_{OL}	$ I_{OUT} < 1\mu A$ $V_{IN} = V_{SS}, V_{DD}$	5 10 15	— 0.05 0.05	0.05 — —	0.00 0.00 0.00	0.05 0.05 0.05	— — —	0.05 0.05 0.05	— — —	V
Output High Current	I_{OH}	$V_{OH} = 4.6V$ $V_{OH} = 2.5V$ $V_{OH} = 9.5V$ $V_{OH} = 13.5V$ $V_{IN} = V_{SS}, V_{DD}$	5 5 10 15	-0.61 -2.5 -1.5 -4.0	— — — —	-0.51 -2.1 -1.3 -3.4	-1.0 -4.0 -2.2 -9.0	— — — —	-0.42 -1.7 -1.1 -2.8	— — — —	mA
Output Low Current	I_{OL}	$V_{OL} = 0.4V$ $V_{OL} = 0.5V$ $V_{OL} = 1.5V$ $V_{IN} = V_{SS}, V_{DD}$	5 10 15	0.61 1.5 4.0	— — —	0.51 1.3 3.4	1.2 3.2 12.0	— — —	0.42 1.1 2.8	— — —	mA
Input High Voltage	V_{IH}	$V_{OUT} = 0.5V, 4.5V$ $V_{OUT} = 1.0V, 9.0V$ $V_{OUT} = 1.5V, 13.5V$ $ I_{OUT} < 1\mu A$	5 10 15	3.5 7.0 11.0	— — —	3.5 7.0 11.0	2.75 5.5 8.25	— — —	3.5 7.0 11.0	— — —	V
Input Low Voltage	V_{IL}	$V_{OUT} = 0.5V, 4.5V$ $V_{OUT} = 1.0V, 9.0V$ $V_{OUT} = 1.5V, 13.5V$ $ I_{OUT} < 1\mu A$	5 10 15	— — —	1.5 3.0 4.0	— — —	2.25 4.5 6.75	1.5 3.0 4.0	— — —	1.5 3.0 4.0	V
Input Current	"H" Level "L" Level	I_{IH} I_{IL}	$V_{IH} = 18V$ $V_{IL} = 0V$	18 18	— —	0.1 -0.1	— —	10^{-5} - 10^{-5}	0.1 -0.1	— —	μA
Quiescent Device Current		I_{DD}	$V_{IN} = V_{SS}, V_{DD}^*$	5 10 15	— — —	1 2 4	— — —	0.001 0.001 0.002	1 2 4	— — —	7.5 15 30

* All valid input combinations.

TC4030BP/BF/BN

DYNAMIC ELECTRICAL CHARACTERISTICS ($T_a = 25^\circ\text{C}$, $V_{SS} = 0\text{V}$, $C_L = 50\text{pF}$)

CHARACTERISTICS	SYMBOL	TEST CONDITION	V_{DD} (V)	MIN.	TYP.	MAX.	UNITS
Output Transition Time (Low to High)	t_{TLH}		5	-	70	200	ns
			10	-	35	100	
Output Transition Time (High to Low)	t_{THL}		5	-	70	200	ns
			15	-	35	100	
Propagation Delay Time	t_{pLH}		5	-	90	280	ns
			10	-	45	130	
Propagation Delay Time	t_{pHL}		15	-	35	100	ns
				-	5	7.5	
Input Capacitance	C_{IN}						pF

CIRCUIT AND WAVEFORM FOR MEASUREMENT OF DYNAMIC CHARACTERISTICS

