



Integrated Device Technology, Inc.

# FAST CMOS 8-INPUT MULTIPLEXER

IDT54/74FCT151T/AT/CT

## FEATURES:

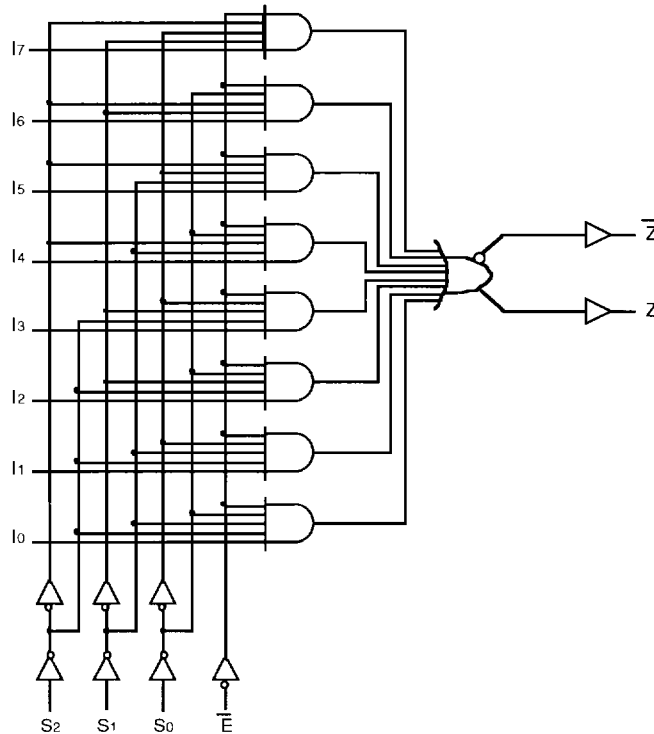
- Std., A, and C speed grades
- Low input and output leakage  $\leq 1\mu\text{A}$  (max.)
- CMOS power levels
- True TTL input and output compatibility
  - $V_{OH} = 3.3\text{V}$  (typ.)
  - $V_{OL} = 0.3\text{V}$  (typ.)
- High drive outputs (-15mA IOH, 48mA IOL)
- Power off disable outputs permit "live insertion"
- Meets or exceeds JEDEC standard 18 specifications
- Product available in Radiation Tolerant and Radiation Enhanced versions
- Military product compliant to MIL-STD-883, Class B and DESC listed (dual marked)
- Available in DIP, SOIC, CERPACK and LCC packages

## DESCRIPTION:

The IDT54/74FCT151T/AT/CT are high-speed 8-input multiplexers built using an advanced dual metal CMOS technology. They select one bit of data from up to eight sources under the control of three select inputs. Both assertion and negation outputs are provided.

The IDT54/74FCT151T/AT/CT has a common Active-LOW enable (E) input. When E is LOW, data from one of eight inputs is routed to the complementary outputs according to the 3-bit code applied to the Select (S<sub>0</sub>-S<sub>2</sub>) inputs. A common application of the 'FCT151 is data routing from one of eight sources.

## FUNCTIONAL BLOCK DIAGRAM



2635 dnv 01

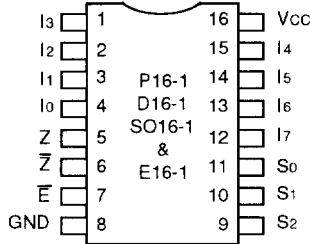
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MILITARY AND COMMERCIAL TEMPERATURE RANGES

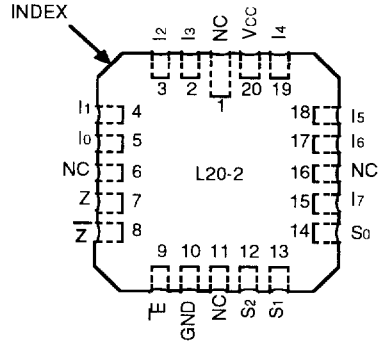
APRIL 1994

**PIN CONFIGURATIONS**



**DIP/SOIC/CERPACK  
TOP VIEW**

2635 drw 02



**LCC  
TOP VIEW**

2635 drw 03

**ABSOLUTE MAXIMUM RATINGS<sup>(1)</sup>**

Symbol	Rating	Commercial	Military	Unit
V <sub>TERM</sub> <sup>(2)</sup>	Terminal Voltage with Respect to GND	-0.5 to +7.0	-0.5 to +7.0	V
V <sub>TERM</sub> <sup>(3)</sup>	Terminal Voltage with Respect to GND	-0.5 to V <sub>CC</sub> +0.5	-0.5 to V <sub>CC</sub> +0.5	V
T <sub>A</sub>	Operating Temperature	0 to +70	-55 to +125	°C
T <sub>BIAS</sub>	Temperature Under Bias	-55 to +125	-65 to +135	°C
T <sub>STG</sub>	Storage Temperature	-55 to +125	-65 to +150	°C
P <sub>T</sub>	Power Dissipation	0.5	0.5	W
I <sub>OUT</sub>	DC Output Current	-60 to +120	-60 to +120	mA

- NOTES:** 2635 Ink 01
- Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability. No terminal voltage may exceed V<sub>CC</sub> by +0.5V unless otherwise noted.
  - Input and V<sub>CC</sub> terminals only.
  - Outputs and I/O terminals only.

**PIN DESCRIPTION**

Pin Names	Description
I <sub>0</sub> - I <sub>7</sub>	Data Inputs
S <sub>0</sub> - S <sub>2</sub>	Selects Inputs
E	Enable Input (Active LOW)
Z	Data Output
Z-bar	Inverted Data Output

2635 tbl 03

**FUNCTION TABLE<sup>(1)</sup>**

Inputs				Outputs	
S <sub>2</sub>	S <sub>1</sub>	S <sub>0</sub>	E	Z	Z-bar
X	X	X	H	L	H
L	L	L	L	I <sub>0</sub>	I <sub>0</sub>
L	L	H	L	I <sub>1</sub>	I <sub>1</sub>
L	H	L	L	I <sub>2</sub>	I <sub>2</sub>
L	H	H	L	I <sub>3</sub>	I <sub>3</sub>
H	L	L	L	I <sub>4</sub>	I <sub>4</sub>
H	L	H	L	I <sub>5</sub>	I <sub>5</sub>
H	H	L	L	I <sub>6</sub>	I <sub>6</sub>
H	H	H	L	I <sub>7</sub>	I <sub>7</sub>

- NOTE:** 2635 tbl 04
- H = HIGH Voltage Level, L = LOW Voltage Level, X = Don't care, Z = High Impedance.

**CAPACITANCE (T<sub>A</sub> = +25°C, f = 1.0MHz)**

Symbol	Parameter <sup>(1)</sup>	Conditions	Typ.	Max.	Unit
C <sub>IN</sub>	Input Capacitance	V <sub>IN</sub> = 0V	6	10	pF
C <sub>OUT</sub>	Output Capacitance	V <sub>OUT</sub> = 0V	8	12	pF

- NOTE:** 2635 Ink 02
- This parameter is measured at characterization but not tested.

### DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

Following Conditions Apply Unless Otherwise Specified:

Commercial: TA = 0°C to +70°C, VCC = 5.0V ± 5%; Military: TA = -55°C to +125°C, VCC = 5.0V ± 10%

Symbol	Parameter	Test Conditions <sup>(1)</sup>		Min.	Typ. <sup>(2)</sup>	Max.	Unit
V <sub>IH</sub>	Input HIGH Level	Guaranteed Logic HIGH Level		2.0	—	—	V
V <sub>IL</sub>	Input LOW Level	Guaranteed Logic LOW Level		—	—	0.8	V
I <sub>IH</sub>	Input HIGH Current <sup>(4)</sup>	VCC = Max.	V <sub>I</sub> = 2.7V	—	—	±1	μA
I <sub>IL</sub>	Input LOW Current <sup>(4)</sup>	VCC = Max.	V <sub>I</sub> = 0.5V	—	—	±1	μA
I <sub>OZH</sub>	High Impedance Output Current <sup>(4)</sup>	VCC = Max.	V <sub>O</sub> = 2.7V	—	—	±1	μA
I <sub>OZL</sub>			V <sub>O</sub> = 0.5V	—	—	±1	
I <sub>I</sub>	Input HIGH Current <sup>(4)</sup>	VCC = Max., V <sub>I</sub> = VCC (Max.)		—	—	20	μA
V <sub>IK</sub>	Clamp Diode Voltage	VCC = Min., I <sub>N</sub> = -18mA		—	-0.7	-1.2	V
I <sub>OS</sub>	Short Circuit Current	VCC = Max. <sup>(3)</sup> , V <sub>O</sub> = GND		-60	-120	-225	mA
V <sub>OH</sub>	Output HIGH Voltage	VCC = Min. V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OH</sub> = -6mA MIL. I <sub>OH</sub> = -8mA COM'L.	2.4	3.3	—	V
			I <sub>OH</sub> = -12mA MIL. I <sub>OH</sub> = -15mA COM'L.	2.0	3.0	—	V
V <sub>OL</sub>	Output LOW Voltage	VCC = Min. V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OL</sub> = 32mA MIL. I <sub>OL</sub> = 48mA COM'L.	—	0.3	0.5	V
V <sub>H</sub>	Input Hysteresis	—		—	200	—	mV
I <sub>CC</sub>	Quiescent Power Supply Current	VCC = Max. V <sub>IN</sub> = GND or VCC		—	0.01	1	mA

**NOTES:**

1. For conditions shown as Max. or Min., use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at VCC = 5.0V, +25°C ambient and maximum loading.
3. Not more than one output should be shorted at one time. Duration of the short circuit test should not exceed one second.
4. The test limit for this parameter is ±5μA at TA = -55°C.

2635 tbl 05

**POWER SUPPLY CHARACTERISTICS**

Symbol	Parameter	Test Conditions <sup>(1)</sup>		Min.	Typ. <sup>(2)</sup>	Max.	Unit
$\Delta I_{CC}$	Quiescent Power Supply Current TTL Inputs HIGH	VCC = Max. VIN = 3.4V <sup>(3)</sup>		—	0.5	2.0	mA
I <sub>CCD</sub>	Dynamic Power Supply Current <sup>(4)</sup>	VCC = Max. Outputs Open E or OE = GND One Bit Toggling 50% Duty Cycle	VIN = VCC VIN = GND	—	0.15	0.25	mA/ MHz
I <sub>C</sub>	Total Power Supply Current <sup>(5)</sup>	VCC = Max. Outputs Open fi = 10MHz 50% Duty Cycle E or OE = GND One Input Toggling	VIN = VCC VIN = GND	—	3.2	6.5	mA
			VIN = 3.4V VIN = GND	—	3.5	7.5	

**NOTES:**

2635 tbl 06

- For conditions shown as Max. or Min., use appropriate value specified under Electrical Characteristics for the applicable device type.
- Typical values are at VCC = 5.0V, +25°C ambient.
- Per TTL driven input (VIN = 3.4V). All other inputs at VCC or GND.
- This parameter is not directly testable, but is derived for use in Total Power Supply Calculations.
- Values for these conditions are examples of the I<sub>CC</sub> formula. These limits are guaranteed but not tested.
- I<sub>C</sub> = I<sub>QUIESCENT</sub> + I<sub>INPUTS</sub> + I<sub>DYNAMIC</sub>  
 $I_C = I_{CC} + \Delta I_{CC} D_{HNT} + I_{CCD} (f_{CP}/2 + f_i N_o)$   
 I<sub>CC</sub> = Quiescent Current  
 $\Delta I_{CC}$  = Power Supply Current for a TTL High Input (VIN = 3.4V)  
 D<sub>H</sub> = Duty Cycle for TTL Inputs High  
 N<sub>T</sub> = Number of TTL Inputs at D<sub>H</sub>  
 I<sub>CCD</sub> = Dynamic Current Caused by an Input Transition Pair (HLH or LHL)  
 f<sub>CP</sub> = Clock Frequency for Register Devices (Zero for Non-Register Devices)  
 f<sub>i</sub> = Input Frequency  
 N<sub>o</sub> = Number of Inputs at f<sub>i</sub>  
 All currents are in milliamps and all frequencies are in megahertz.

**SWITCHING CHARACTERISTICS OVER OPERATING RANGE**

Symbol	Parameter	Condition <sup>(1)</sup>	IDT54/74FCT151T				IDT54/74FCT151AT				IDT54/74FCT151CT				Unit
			Com'l.		Mil.		Com'l.		Mil.		Com'l.		Mil.		
			Min. <sup>(2)</sup>	Max.	Min. <sup>(2)</sup>	Max.	Min. <sup>(2)</sup>	Max.	Min. <sup>(2)</sup>	Max.	Min. <sup>(2)</sup>	Max.	Min. <sup>(2)</sup>	Max.	
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay S <sub>N</sub> to Z	CL = 50pF RL = 500Ω	1.5	9.0	1.5	10.0	1.5	6.6	1.5	7.4	1.5	5.6	1.5	6.2	ns
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay S <sub>N</sub> to Z		1.5	10.5	1.5	11.5	1.5	6.8	1.5	7.6	1.5	5.8	1.5	6.5	ns
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay E to Z		1.5	7.0	1.5	7.5	1.5	5.6	1.5	6.3	1.5	4.8	1.5	5.4	ns
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay E to Z		1.5	9.5	1.5	11.0	1.5	5.8	1.5	6.6	1.5	5.0	1.5	5.7	ns
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay I <sub>N</sub> to Z		1.5	6.5	1.5	7.5	1.5	5.2	1.5	5.8	1.5	4.4	1.5	4.9	ns
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay I <sub>N</sub> to Z		1.5	7.5	1.5	9.0	1.5	5.5	1.5	6.1	1.5	4.7	1.5	5.2	ns

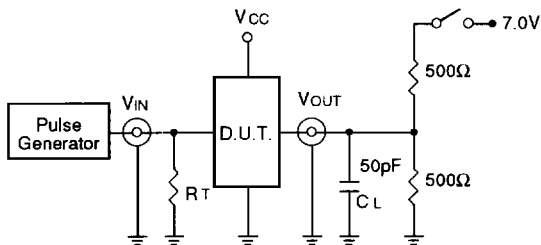
**NOTES:**

2635 tbl 07

- See test circuit and waveforms.
- Minimum limits are guaranteed but not tested on Propagation Delays.

**TEST CIRCUITS AND WAVEFORMS**

**TEST CIRCUITS FOR ALL OUTPUTS**



2635 drw 04

**SWITCH POSITION**

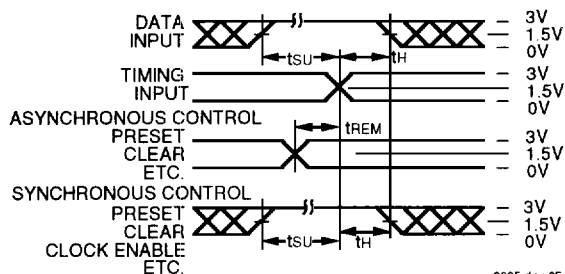
Test	Switch
Open Drain Disable Low Enable Low	Closed
All Other Tests	Open

**DEFINITIONS:**

CL = Load capacitance; includes jig and probe capacitance.  
RT = Termination resistance; should be equal to ZOUT of the Pulse Generator.

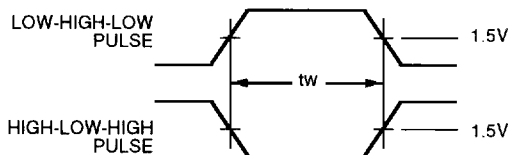
2635 Ink 08

**SET-UP, HOLD AND RELEASE TIMES**



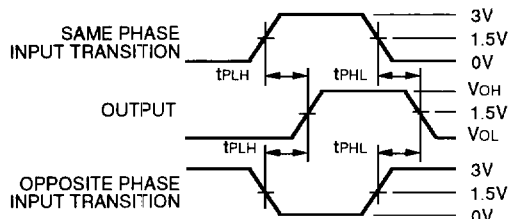
2635 drw 05

**PULSE WIDTH**



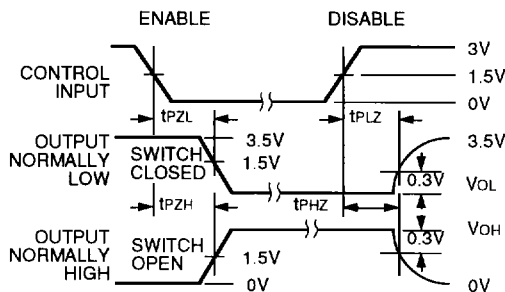
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**PROPAGATION DELAY**



2635 drw 07

**ENABLE AND DISABLE TIMES**



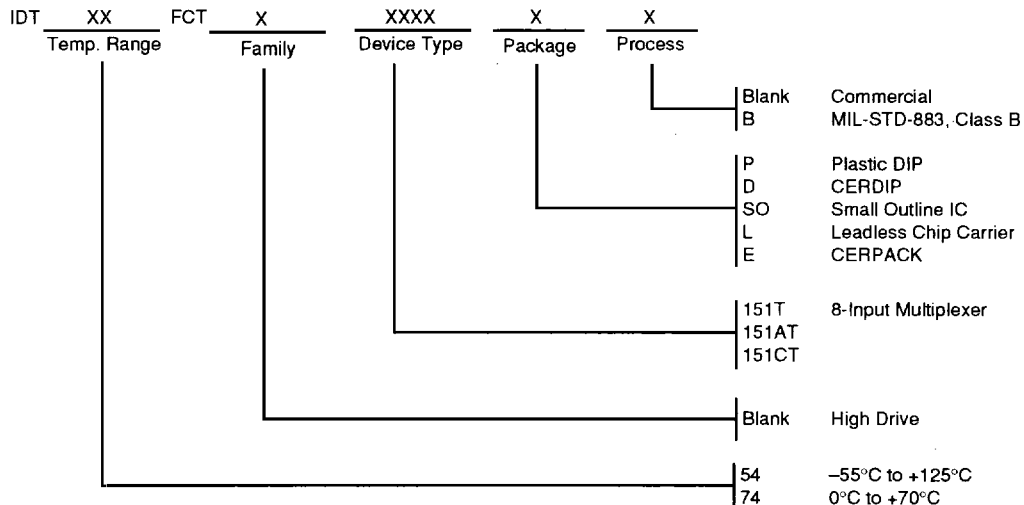
2635 drw 08

**NOTES:**

- Diagram shown for input Control Enable-LOW and input Control Disable-HIGH
- Pulse Generator for All Pulses: Rate ≤ 1.0MHz; tr ≤ 2.5ns; tr ≤ 2.5ns



**ORDERING INFORMATION**



2635 drw 09