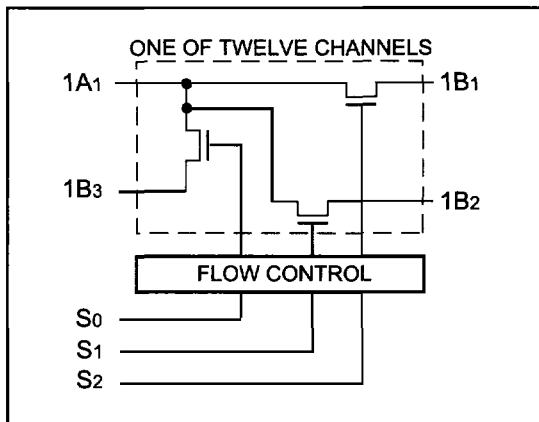


12-Bit, 3-To-1 Bus-Select Switch

Product Features:

- Near-zero propagation delay
- 5Ω switches connect inputs to outputs
- Direct bus connection when switches are ON
- Ultra-low quiescent power (0.2 μA typical) – ideally suited for notebook applications
- Industrial operating temperature: -40°C to +85°C
- Packages available:
 - 56-pin 240-mil wide plastic TSSOP (A56)
 - 56-pin 300-mil wide plastic SSOP (V56)

Logic Block Diagram



Truth Table⁽¹⁾

Function	S2	S1	S0	A1
Disconnect	L	L	L	Z
A1 to B1	L	L	H	B1
A1 to B2	L	H	L	B2
Disconnect	L	H	H	Z
Disconnect	H	L	L	Z
A1 to B3	H	L	H	B1
A1 to B1	H	H	L	B3
A1 to B2	H	H	H	B2

Note:

1. H = High Voltage Level
L = Low Voltage Level
Z = High Impedance

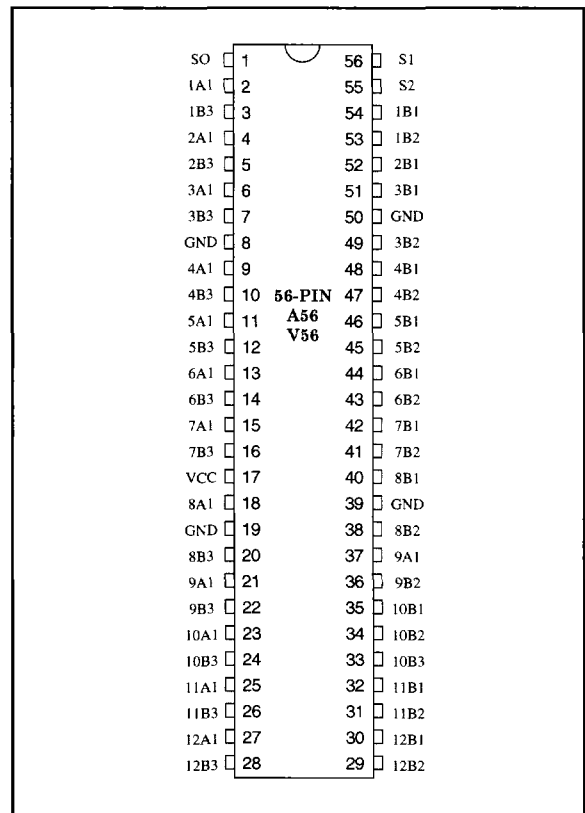
Product Description:

Pericom Semiconductor's PI5C series of BusSwitch circuits are produced in the Company's advanced 0.8 micron CMOS technology, achieving industry leading speed.

The PI5C16214 and PI5C162214 are 3-to-1 bus-select switches designed with a low ON resistance (5Ω) allowing inputs to be connected directly to outputs. These devices operate as 12-bit bus switches via the data select pins (S0-S2).

The PI5C162214 device has a built-in 25-ohm series resistor to reduce noise resulting from reflections, thus eliminating the need for an external terminating resistor.

Product Pin Configuration



Product Pin Description

Pin Name	I/O	Description
SO-S2	I	Select Inputs
xAx	I/O	Bus A
xBx	I/O	Bus B

Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	-65°C to +150°C
Ambient Temperature with Power Applied	-40°C to +85°C
Supply Voltage to Ground Potential (Inputs & Vcc Only)	-0.5V to +7.0V
Supply Voltage to Ground Potential (Outputs & D/O Only) ..	-0.5V to +7.0V
DC Input Voltage	-0.5V to +7.0V
DC Output Current	120 mA
Power Dissipation	0.5W

Note:

 Stresses greater than those listed under **MAXIMUM RATINGS** may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over the Operating Range, $T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$, $V_{CC} = 4$ to 5V)

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ ⁽²⁾	Max.	Units	
V_{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level	2.0	—	—	V	
V_{IL}	Input LOW Voltage	Guaranteed Logic LOW Level	-0.5	—	0.8	V	
I_i	Input Current	$V_{CC} = 5.5\text{V}$, $V_{IN} = 5.5\text{V}$ OR GND	—	—	± 1	μA	
		$V_{CC} = 0\text{V}$, $V_{IN} = V_{CC}$	—	—	± 1	μA	
I_{OZH}	High Impedance Output Current	$0 \leq A, B \leq V_{CC}$	—	—	10	μA	
V_{IK}	Clamp Diode Voltage	$V_{CC} = 4.5\text{V}$, $I_{IN} = -18\text{ mA}$	—	-0.7	-1.2	V	
I_{OS}	Short Circuit Current ⁽³⁾	A (B) = 0V, B (A) = V_{CC}	100	—	—	mA	
V_H	Input Hysteresis at Control Pins		—	150	—	mV	
R_{ON}	Switch On Resistance ⁽⁴⁾	$V_{CC} = 4.5\text{V}$, $V_{IN} = 0.0\text{V}$, $I_{ON} = 30\text{ mA}$, 64 mA	16210	—	5	7	Ω
			162210	20	28	40	
		$V_{CC} = 4.5\text{V}$, $V_{IN} = 2.4\text{V}$, $I_{ON} = 15\text{ mA}$	16210	—	—	15	Ω
			162210	20	35	48	
		$V_{CC} = 4\text{ V}$, $V_{IN} = 2.4\text{V}$, $I_{ON} = 15\text{ mA}$	16210	—	14	20	Ω

Capacitance ($T_A = 25^\circ\text{C}$, $f = 1\text{ MHz}$)

Parameters ⁽⁵⁾	Description	Test Conditions	Typ	Units
C_{IN}	Input Capacitance	$V_{IN} = 0\text{V}$ or 3V	4.5	pF
C_{OFF}	A/B Capacitance, Switch Off	$V_{IN} = 0\text{V}$ or 3V	5.5	pF

Notes:

- For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- Typical values are at $V_{CC} = 5.0\text{V}$, $T_A = 25^\circ\text{C}$ ambient and maximum loading.
- Not more than one output should be shorted at one time. Duration of the test should not exceed one second.
- Measured by the voltage drop between A and B pin at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (A,B) pins.
- This parameter is determined by device characterization but is not production tested.

Power Supply Characteristics

Parameters	Description	Test Conditions ⁽¹⁾		Min.	Typ ⁽²⁾	Max.	Units
I _{cc}	Quiescent Power Supply Current	V _{CC} = Max.	V _{IN} = GND or V _{CC}		0.1	10	μA
ΔI _{cc}	Supply Current per Input @ TTL HIGH	V _{CC} = Max.	V _{IN} = 3.4V ⁽³⁾			2.5	mA
I _{CCD}	Supply Current per Input per MHz ⁽⁴⁾	V _{CC} = Max., A and B Pins Open Control Input Toggling 50% Duty Cycle				0.25	mA/ MHz

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
2. Typical values are at V_{CC} = 5.0V, +25°C ambient.
3. Per TTL driven input (V_{IN} = 3.4V, control inputs only); A and B pins do not contribute to I_{cc}.
4. This current applies to the control inputs only and represent the current required to switch internal capacitance at the specified frequency. The A and B inputs generate no significant AC or DC currents as they transition. This parameter is not tested, but is guaranteed by design.

PI5C16214 Switching Characteristics over Operating Range

Parameters	Description	Conditions ⁽¹⁾	PI5C16214		Unit
			V _{CC} = 5V ±0.5V		
			Min.	Max.	
t _{PLH}	Propagation Delay ^(2,3)	C _L = 50 pF R _L = 500Ω		0.25	ns
t _{PHL}	Ax to Bx, Bx to Ax				
t _{PZH}	Bus Enable Time		1.5	6.5	ns
t _{PZL}	xOE to Ax or Bx				
t _{PHZ}	Bus Disable Time		1.5	5.5	ns
t _{PLZ}	xOE to Ax or Bx				

PI5C162214 Switching Characteristics over Operating Range

Parameters	Description	Conditions ⁽¹⁾	PI5C162214		Unit
			V _{CC} = 5V ±0.5V		
			Min.	Max.	
t _{PLH}	Propagation Delay ^(2,3)	C _L = 50 pF R _L = 500Ω		1.25	ns
t _{PHL}	Ax to Bx, Bx to Ax				
t _{PZH}	Bus Enable Time		1.5	6.5	ns
t _{PZL}	xOE to Ax or Bx				
t _{PHZ}	Bus Disable Time		1.5	5.5	ns
t _{PLZ}	xOE to Ax or Bx				

Notes:

1. See test circuit and wave forms.
2. This parameter is guaranteed but not tested on Propagation Delays.
3. The bus switch contributes no propagational delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25 ns for 50 pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.