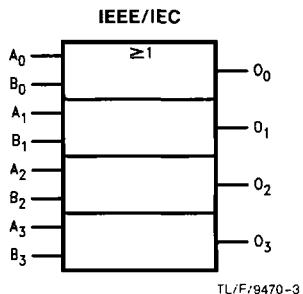




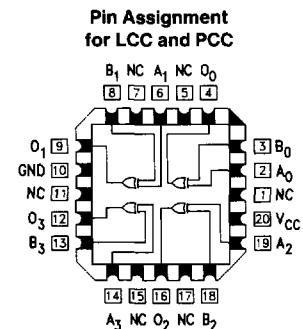
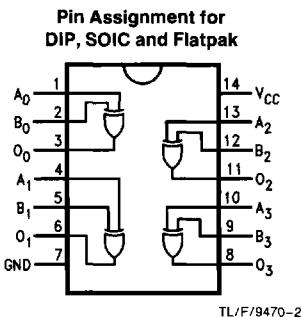
54F/74F86 2-Input Exclusive-OR Gate

Ordering Code: See Section 5

Logic Symbol



Connection Diagrams



Unit Loading/Fan Out: See Section 2 for U.L. definitions

Pin Names	Description	54F/74F	
		U.L. HIGH/LOW	Input I _{IH} /I _{IL} Output I _{OH} /I _{OL}
A _n , B _n O _n	Inputs Outputs	1.0/1.0 50/33.3	20 μA/-0.6 mA -1 mA/20 mA

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Storage Temperature	−65°C to +150°C
Ambient Temperature under Bias	−55°C to +125°C
Junction Temperature under Bias	−55°C to +175°C
V _{CC} Pin Potential to Ground Pin	−0.5V to +7.0V
Input Voltage (Note 2)	−0.5V to +7.0V
Input Current (Note 2)	−30 mA to +5.0 mA
Voltage Applied to Output in HIGH State (with V _{CC} = 0V)	
Standard Output	−0.5V to V _{CC}
TRI-STATE® Output	−0.5V to +5.5V

Current Applied to Output in LOW State (Max) twice the rated I_{OL} (mA)

Note 1: Absolute maximum ratings are values beyond which the device may be damaged or have its useful life impaired. Functional operation under these conditions is not implied.

Note 2: Either voltage limit or current limit is sufficient to protect inputs.

Recommended Operating Conditions

Free Air Ambient Temperature	
Military	−55°C to +125°C
Commercial	0°C to +70°C
Supply Voltage	
Military	+4.5V to +5.5V
Commercial	+4.5V to +5.5V

DC Electrical Characteristics

Symbol	Parameter	54F/74F			Units	V _{CC}	Conditions
		Min	Typ	Max			
V _{IH}	Input HIGH Voltage	2.0			V		Recognized as a HIGH Signal
V _{IL}	Input LOW Voltage		0.8		V		Recognized as a LOW Signal
V _{CD}	Input Clamp Diode Voltage		−1.2		V	Min	I _{IN} = −18 mA
V _{OH}	Output HIGH Voltage	54F 10% V _{CC} 74F 10% V _{CC} 74F 5% V _{CC}	2.5 2.5 2.7		V	Min	I _{OH} = −1 mA I _{OH} = −1 mA I _{OH} = −1 mA
V _{OL}	Output LOW Voltage	54F 10% V _{CC} 74F 10% V _{CC}	0.5 0.5		V	Min	I _{OL} = 20 mA I _{OL} ≈ 20 mA
I _{IH}	Input HIGH Current		20	μA	Max	V _{IN} = 2.7V	
I _{BVI}	Input HIGH Current Breakdown Test		100	μA	Max	V _{IN} = 7.0V	
I _{IL}	Input LOW Current		−0.6	mA	Max	V _{IN} = 0.5V	
I _{OS}	Output Short-Circuit Current	−60	−150	mA	Max	V _{OUT} = 0V	
I _{CEx}	Output HIGH Leakage Current		250	μA	Max	V _{OUT} = V _{CC}	
I _{CCH}	Power Supply Current	12	18	mA	Max	V _O = HIGH	
I _{CCL}	Power Supply Current	18	28	mA	Max	V _O = LOW	

AC Electrical Characteristics: See Section 2 for Waveforms and Load Configurations

Symbol	Parameter	74F			54F		74F		Units	Fig No		
		$T_A = +25^\circ C$ $V_{CC} = +5.0V$ $C_L = 50 pF$			$T_A, V_{CC} = \text{Mil}$ $C_L = 50 pF$		$T_A, V_{CC} = \text{Com}$ $C_L = 50 pF$					
		Min	Typ	Max	Min	Max	Min	Max				
t_{PLH}	Propagation Delay A_n, B_n to O_n (Other Input LOW)	3.0	4.0	5.5	2.5	7.0	3.0	6.5	ns	2-3		
t_{PHL}	Propagation Delay A_n, B_n to O_n (Other Input HIGH)	3.0	4.2	5.5	3.0	7.0	3.0	6.5	ns	2-3		
t_{PLH}	Propagation Delay A_n, B_n to O_n (Other Input HIGH)	3.5	5.3	7.0	3.5	8.5	3.5	8.0	ns	2-3		
t_{PHL}		3.0	4.7	6.5	3.0	8.0	3.0	7.5				