

AAHH32

Radiation Tolerant QUAD 2-Input OR Gate

PRELIMINARY DATA

DESCRIPTION

The AAHH32 is part of ASIC Advantage's family of Radiation Tolerant products aimed at the military and aerospace markets. The AAHH32 is a Radiation-Tolerant Quad 2 input OR gate. It utilizes Bi-CMOS technology and is a replacement to the obsolete part 5962R9573601VXC.

FEATURES

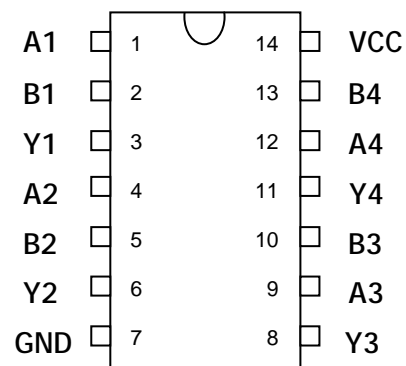
- Tolerance to 200kRad(Si) Total Dose
- Latch-Up Free Design
- Input Compatible to LSTTL
 $V_{IL} = 0.8V$, $V_{IH} = V_{CC}/2$
- Low Power ICs
- Operating Temperature Range of $-55^{\circ}C$ to $+125^{\circ}C$
- Input Compatibility to CMOS
 $I_i \leq 5\mu A$ at V_{OL} , V_{OH}
- SEP Effective LET no Upsets: $>100MEV\text{-}cm^2/mg$
- Immunity to SEU up to $<5 \times 10^{-16}$ errors/Bit-Day
- Available in Die or 14-pin Ceramic Dual-In-Line FLATPACK package.

TRUTH TABLE

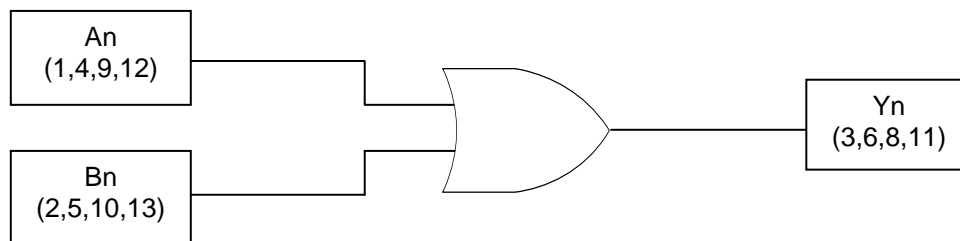
Inputs		Output
An	Bn	Yn
L	L	L
L	H	H
H	L	H
H	H	H

PIN CONFIGURATION:

14 pin CERAMIC DUAL-IN-LINE FLATPACK



FUNCTIONAL DIAGRAM



Radiation Tolerant AAHH32

Absolute Maximum Ratings

Supply voltage: -0.5V to 7V
 Input Voltage Range: -0.5V to VCC+0.5V
 Dc Input Current: $\pm 10\text{mA}$
 DC Drain Current: $\pm 25\text{mA}$
 Storage Temperature Range: -65°C to 150°C
 Lead Temperature Range: 265°C
 Junction Temperature: 175°C
 ESD Classification: Class 1

Reliability Information

TBD

Operating Conditions:

Supply Voltage: 4.5V to 5.5V
 Input rise and Fall Times: 100ns/Vmax
 Input Low Voltage (VIL): 0.0 to 0.8V
 Input High Voltage (VIH): VCC/2 to VCC
 Operating Temperature Range: -55°C to 125°C

DC ELECTRICAL CHARACTERISTICS							
PARAMETER	SYMBOL	CONDITION	GROUP	TEMPERATURE	LIMITS		UNIT
					MIN	MAX	
Quiescent Current	ICC	VCC=5.5V VIN=VCC or GND	1	25°C		10	μA
			2, 3	-55°C, 125°C		200	μA
Output Current (sink)	IOL	VCC=4.5V, VIH =4.5V VOUT=0.4V, VIL=0V	1	25°C	4.8		mA
			2, 3	-55°C, 125°C	4.0		mA
Output Current (source)	IOH	VCC=4.5V, VIH=4.5V, VOUT=VCC-0.4V, VIL=0V	1	25°C		-4.8	mA
			2, 3	-55°C, 125°C		-4.0	mA
Output Voltage Low	VOL	VCC=4.5V, VIH=2.25V	1, 2, 3	25°C		0.1	V
		VCC=5.5V, VIH=2.75V IOL=50 μA , VIL=0.8V	1, 2, 3	-55°C, 125°C		0.1	V
Output Voltage High	VOH	VCC=4.5V, VIH=2.25V IOH= -50 μA , VIL=0.8V	1, 2, 3	25°C	VCC-0.1		V
		VCC=5.5V, VIH=2.75V IOH= -50 μA , VIL=0.8V	1, 2, 3	-55°C, 125°C	VCC-0.1		V
Input Leakage Current	IIN	VCC=5.5V, VIN=VCC or GND	1	25°C		± 0.5	μA
			2, 3	-55°C, 125°C		± 5.0	μA
Noise Immunity Functional Test	FN	VCC=4.5V, VIH=2.25V VIL=0.8V	7, 8A, 8B	25°C, -55°C, 125°C			

AC ELECTRICAL PERFORMANCE CHARACTERISTICS							
PARAMETER	SYMBOL	CONDITIONS	Group	TEMPERATURE	LIMITS		UNITS
					MIN	MAX	
Input to YN	TPHL	VCC=4.5V	9	25°C	2	18	ns
			10, 11	125°C, -55°C	2	20	ns
Input to YN	TPLH	VCC=4.5V	9	25°C	2	20	ns
			10, 11	125°C, -55°C	2	22	ns

Notes: RL=500ohms, CL=50pF, TR=TF=3ns

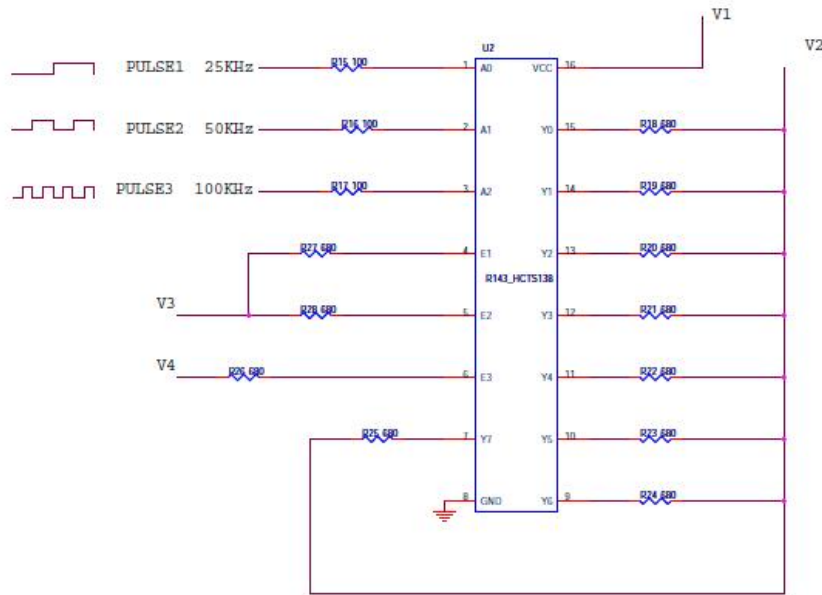
ELECTRICAL PERFORMANCE CHARACTERISTICS						
PARAMETER	SYMBOL	CONDITIONS	TEMPERATURE	LIMITS		UNITS
				MIN	MAX	
Capacitance Power Dissipation	CPD	VCC=5.0V, f=1Mhz	25°C		38	pF
			125°C, -55°C		72	pF
Input Capacitance	CIN	VCC=5.0V, f=1Mhz	25°C		10	pF
			125°C		10	pF
Output Transition Time	TTHL	VCC=4.5V	25°C		15	ns
	TTLH		125°C		22	ns

DC POST RADIATION ELECTRICAL CHARACTERISTICS						
PARAMETER	SYMBOL	CONDITION	TEMPERATURE	LIMITS		UNITS
				MIN	MAX	
Quiescent Current	ICC	VCC=5.5V, VIN=VCC or GND	25°C		0.2	mA
Output Current (sink)	IOL	VCC=4.5V, VIH=4.5V VOUT=0.4V, VIL=0V	25°C	4.0		mA
Output Current (source)	IOH	VCC=4.5V, VIH=4.5V, VOUT=VCC-0.4V, VIL=0V	25°C		-4.0	mA
Output Voltage Low	VOL	VCC=4.5V, VIH=2.25V IOL=50uA, VIL=0.8V	25°C		0.1	V
Output Voltage High	VOH	VCC=4.5V, VIH=2.25V IOH= -50uA, VIL=0.8V	25°C	VCC-0.1		V
Input Leakage Current	IIN	VCC=5.5V, VIN=VCC or GND	25°C		± 5	uA
Noise Immunity Functional Test	FN	VCC=4.5V, VIH=2.25V VIL=0.8V	25°C			
Input to Yn	TPHL	VCC=4.5V	25°C	2	20	ns
	TPLH	VCC=4.5V	25°C	2	22	ns

Burn-in and Operating Life Test , Delta Parameters (+25°C)		
PARAMETER	GROUP	DELTA LIMIT
ICC	5	3uA
IOL/IOH	5	-15% of 0 Hour

Radiation Tolerant AAHH32

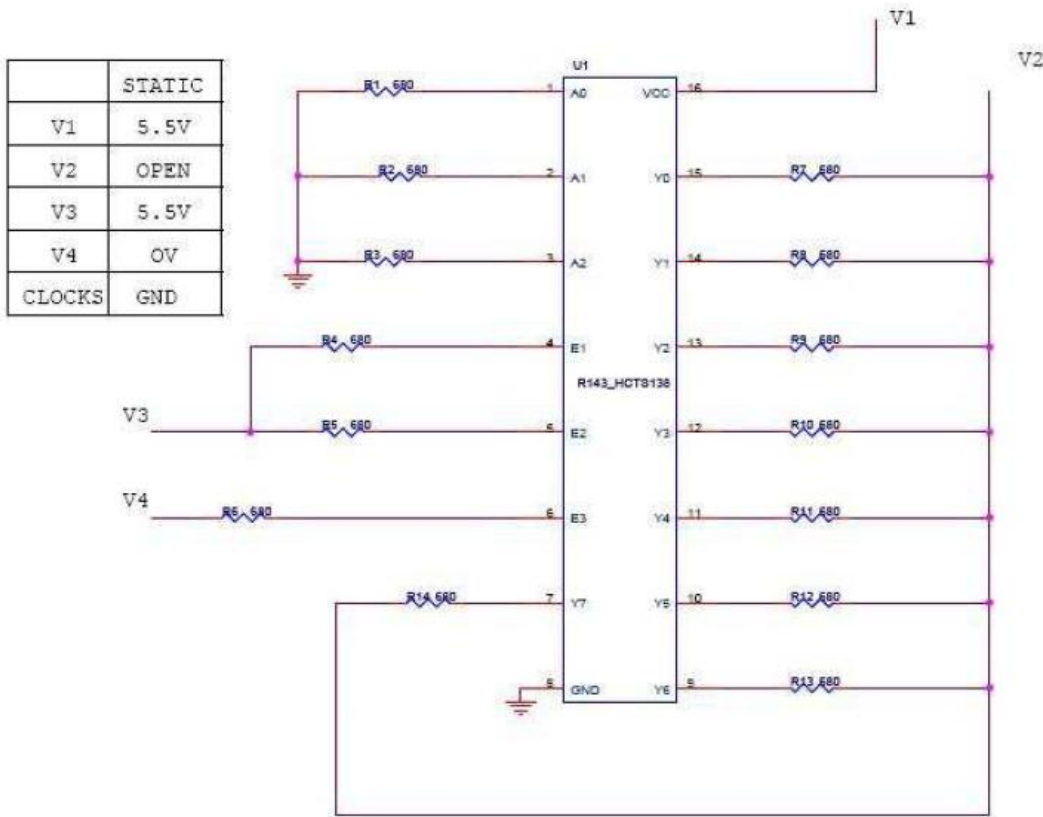
Dynamic Burn-In Schematic



	STATIC	DYNAMIC	TOLERANCES
V1	5.5V	5.5V	5%
V2	OPEN	2.75V	5%
V3	5.5V	0V	5%
V4	0V	5.5V	5%
PULSE1	GND	25KHz/0-5.5V	5%
PULSE2	GND	50KHz/0-5.5V	5%
PULSE3	GND	100KHz/0-5.5V	5%

Total Ionization Dose Schematic

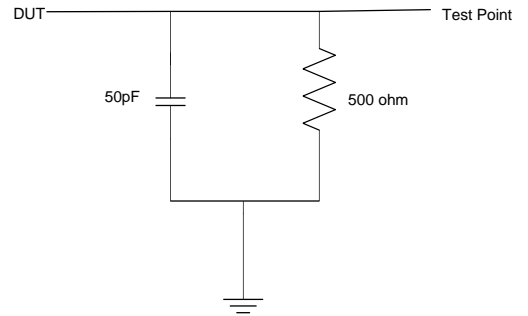
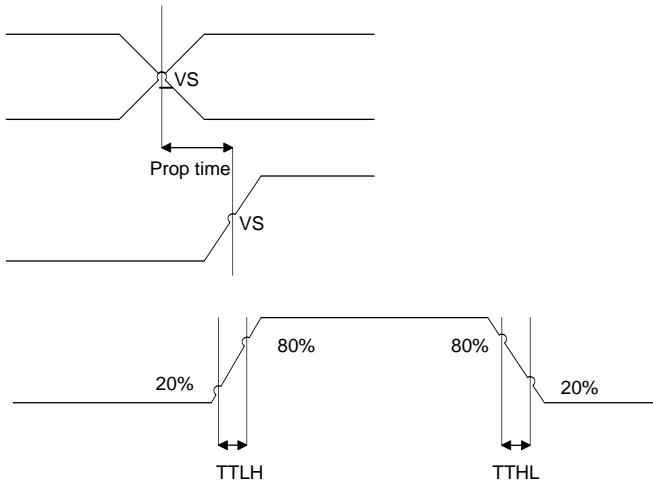
The biasing used for the TID irradiation exposure will be the same as the static burn-in one.



	STATIC
V1	5.5V
V2	OPEN
V3	5.5V
V4	0V
CLOCKS	GND

Radiation Tolerant AAHH32

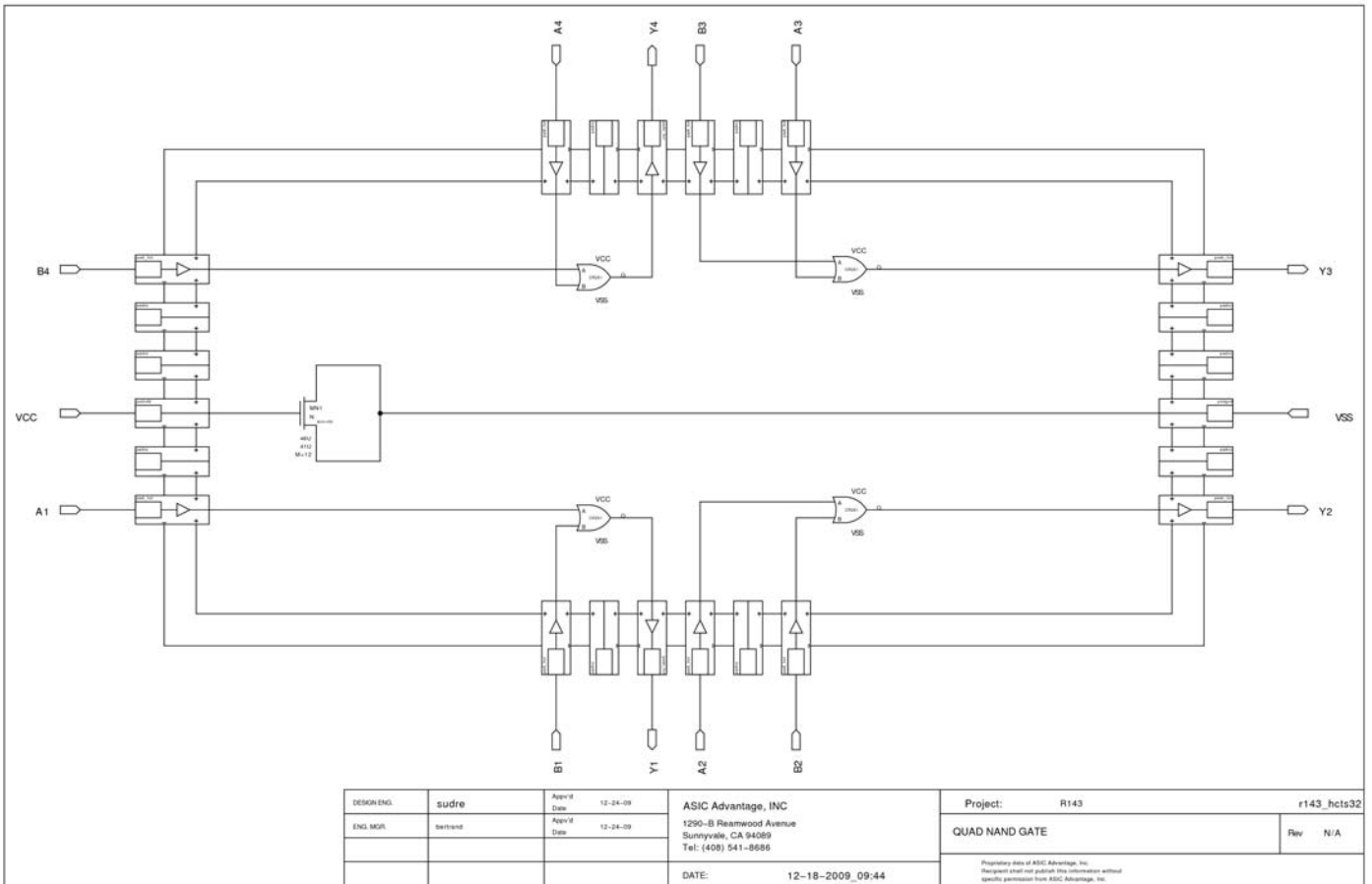
Timing Diagram



Note:

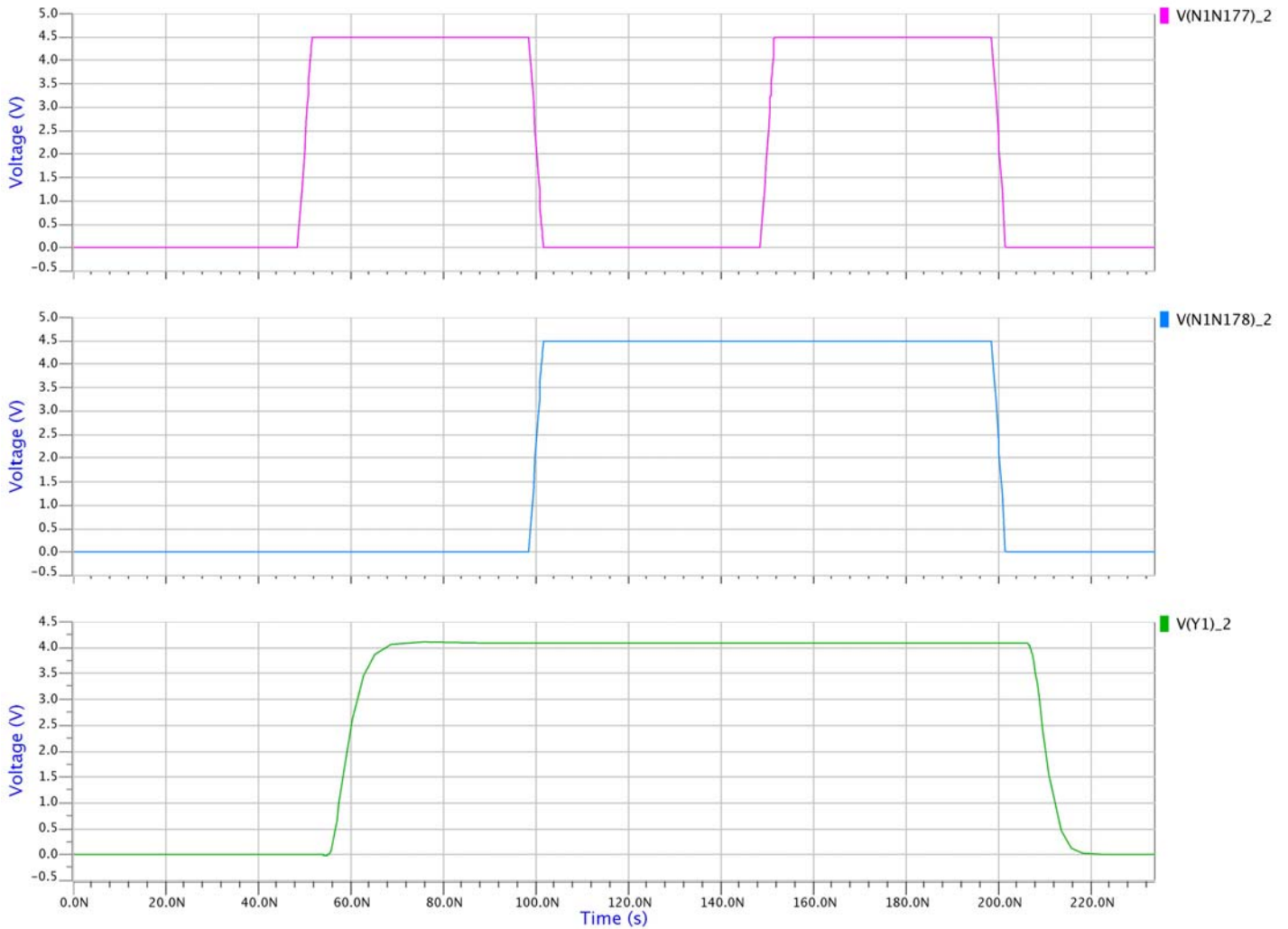
1. Rise/Fall time is 80% to 20%
2. The propagation time is measured at $V_S=1.3V$

Schematic



Radiation Tolerant AAHH32

Simulation Waveforms:



Simulation Readings

			-55°C	25°C	125°C		
Input to YN	TPHL	VCC=4.5V	nom	8.78	10.87	13.09	ns
			w.s	12.12	15	18.91	
			w.p	6.31	7.63	9.22	
Input to YN	TPLH	VCC=4.5V	nom	6.73	8.49	10.63	ns
			w.s	10.14	12.97	16.42	
			w.p	4.45	5.49	6.77	

Note: ** For DC characteristics simulation results please refer the pads document

Radiation Tolerant AAHH32

ORDERING INFORMATION

Ordering PN(1)	Subgroup	Description	Temp. Range	Package	Packing Type	Packing Qty
AAHH32 S-L14A	Digital Interface	Quad 2-Input OR Gate	S - Special -55°C to +125°C	14-Pin Ceramic DFP	TBD	TBD
AAHH32 S-DIE-W-S	Digital Interface	Quad 2-Input OR Gate	S - Special -55°C to +125°C	Die	Waffle Pack	TBD

- (1) Add "-S" or "-B" to end of part number to specify Class S or Class B screening levels.
Add "-ENGR" for engineering samples, contact factory for more information.
Die is only offered with Class S screening, as indicated.

PACKAGE DIMENSIONS AND MARKING

The AAHH32 devices are available in a 14-Pin Ceramic Dual-In-Line Flatpack package and in Die form. Refer to the latest version of specification AAPS001 (ASIC Advantage's "Package Numbering, Marking, and Outline Standard", available at www.asicadvantage.com, for specific information concerning the package dimensions and package marking.

Radiation Tolerant AAHH32

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- AAI's modified snubber network is patented under the US Patent # 6,233,165

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