



54F/74F540 • 54F/74F541 Octal Buffer/Line Driver with TRI-STATE® Outputs

General Description

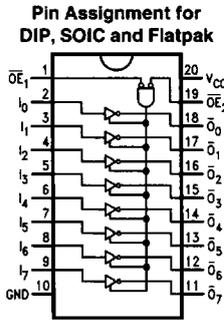
The 'F540 and 'F541 are similar in function to the 'F240 and 'F244 respectively, except that the inputs and outputs are on opposite sides of the package (see Connection Diagrams). This pinout arrangement makes these devices especially useful as output ports for microprocessors, allowing ease of layout and greater PC board density.

Features

- TRI-STATE outputs drive bus lines
- Inputs and outputs opposite side of package, allowing easier interface to microprocessors

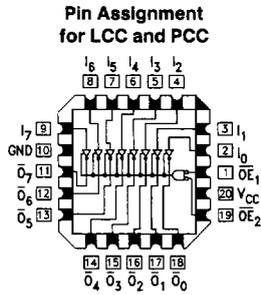
Ordering Code: See Section 5

Connection Diagrams

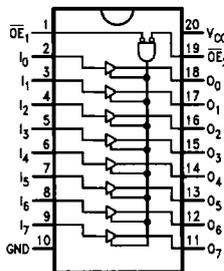


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'F540

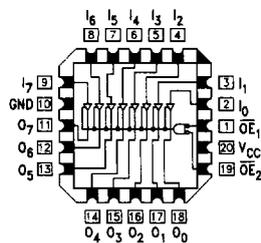


TL/F/9553-2



TL/F/9553-4

'F541



TL/F/9553-5

Unit Loading/Fan Out: See Section 2 for U.L. definitions

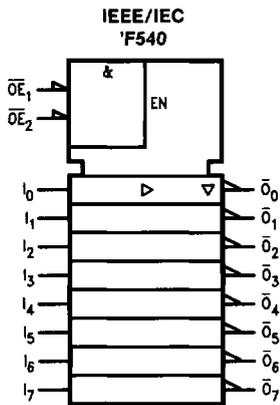
| Pin Names | Description | 54F/74F | |
|-----------------------------------|--|----------------|---|
| | | U.L. HIGH/LOW | Input I _H /I _L Output I _{OH} /I _{OL} |
| OE ₁ , OE ₂ | TRI-STATE Output Enable Input (Active LOW) | 1.0/1.0 | 20 μA/ -0.6 mA |
| I _n | Inputs | 1.0/1.0 | 20 μA/ -0.6 mA |
| O _n , O _n | Outputs | 600/106.6 (80) | -12 mA/64 mA (48 mA) |

Truth Table

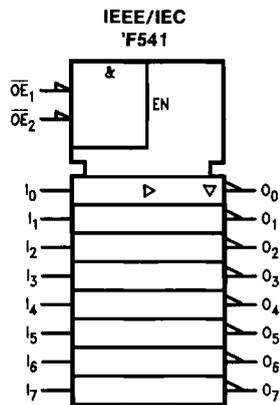
| Inputs | | | Outputs | |
|-------------------|-------------------|---|---------|-------|
| \overline{OE}_1 | \overline{OE}_2 | I | 'F540 | 'F541 |
| L | L | H | L | H |
| H | X | X | Z | Z |
| X | H | X | Z | Z |
| L | L | L | H | L |

H = HIGH Voltage Level
 L = LOW Voltage Level
 X = Immaterial
 Z = High Impedance

Logic Diagrams



TL/F/9553-3



TL/F/9553-6

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, contact the National Semiconductor Sales Office/Distributors for availability and specifications.

| | |
|---|--------------------------------------|
| Storage Temperature | -65°C to +150°C |
| Ambient Temperature under Bias | -55°C to +125°C |
| Junction Temperature under Bias | -55°C to +175°C |
| V _{CC} Pin Potential to Ground Pin | -0.5V to +7.0V |
| Input Voltage (Note 2) | -0.5V to +7.0V |
| Input Current (Note 2) | -30 mA to +5.0 mA |
| Voltage Applied to Output in HIGH State (with V _{CC} = 0V) | |
| Standard Output | -0.5V to V _{CC} |
| TRI-STATE Output | -0.5V to +5.5V |
| Current Applied to Output in LOW State (Max) | twice the rated I _{OL} (mA) |

Note 1: Absolute maximum ratings are values beyond which the device may be damaged or have its useful life impaired. Functional operation under these conditions is not implied.

Note 2: Either voltage limit or current limit is sufficient to protect inputs.

Recommended Operating Conditions

| | |
|------------------------------|-----------------|
| Free Air Ambient Temperature | |
| Military | -55°C to +125°C |
| Commercial | 0°C to +70°C |
| Supply Voltage | |
| Military | +4.5V to +5.5V |
| Commercial | +4.5V to +5.5V |

DC Electrical Characteristics

| Symbol | Parameter | 54F/74F | | | Units | V _{CC} | Conditions |
|------------------|-----------------------------------|--|--|--------------|-------|-----------------|---|
| | | Min | Typ | Max | | | |
| V _{IH} | Input HIGH Voltage | 2.0 | | | V | | Recognized as a HIGH Signal |
| V _{IL} | Input LOW Voltage | | | 0.8 | V | | Recognized as a LOW Signal |
| V _{CD} | Input Clamp Diode Voltage | | | -1.2 | V | Min | I _{IN} = -18 mA |
| V _{OH} | Output HIGH Voltage | 54F 10% V _{CC} 54F 10% V _{CC} 74F 10% V _{CC} 74F 10% V _{CC} 74F 5% V _{CC} 74F 5% V _{CC} | 2.4 2.0 2.4 2.0 2.7 2.0 | | V | Min | I _{OH} = -3 mA I _{OH} = -12 mA I _{OH} = -3 mA I _{OH} = -12 mA I _{OH} = -3 mA I _{OH} = -15 mA |
| V _{OL} | Output LOW Voltage | 54F 10% V _{CC} 74F 10% V _{CC} | | 0.55 0.55 | V | Min | I _{OL} = 48 mA I _{OL} = 64 mA |
| I _{IH} | Input HIGH Current | | | 20 | μA | Max | V _{IN} = 2.7V |
| I _{BVI} | Input HIGH Current Breakdown Test | | | 100 | μA | Max | V _{IN} = 7.0V |
| I _{IL} | Input LOW Current | | | -0.6 | mA | Max | V _{IN} = 0.5V |
| I _{OZH} | Output Leakage Current | | | 50 | μA | Max | V _{OUT} = 2.7V |
| I _{OZL} | Output Leakage Current | | | -50 | μA | Max | V _{OUT} = 0.5V |
| I _{OS} | Output Short-Circuit Current | | -100 | -225 | mA | Max | V _{OUT} = 0V |
| I _{CEX} | Output HIGH Leakage Current | | | 250 | μA | Max | V _{OUT} = V _{CC} |
| I _{ZZ} | Bus Drainage Test | | | 500 | μA | 0.0V | V _{OUT} = V _{CC} |
| I _{CCH} | Power Supply Current ('F540) | | 11 | 20 | mA | Max | V _O = HIGH |
| I _{CCL} | Power Supply Current ('F540) | | 53 | 75 | mA | Max | V _O = LOW |
| I _{CCZ} | Power Supply Current ('F540) | | 31 | 45 | mA | Max | V _O = HIGH Z |
| I _{CCH} | Power Supply Current ('F541) | | 26 | 35 | mA | Max | V _O = HIGH |
| I _{CCL} | Power Supply Current ('F541) | | 55 | 75 | mA | Max | V _O = LOW |
| I _{CCZ} | Power Supply Current ('F541) | | 31 | 55 | mA | Max | V _O = HIGH Z |

AC Electrical Characteristics: See Section 2 for Waveforms and Load Configurations

| Symbol | Parameter | 74F | | | 54F | | 74F | | Units | Fig No |
|--------------------------------------|---|---|------------|-------------|--|-------------|--|-------------|-------|--------|
| | | T _A = +25°C V _{CC} = +5.0V C _L = 50 pF | | | T _A , V _{CC} = Mil C _L = 50 pF | | T _A , V _{CC} = Com C _L = 50 pF | | | |
| | | Min | Typ | Max | Min | Max | Min | Max | | |
| t _{PLH} t _{PHL} | Propagation Delay Data to Output ('F540) | 1.5 1.0 | 3.0 2.0 | 5.0 4.0 | 1.0 1.0 | 6.0 4.5 | 1.0 1.0 | 5.5 4.0 | ns | 2-3 |
| t _{pZH} t _{pZL} | Output Enable Time ('F540) | 2.5 3.5 | 4.9 5.8 | 8.0 10.0 | 2.5 3.5 | 9.0 11.0 | 2.5 3.5 | 8.5 10.5 | ns | 2-5 |
| t _{PHZ} t _{PLZ} | Output Disable Time ('F540) | 1.5 1.0 | 3.4 2.5 | 6.0 5.5 | 1.5 1.0 | 7.0 7.5 | 1.5 1.0 | 6.5 6.0 | | |
| t _{PLH} t _{PHL} | Propagation Delay Data to Output ('F541) | 1.5 1.5 | 3.3 2.7 | 5.5 5.5 | | | 1.5 1.5 | 6.0 6.0 | ns | 2-3 |
| t _{pZH} t _{pZL} | Output Enable Time ('F541) | 3.0 3.5 | 5.8 6.1 | 8.0 8.5 | | | 2.5 3.0 | 9.5 9.5 | ns | 2-5 |
| t _{PHZ} t _{PLZ} | Output Disable Time ('F541) | 1.5 1.5 | 3.4 2.9 | 6.0 5.5 | | | 1.5 1.5 | 6.5 6.0 | | |