

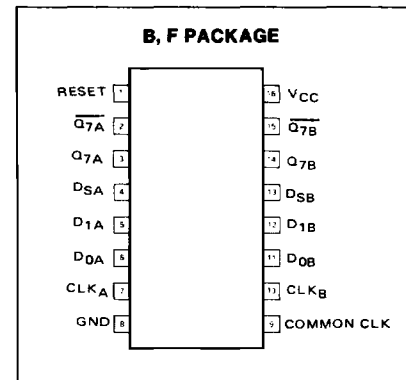
Product available in 0° to 75°C temp. range only.

**DESCRIPTION**

The 8277 is a dual 8-Bit Shift Register which provides the designer with sixteen (16) bits of serial storage operating at a typical shift rate of 20MHz. Features of the 8277 are:

1. TRUE and COMPLEMENT outputs are provided on each register's eighth bit.
2. Positive edge triggering on clock input.
3. SEPARATE CLOCK lines (pins 7 and 10) for each 8-bit register are provided as well as a COMMON CLOCK line (pin 9) for all sixteen storage bits.
4. Common RESET (pin 1).
5. AND-OR gating to the input of each 8-bit register is provided to accomplish the multiplex function.
6. Direct replacement for 9328.

**PIN CONFIGURATION**



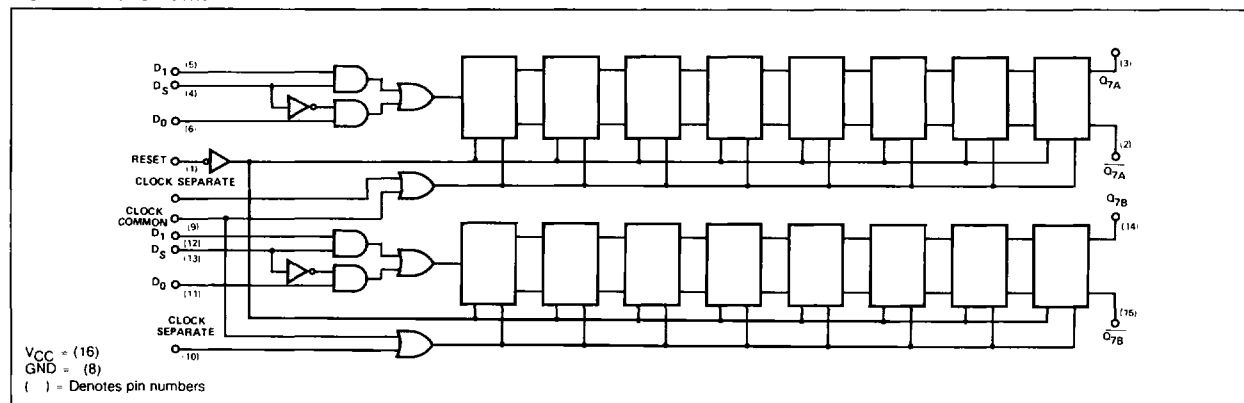
**SWITCHING CHARACTERISTICS**  $T_A=25^\circ\text{C}$ ,  $V_{CC}=5\text{V}$

PARAMETER	LIMITS			UNIT
	MIN	TYP	MAX	
$t_{on}$ Turn-on delay		25	40	ns
	Clock to output			
	Reset to output			
$t_{off}$ Turn-off delay		25	40	ns
	Clock to output			
	Reset to output			
$t_w$ Width of clock pulse	15			ns
	Shift rate	15	20	MHz
$t_{setup}$ Data setup time		20	30	ns
$t_{Hold}$ Data hold time			5	ns

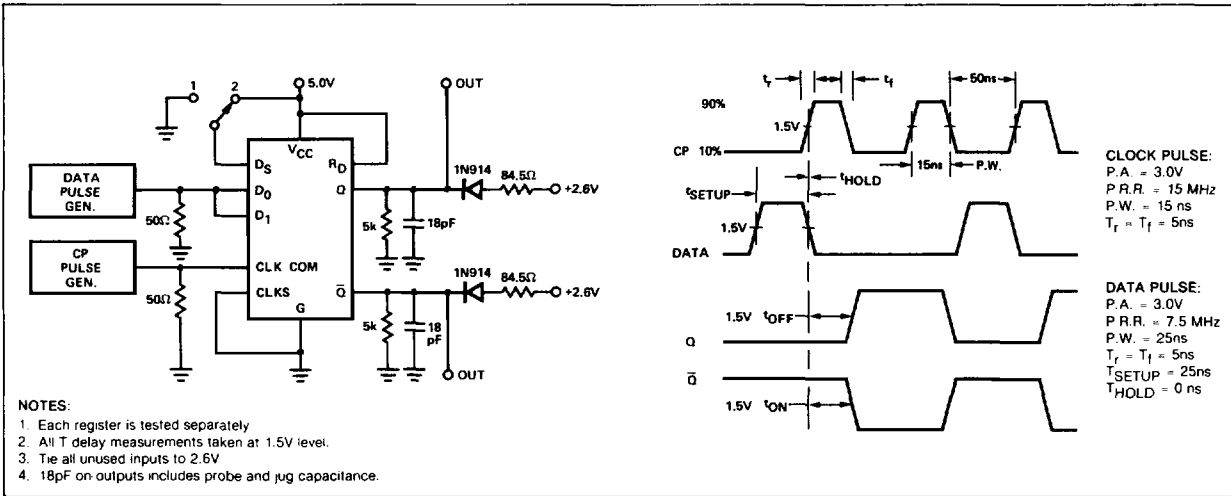
**TRUTH TABLE**

$D_S$	$D_0$	$D_1$	Reset	Function
0	0	x	1	Shift in "0"
0	1	x	1	Shift in "1"
1	x	0	1	Shift in "0"
1	x	1	1	Shift in "1"
x	x	x	0	Reset "Q" to "0"

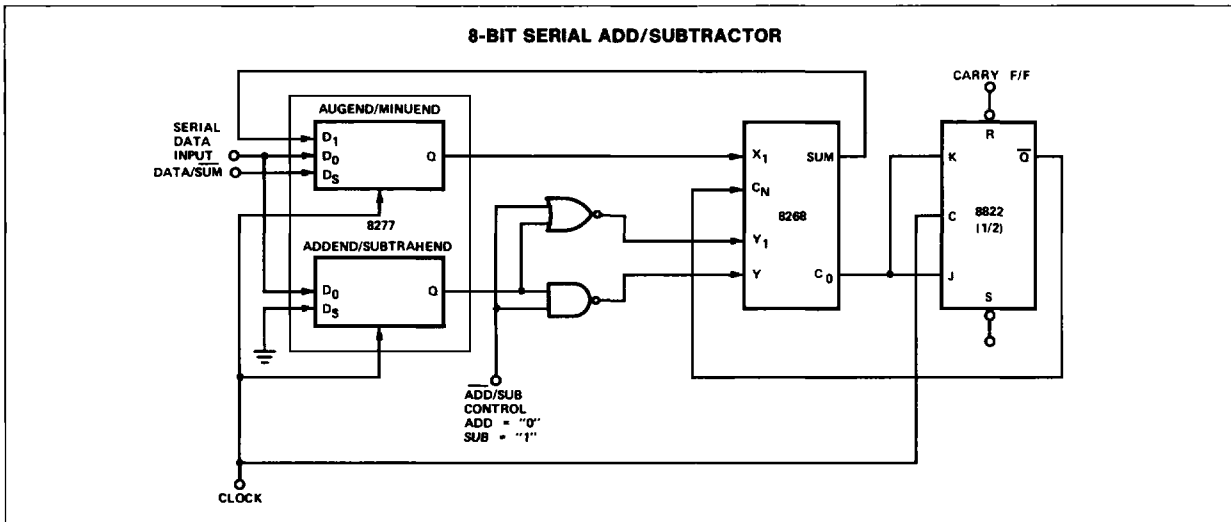
**LOGIC DIAGRAM**



AC TEST FIGURE AND WAVEFORMS



TYPICAL APPLICATION



10901