

FAIRCHILD DIGITAL

TTL

MULTIPLEXERS

Item	Function	DEVICE NO.	Enable Inputs	True Output ⁽¹⁾	Complement Output ⁽¹⁾	Select Delays (Typ) ns	Enable Delays (Typ) ns	Data Delays (Typ) ns	Power Dissipation mW (Typ)	Fan-Out (UL) ⁽²⁾	Logic/Connection Diagram	Package(s)
1	Quad 2-Input	9322	1	X	—	18	14	90	150	10	D157	4L,7B,9B
2	Quad 2-Input	93L22	1	X	—	23	20	14	45	50	D157	4L,7B,9B
3	Quad 2-Input	54/74157	1	X	—	18	14	90	150	10	D157	4L,7B,9B
4	Quad 2-Input	54LS/74LS157	1	X	—	18	14	90	49	50	D157	4L,6B,9B
5	Quad 2-Input	54S/74S157	1	X	—	10	80	50	250	125	D157	4L,6B,9B
6	Quad 2-Input	54LS/74LS158	1	—	X	16	12	70	24	50	D157	4L,6B,9B
7	Quad 2-Input	54S/74S158	1	—	X	80	70	40	195	125	D157	4L,6B,9B
8	Quad 2-Input	54LS/74LS257	1	3S	—	14	16	12	50	50	D157	4L,6B,9B
9	Quad 2-Input	54S/74S257	1	3S	—	10	13	50	320	125	D157	4L,6B,9B
10	Quad 2-Input	54LS/74LS258	1	—	3S	12	16	10	35	50	D157	4L,6B,9B
11	Quad 2-Input	54S/74S258	1	—	3S	80	13	40	280	125	D157	4L,6B,9B
12	Quad 2-Input	54/74298	Clocked (edge-trigger)	X Latched	—	—	20	—	195	10	D156	4L,7B,9B
13	Quad 2-Input	54LS/74LS298	Clocked (edge-trigger)	X Latched	—	—	20	—	65	50	D156	4L,6B,9B
14	Dual 4-Input	9309	—	X	X	15	—	10	150	10	D155	4L,6B,9B
15	Dual 4-Input	93L09	—	X	X	45	—	30	38	50	D155	4L,6B,9B
16	Dual 4-Input	54/74153	2	X	—	22	19	15	180	10	D158	4L,6B,9B
17	Dual 4-Input	54LS/74LS153	2	X	—	18	16	10	31	50	D158	4L,6B,9B
18	Dual 4-Input	54S/74S153	2	X	—	12	10	60	225	125	D158	4L,6B,9B
19	Dual 4-Input	54LS/74LS253	2	3S	—	18	16	10	43	50	D158	4L,6B,9B
20	Dual 4-Input	54S/74S253	2	3S	—	12	13	60	325	125	D158	4L,6B,9B
21	Dual 4-Input	54LS/74LS352	2	—	X	17	15	80	31	50	D180	4L,6B,9B
22	Dual 4-Input	54LS/74LS353	2	3S	3S	20	12	10	42	50	D181	4L,6B,9B
23	8-Input	9312	1	X	X	18	15	10	135	10	D159	4L,7B,9B
24	8-Input	93L12	1	X	X	54	45	30	36	50	D159	4L,7B,9B
25	8-Input	93S12	1	X	X	12	10	70	190	125	D159	4L,7B,9B
26	8-Input	9313	1	X	OC	25	22	18	135	10	D159	4L,7B,9B

1 OC = open collector, 3S = 3-state
 2 Unit Load (UL) = 40 μA HIGH/16 mA LOW

FAIRCHILD LOGIC/CONNECTION DIAGRAMS

DIGITAL - TTL

