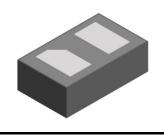


## **Uni-directional 5V Ultra Low Capacitance ESD Protector**

### **Description**

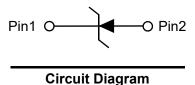
The PESDUC2XD5VUF protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, low operating voltage. It gives designer the flexibility to protect one unidirectional line in applications where arrays are not practical.



DFN0603-2L(Bottom View)

### **Feature**

- ➤ DFN0603-2L package
- ➤ Replacement for MLV(0201)
- > Response time is typically < 1 ns
- ➤ Low clamping voltage
- > RoHS compliant
- ➤ Transient protection for data lines to IEC 61000-4-2(ESD) ±25KV(air), ±25KV(contact); IEC 61000-4-4 (EFT) 40A (5/50ns)





Marking (Top View)

### **Applications**

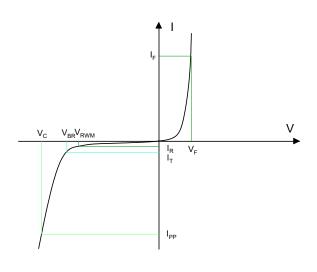
- Cellular phones
- > Portable devices
- Digital cameras
- > Power supplies

### **Mechanical Characteristics**

- Mounting position: Any
- Qualified max reflow temperature:260°C
- > Device meets MSL 1 requirements
- > DFN0603-2L without plating

## **Electronics Parameter**

Symbol	Parameter
$V_{RWM}$	Peak Reverse Working Voltage
I <sub>R</sub>	Reverse Leakage Current @ V <sub>RWM</sub>
$V_{BR}$	Breakdown Voltage @ I <sub>T</sub>
I <sub>T</sub>	Test Current
I <sub>PP</sub>	Maximum Reverse Peak Pulse Current
V <sub>C</sub>	Clamping Voltage @ I <sub>PP</sub>
P <sub>PP</sub>	Peak Pulse Power
C <sub>J</sub>	Junction Capacitance
I <sub>F</sub>	Forward Current
V <sub>F</sub>	Forward Voltage @ I <sub>F</sub>



## Electrical characteristics per line@25°C (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Peak Reverse Working Voltage	$V_{RWM}$	-	-	-	5.0	V
Breakdown Voltage	$V_{BR}$	I <sub>t</sub> = 1mA	5.5	7.0	8.5	V
Reverse Leakage Current	I <sub>R</sub>	V <sub>RWM</sub> = 5V	-	-	1.0	μA
Forward Voltage	$V_{F}$	I <sub>F</sub> = 10mA	0.8	•	1.25	V
Clamping Voltage1)	V <sub>C</sub>	$I_{PP} = 1A, t_{P} = 8/20 \mu s$	-	9.0	10.5	V
Clamping Voltage <sup>1)</sup>		$I_{PP} = 4A, t_P = 8/20 \mu s$	-	11	14	V
Junction Capacitance	CJ	$V_R = 0V, f = 1MHz$	-	0.5	0.65	pF

Notes

## Absolute maximum rating@25°C

Rating	Symbol	Value	Units
Peak Pulse Power ( t <sub>P</sub> = 8/20µs )	P <sub>PP</sub>	45	W
Peak Pulse Current ( t <sub>P</sub> = 8/20μs )	I <sub>PP</sub>	4.0	А
Lead Soldering Temperature	T <sub>L</sub>	260 (10 sec)	°C
Junction and Storage Temperature Range	$T_{J,}T_{STG}$	-55~+150	°C
ESD Protection-Contact Discharge	V <sub>ESD</sub>	±25	kV
ESD Protection-Air Discharge	V <sub>ESD</sub>	±25	kV

<sup>1.</sup> Non-repetitive current pulse, according to IEC61000-4-5.

# Typical Characteristics

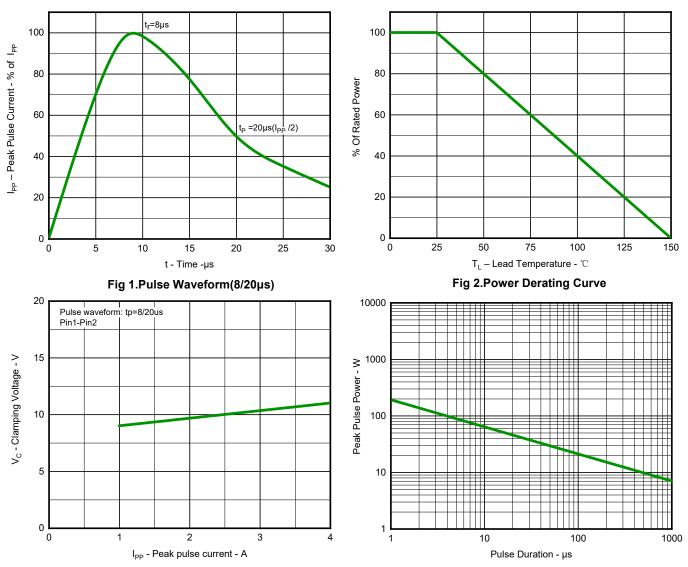
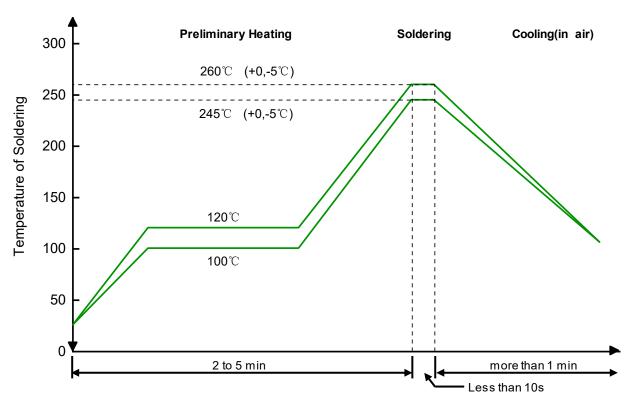


Fig 3. Clamping voltage vs. Peak pulse current

Fig 4. Non Repetitive Peak Pulse Power vs. Pulse time

### **Solder Reflow Recommendation**



Remark: Pb free for 260°C; Pb for 245°C.

## **PCB** Design

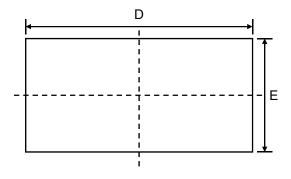
For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

- > Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- > Do not make false economies and save copper for the ground connection.
- Place via holes to ground as close as possible to the anode of the TVS diode.
- Use as many via holes as possible for the ground connection.
- > Keep the length of via holes in mind! The longer the more inductance they will have.

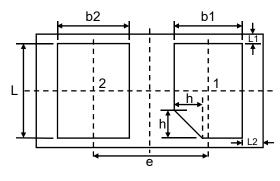
### **Ordering information**

Device	Package	Reel	Shipping
PESDUC2XD5VUF	DFN0603-2L (Pb-Free)	7"	12000 / Tape & Reel

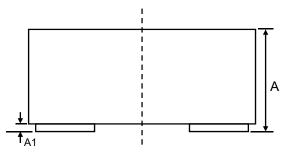
# Product dimension (DFN0603-2L)



Top View

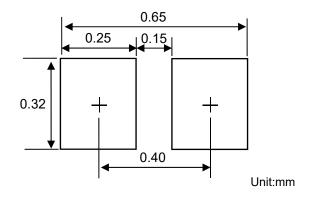


**Bottom View** 



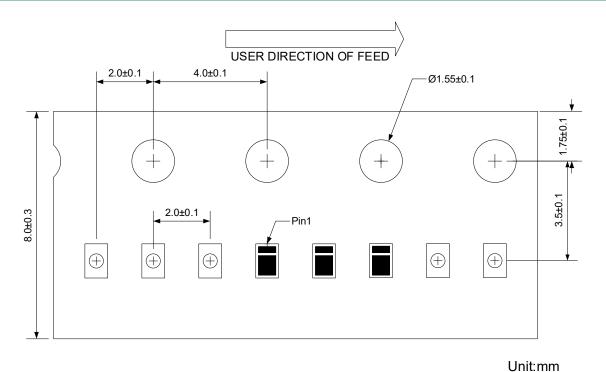
Side View

Dim	Millimeters		Inches		
Dim	Min	Max	Min	Max	
А	0.28	0.35	0.011	0.014	
A1	0.00	0.05	0.000	0.002	
b1	0.13	0.23	0.005	0.009	
b2	0.14	0.24	0.006	0.009	
D	0.55	0.65	0.022	0.026	
е	0.35 BSC		0.014 BSC		
L1	0.025	BSC	0.001	BSC	
L2	0.035 BSC		0.001 BSC		
E	0.25	0.35	0.010	0.014	
L	0.20	0.30	0.008	0.012	
h	0.00	0.10	0.000	0.004	



Suggested PCB Layout

# Load with information



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