

SRA2212UF

PNP Silicon Transistor

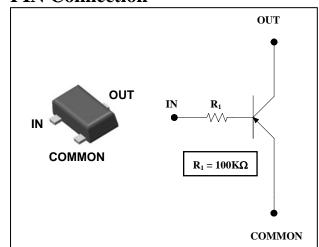
Descriptions

- Switching application
- Interface circuit and driver circuit application

Features

- With built-in bias resistor
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

PIN Connection



Ordering Information

Type NO.	Marking	Package Code
SRA2212UF	<u>BR</u> □ ① ②	SOT-323F

1) Device Code 2) Year&Week Code

Absolute Maximum Ratings

(Ta=25°C)

Characteristic	Symbol	Rating	Unit
Output voltage	Vo	-50	V
Input voltage	VI	-40, 5	V
Output current	I _O	-100	mA
Power dissipation	P_{D}	200	mW
Junction temperature	T_J	150	°C
Storage temperature range	T_{stg}	-55 ~ 150	°C

Electrical Characteristics

 $(Ta=25^{\circ}C)$

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Output cut-off current	I _{O(OFF)}	$V_0 = -50V, V_1 = 0$	-	-	-500	nA
DC current gain	Gı	$V_O = -5V$, $I_O = -10$ mA	120	-	-	-
Output voltage	V _{O(ON)}	I _O =-10mA, I _I =-0.5mA	-	-0.1	-0.3	V
Input voltage (ON)	$V_{I(ON)}$	$V_{O} = -0.2V$, $I_{O} = -5mA$	-	-	-4.4	V
Input voltage (OFF)	$V_{I(OFF)}$	$V_{O} = -5V$, $I_{O} = -0.1$ mA	-0.3	-	-	V
Transition frequency	f _T *	$V_O = -10V$, $I_O = -5$ mA, $f = 1$ MHz	-	200	-	MHz
Input current	I_1	$V_1 = -5V, I_0 = 0$	-	-	-0.1	mA
Input resistor (Input to base)	R ₁	-	70	100	130	ΚΩ

^{* :} Characteristic of transistor only

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Electrical Characteristic Curves

Fig. 1 I_O - V_{I(ON)}

-100

-10

-10

-10

-10

-10

-25°C

Input on voltage Vi(on) [V]

Fig. 2 I_O - $V_{I(OFF)}$

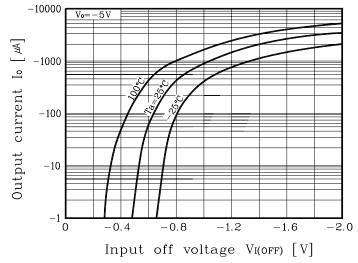
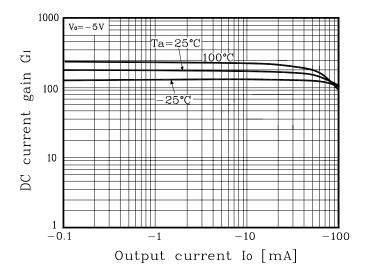


Fig. 3 G_I - I_O

Output current $lo\ [mA]$

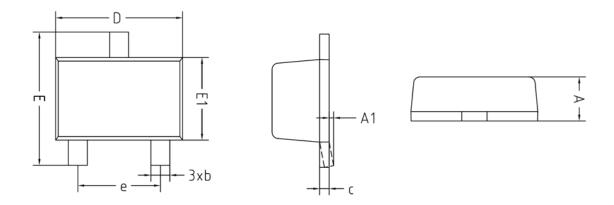
-0.1

-0.01 -0.01



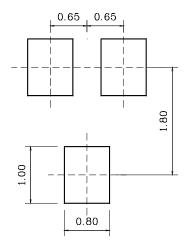
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Outline Dimension



SYMBOL	MILLIMETERS			NOTE	
STINDUL	MINIMUM	NOMINAL	MAXIMUM	NOTE	
Α	0.60	-	0.80		
A1	0.00	-	0.10		
Ь	0.30	-	0.40		
С	0.08	-	0.16		
D	1.90	2.00	2.10		
Е	1.95	2.10	2.25		
E1	1.20	1.30	1.40		
е	1.30BSC				

*Recommend PCB solder land [Unit: mm]



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