



WE05-4RT3

Transient Voltage Suppressor

Features

- Low operating voltage: 5V
- Ultra-low capacitance
- Solid-state silicon-avalanche
- Low Leakage Current
- Lead-free version available

IEC COMPATIBILITY (EN61000-4)

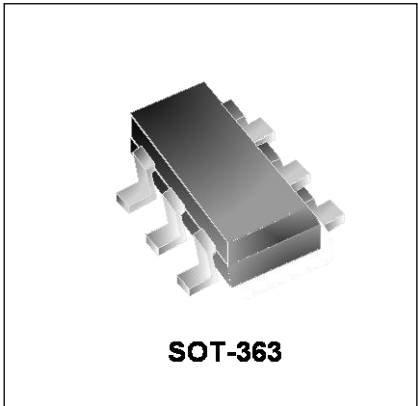
- IEC 61000-4-2 (ESD) $\pm 30\text{kV}$ (air), $\pm 25\text{kV}$ (contact)
- IEC 61000-4-4 (EFT) 40A (5/50ns)
- IEC 61000-4-5 (Lightning) 4.5A (8/20 μs)

Mechanical Characteristics

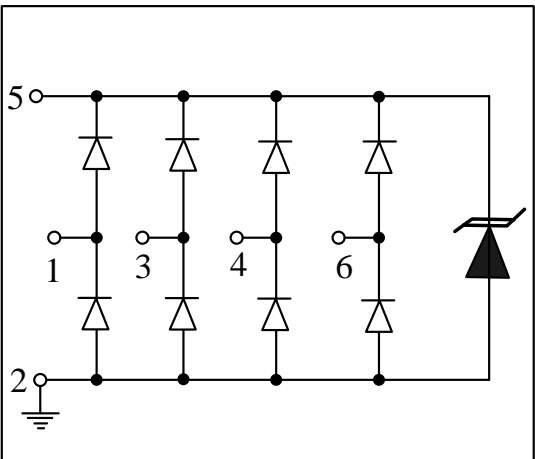
- SOT-363 package
- Marking : Marking Code
- Packaging : Tape and Reel
- RoHS Compliant

Applications

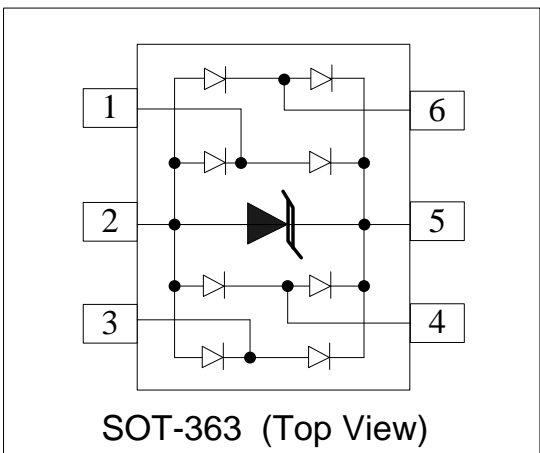
- Video/Graphics Card
- Digital Visual Interface (DVI)
- USB2.0 Power and Data lines protection
- Notebook and PC Computers
- Monitors and Flat Panel Displays



Circuit Diagram



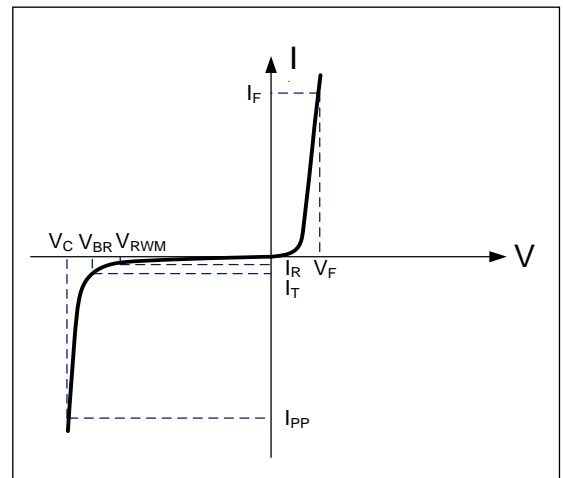
Schematic & PIN Configuration



Absolute Maximum Rating			
Rating	Symbol	Value	Units
Peak Pulse Power ($t_p = 8/20\mu s$)	P_{PP}	56	Watts
Peak Pulse Current ($t_p = 8/20\mu s$)	I_{PP}	4.5	A
Operating Temperature	T_J	-55 to + 85	°C
Storage Temperature	T_{STG}	-55 to +150	°C

Electrical Parameters (T=25°C)

Symbol	Parameter
I_{PP}	Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_{PP}
V_{RWM}	Reverse Stand-Off Voltage
I_R	Reverse Leakage Current @ V_{RWM}
V_{BR}	Breakdown Voltage @ I_T
I_T	Test Current
I_F	Forward Current
V_F	Forward Voltage @ I_F



Electrical Characteristics

WE05-4RT3						
Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	V_{RWM}				5	V
Reverse Breakdown Voltage	V_{BR}	$I_T=1mA$	6		8.5	V
Reverse Leakage Current	I_R	$V_{RWM}=5V, T=25^\circ C$			500	nA
Forward Voltage	V_F	$I_T=10mA$	0.5	0.8	1	V
Clamping Voltage	V_C	$I_{PP}=4.5A, t_p=8/20\mu s$		10.5	12.5	V
ESD Clamping Voltage ¹	V_C	$I_{PP} = 4A$ $t_p = 0.2/100ns$		9.0		V
ESD Clamping Voltage ¹	V_C	$I_{PP} = 16A$ $t_p = 0.2/100ns$		12.6		V
Dynamic Resistance ^{1,2}	R_{DYN}	TLP=0.2/100ns		0.3		Ω
Junction Capacitance	C_j	VR = 0V, f = 1MHz I/O pin to GND		0.6	1.5	pF
		VR = 0V, f = 1MHz Between I/O pins		0.3	0.6	pF

Note: 1、 TLP Setting : $t_p=100ns, t_r=0.2ns, I_{TLP}$ and V_{TLP} sample window: $t_1=70ns$ to $t_2=90ns$.
 2、 Dynamic resistance calculated from $I_{PP}=4A$ to $I_{PP}=16A$ using "Best Fit"

Typical Characteristics

Figure 1: Peak Pulse Power Vs Pulse Time

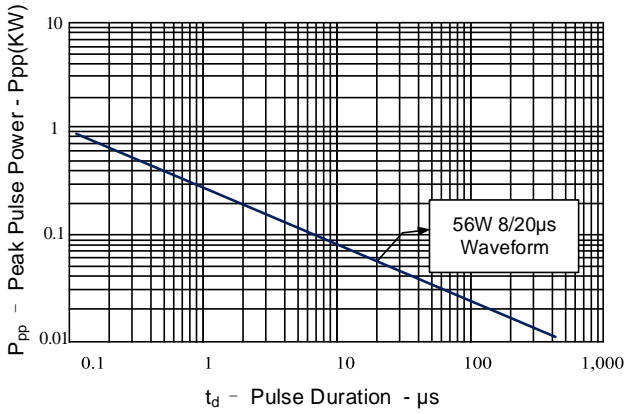


Figure 2: Power Derating Curve

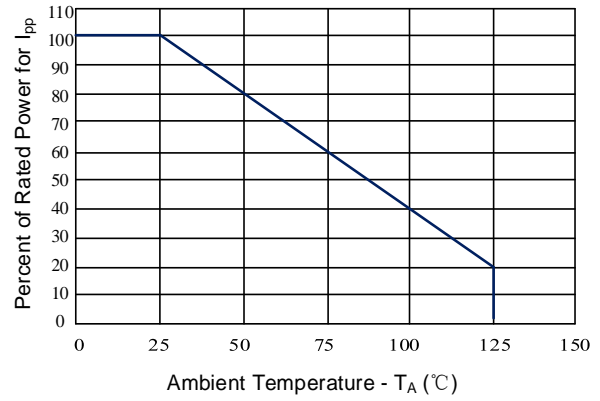


Figure 3: Clamping Voltage vs. Peak Pulse Current

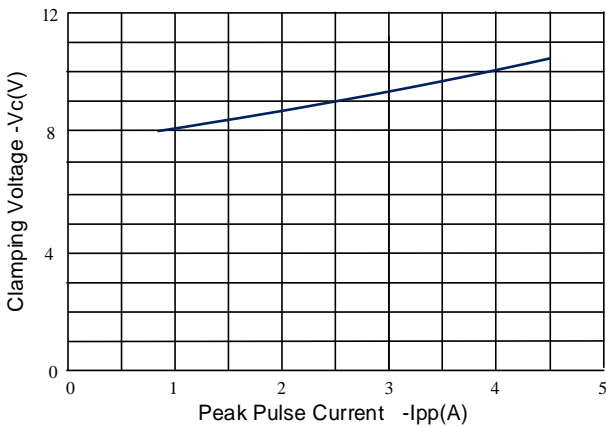


Figure 4: Normalized Junction Capacitance vs. Reverse Voltage

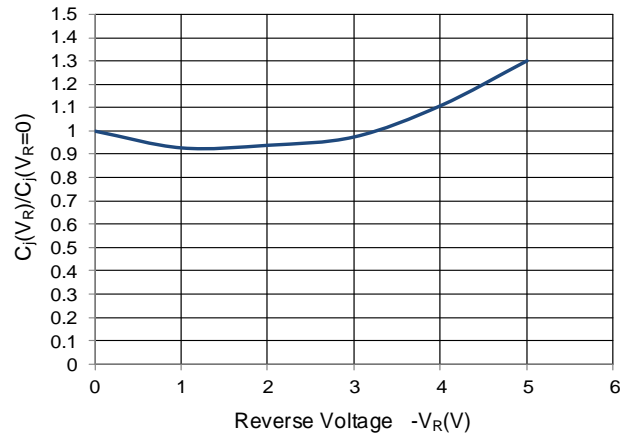


Figure 5: Pulse Waveform

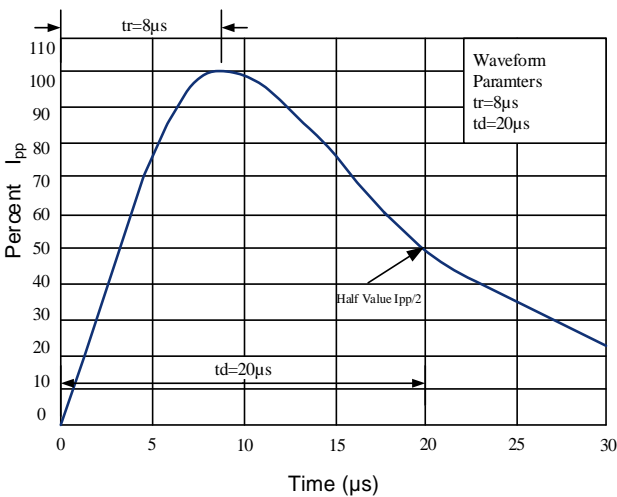
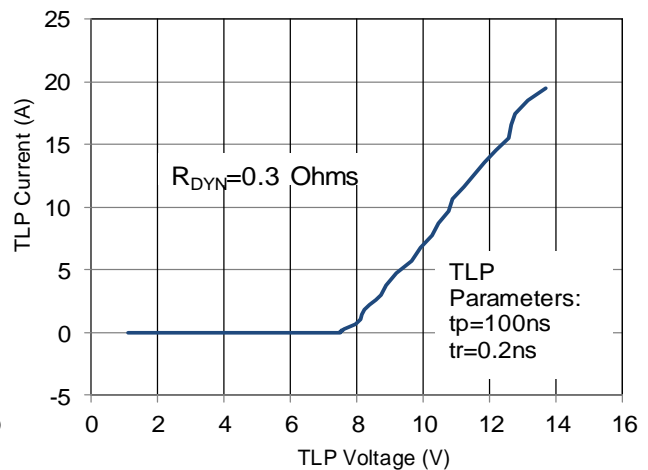
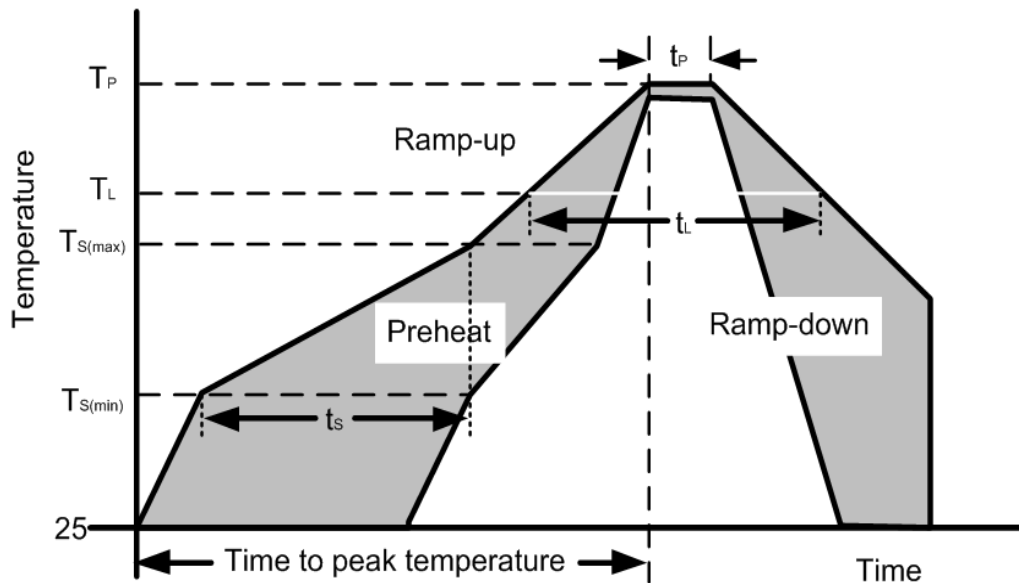


Figure 6: TLP I-V Curve



Soldering Parameters

Reflow Condition		Pb – Free assembly
Pre Heat	Temperature Min ($T_{S(min)}$)	150°C
	Temperature Max ($T_{S(max)}$)	200°C
	Time (min to max) (t_s)	60 – 190 secs
Average ramp up rate (Liquidus Temp) (T_L) to peak		5°C/second max
$T_{S(max)}$ to T_L —Ramp-up Rate		5°C/second max
Reflow	Temperature (T_L) (Liquidus)	217°C
	Temperature (t_L)	60 – 150 seconds
Peak Temperature (T_P)		260+0/-5 °C
Time within actual peak Temperature (t_p)		20 – 40 seconds
Ramp-down Rate		5°C/second max
Time 25°C to peak Temperature (T_P)		8 minutes Max.
Do not exceed		280°C



Outline Drawing – SOT-363

PACKAGE OUTLINE

SEE DETAIL A

DETAIL A

SOT-363

DIMENSIONS				
SYMBOL	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.900	1.100	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.150	0.350	0.006	0.014
c	0.100	0.150	0.004	0.006
D	2.000	2.200	0.079	0.087
E1	1.150	1.350	0.045	0.053
E	2.150	2.450	0.085	0.096
e	0.650 TYP.		0.026 TYP.	
e1	1.200	1.400	0.047	0.055
L	0.525 REF.		0.021 REF.	
L1	0.260	0.460	0.010	0.018
θ	0°	8°	0°	8°

DIMENSIONS		
DIM	INCHES	MILLIMETERS
Z	0.110	2.79
G	0.043	1.09
C	0.076	1.94
P	0.026 TYP	0.65 TYP
X	0.016	0.40
Y	0.033	0.85

Notes:

Controlling Dimension: Millimeter.

Marking Codes

Part Number	Marking Code
WE05-4RT3	5T

Package Information

Qty: 3k/Reel

CONTACT INFORMATION

No.1001, Shiwan (7) Road, Pudong District, Shanghai, P.R.China.201207
 Tel: 86-21-68969993 Fax: 86-21-50757680 Email: market@way-on.com
 WAYON website: <http://www.way-on.com>
 For additional information, please contact your local Sales Representative.

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Specifications are subject to change without notice.
 The device characteristics and parameters in this data sheet can and do vary in different applications and actual device performance may vary over time.
 Users should verify actual device performance in their specific applications.