

BCT0102 2-Bit Bidirectional Voltage-Level Translator

GENERAL DESCRIPTION

This two-bit non-inverting translator is a bidirectional voltage-level translator and can be used to establish digital switching compatibility between mixed-voltage systems. It uses two separate configurable power-supply rails, with the A ports supporting operating voltages from 1.65V to 5.5V while it tracks the VCCA supply, and the B ports supporting operating voltages from 2.3V to 5.5V while it tracks the VCCB supply. This allows the support of both lower and higher logic signal levels while providing bidirectional translation capabilities between any of the 1.8V, 2.5V, 3.3V, and 5V voltage nodes.

When the output-enable (OE) input is low, all I/Os are placed in the high-impedance state, which significantly reduces the power-supply quiescent current consumption. OE has an internal pull-down current source, as long as VCCA is powered.

To ensure the high-impedance state during power up or power down, OE should be tied to GND through a pull-down resistor; the minimum value of the resistor is determined by the current-sourcing capability of the driver.

FEATURES

- No Direction-Control Signal Needed
- Data Rates
 24Mbps (Push-Pull)
 2Mbps (Open-Drain)
- 1.65V to 5.5V on A Ports and 2.3V to 5.5V on B Ports (VCCA \leq VCCB)
- VCC Isolation: If Either VCC is at GND, Both Ports are in the High-Impedance State
- No Power-Supply Sequencing Required, Either VCCA or VCCB can be Ramped First
- I_{OFF}: Supports Partial-Power-Down Mode Operation
- Available in TSOT23-8, XTDFN1.4X1.0-8L, QFN1.4X1.2-8L package.

APPLICATIONS

I2C BUS Portable POS Systems Smart Card Readers Cell-Phones GPS Portable Communication Devices

Order Number	Package Type	Temperature Range	Marking	QTY/Reel
BCT0102EDA-TR	TSOT23-8	-40°C to +85°C	SAXX	4000
BCT0102ETA-TR	XTDFN1.4X1.0-8L	-40°C to +85°C	SAXX	3000
BCT0102EGA-TR	QFN1.4X1.2-8L	-40°C to +85°C	SAXX	3000

ORDERING INFORMATION

Note 1: "SA" in Marking is product short code for BCT0102. "XX" in Marking will be appeared as the batch code.



PIN CONFIGURATION (Top View)

BCT0102 2-Bit Bidirectional Voltage-Level Translator

VCCA VCCB 8 VCCA 1 8 VCCB MARKING A1 7 Β1 B1 7 A1 2 6 B2 3 A2 A2 3 6 B2 OE 5 GND 4 GND 4 OE 5 **TSOT23-8** XTDFN1.4X1.0-8L VCCA 1 A1 8 VCCB 2 Β1 A2 7 3 4 6 GND B2 5 OE QFN1.4X1.2-8L

PIN DESCRIPTION

	PIN	NAME	FUNCTION
SOT	DFN/QFN		FUNCTION
1	8	V _{CCB}	B-Port Supply Voltage. 2.3V≤V _{CCB} ≤5.5V.
2	7	B1	Input/Output B. Referenced to V _{CCB} .
3	6	B2	Input/Output B. Referenced to V _{CCB} .
4	Б	5 OE	Output Enable (Active High). Pull OE low to place all outputs in 3-state
4	5		mode. Referenced to V _{CCA} .
5	4	GND	Ground.
6	3	A2	Input/Output A. Referenced to V _{CCA} .
7	2	A1	Input/Output A. Referenced to V _{CCA} .
8	1	V _{CCA}	A-Port Supply Voltage. 1.65V \leq V _{CCA} \leq 5.5V and V _{CCA} \leq V _{CCB} .



ABSOLUTE MAXIMUM RATINGS

V _{CCA} , Supply Voltage Range	-0.3V to 6V
V _{CCB} , Supply Voltage Range	-0.3V to 6V
V _I , A Port, B Port, OE Input Voltage Range (2)	0.3V to 6V
Vo, Voltage Range Applied to Any Output in the High- Impedance or Power-Off State (2)	
A Port	0.3V to 6V
B Port	0.3V to 6V
V _o , Voltage Range Applied to Any Output in the High or Low State (2) (3)	
A Port	0.3V to V _{CCA} + 0.3V
B Port	0.3V to V _{CCB} + 0.3V
I _{IK} , Input Clamp Current (VI < 0)	50mA
I _{OK} , Output Clamp Current (VO < 0)	50mA
Io, Continuous Output Current	±50mA
Continuous Current through VCCA, VCCB, or GND	±100mA
Operating Temperature Range	40℃ to +85℃
Junction Temperature	150 ℃
Storage Temperature Range	-65 ℃ to +150℃
Lead Temperature (Soldering, 10sec)	260 ℃
ESD	
HBM B Port	8KV
HBM Other Pin	4KV

NOTE: 1. Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not

implied. Exposure to absolute Maximum rating conditions for extended periods may affect device reliability. 2. The input and output negative-voltage ratings may be exceeded if the input and output current ratings are observed.

3. The value of VCCA and VCCB are provided in the recommended operating conditions table.



ELECTRICAL CHARACTERISTICS

(V_{CCA} = 1.65V to 5.5V, V_{CCB} = 2.3V to 5.5V, typical values are at T_A = +25°C, unless otherwise noted)

PARAMETER		co	NDITIONS	MIN	ТҮР	MAX	UNITS
RECOMMENDED OPERATI	NG CONDITIO	NS ^{(1) (2)}					
(3)	V _{CCA}			1.65		5.5	.,
Supply Voltage ⁽³⁾	V _{CCB}			2.3		5.5	V
		V _{CCA} = 1.65V to 1	.95V, $V_{CCB} = 2.3V$ to 5.5V	V _{CCI} - 0.2		V _{CCI}	
High-Level Input Voltage	A Port I/Os	$V_{CCA} = 2.3V$ to 5.5	5V, $V_{CCB} = 2.3V$ to 5.5V	V _{CCI} - 0.4		V _{CCI}	V
(V _{IH})	B Port I/Os			V _{CCI} - 0.4		V _{CCI}	v
	OE Input			V _{CCA} ×0.8		5.5	
	A Port I/Os			0		0.15	
Low-Level Input Voltage (V _{IL})	B Port I/Os			0		0.15	V
(VIL)	OE Input			0		V _{CCA} ×0.25	
		A Port I/Os Push-	Pull Driving			10	
Input Transition Rise or Fal	l Rate (Δt/ΔV)	B Port I/Os Push-	Pull Driving			10	ns/V
		Control Input				10	
ELECTRICAL CHARACTER	RISTICS						
A Port High Level Output V	′oltage (V _{OHA})	I _{OH} = -20μA, V _{IB} ≥V _{CCB} - 0.4V		V _{CCA} ×0.7			
A Port Low Level Output V	oltage (V _{OLA})	$I_{OL} = 1mA, V_{IB} \le 0.15V$				0.4	V
B Port High Level Output V	′oltage (V _{OHB})	I _{OH} = -20μA, V _{IA} ≥V _{CCA} - 0.4V		V _{CCB} ×0.7			v
B Port Low Level Output V	oltage (V _{OLB})	$I_{OL} = 1 \text{mA}, V_{IA} \leq 0$).15V			0.4	
Input Leakage Current (II)	OE				0.5		
Power Off Leakage	A Port	$V_{CCA} = 0V, V_{CCB} =$	= 0V to 5.5V		0.1		
Current (I _{OFF})	B Port	$V_{CCA} = 0V$ to 5.5V	$V, V_{CCB} = 0V$		0.1		μA
3-State Output Leakage (I _{OZ})	A or B Port	OE = 0V			0.1		
		$V_{I} = V_{O} = OPEN,$	$V_{CCA} = 1.65V$ to V_{CCB} , V_{CCB} = 2.3V to 5.5V		0.1		
Quiescent Supply Curr	ent (I _{CCA})	I _O = 0	V_{CCA} =5.5V, V_{CCB} = 0V		0.1		μA
			$V_{CCA} = 0V, V_{CCB} = 5.5V$		0.1		
Quiescent Supply Current (I _{CCB})		$V_1 = V_0 = OPEN,$	$V_{CCA} = 1.65V$ to V_{CCB} , V_{CCB} = 2.3V to 5.5V		7		
		I _O = 0	V_{CCA} =5.5V, V_{CCB} = 0V		0.1		μA
			$V_{CCA} = 0V, V_{CCB} = 5.5V$		0.1		
Quiescent Supply Current	(I _{CCA} + I _{CCB})	$V_1 = V_0 = OPEN,$ $I_0 = 0$	$V_{CCA} = 1.65V$ to V_{CCB} , V_{CCB} = 2.3V to 5.5V		7.1		μA



ELECTRICAL CHARACTERISTICS

(V_{CCA} = 1.65V to 5.5V, V_{CCB} = 2.3V to 5.5V, typical values are at T_A = +25°C, unless otherwise noted)

PARAMETER	CON	DITIONS	MIN	ТҮР	МАХ	UNITS
	$V_{I}=V_{CCI},\ I_{O}=0,$	$V_{CCA} = 1.65V$ to V_{CCB} , $V_{CCB} = 2.3V$ to 5.5V		0.1		
Quiescent Supply Current (I _{CCZA})	OE = GND	V_{CCA} =5.5V, V_{CCB} = 0V		0.1		μA
		$V_{CCA}=0V,V_{CCB}=5.5V$		0.1		
	$V_{I}=V_{CCI},I_{O}=0,$	$V_{CCA} = 1.65V$ to V_{CCB} , $V_{CCB} = 2.3V$ to 5.5V		0.1		
Quiescent Supply Current (I _{CCZB})	OE = GND	V_{CCA} =5.5V, V_{CCB} = 0V		0.1		μA
		$V_{CCA} = 0V, V_{CCB} = 5.5V$		0.1		
OE Input Capacitance (CI)	$V_{CCA} = 3.3V, V_{CCB} = 3.3$	3V		3.6		pF
Input/Output Capacitance A Port (C _{IO})	$V_{CCA} = 3.3V, V_{CCB} = 3.3$		5.4		pF	
Input/Output Capacitance B Port (C _{IO})	$v_{CCA} = 0.0 v$, $v_{CCB} = 0.0 v$, v		6.3		ΡГ

NOTES:

- 1. V_{CCI} is the V_{CC} associated with the input port.
- 2. V_{CCO} is the V_{CC} associated with the output port.
- 3. V_{CCA} must be less than or equal to $V_{\text{CCB}},$ and V_{CCA} must not exceed 5.5V.



TIMING REQUIREMENTS

			$V_{CCB} = 2.5V$	$V_{CCB} = 3.3V$	$V_{CCB} = 5V$	
			ТҮР	ТҮР	ТҮР	UNITS
(T _A = +25℃, V _{CCA} = 1.8	V, unless otherwise not	ed.)				
Data Rate	Push-Pull Driving		21	22	24	Mhpa
Dala Rale	Open-Drain Driving		2	2	2	- Mbps
Pulse Duration (t _w)	Push-Pull Driving	Data Innuta	47	45	41	20
	Open-Drain Driving	Data Inputs	500	500	500	ns
(T _A = +25℃, V _{CCA} = 2.5	V, unless otherwise not	ed.)				
Data Data	Push-Pull Driving		20	22	24	Mbps
Data Rate	Open-Drain Driving		2	2	2	
	Push-Pull Driving	Data Inputs	50	45	41	- ns
Pulse Duration (t _w)	Open-Drain Driving		500	500	500	
(T _A = +25℃, V _{CCA} = 3.3	V, unless otherwise noted	d.)		•		
Data Data	Push-Pull Driving			23	24	D.4L
Data Rate	Open-Drain Driving			2	2	- Mbps
	Push-Pull Driving			43	41	
Pulse Duration (t_W)	Open-Drain Driving	Data Inputs		500	500	ns
(T _A = +25℃, V _{CCA} = 5V,	unless otherwise noted.)					
Data Rate	Push-Pull Driving				24	Mbps
	Open-Drain Driving				2	
	Push-Pull Driving				41	
Pulse Duration (t_W)	Open-Drain Driving	Data Inputs			500	ns

(typical values are at T_A = +25 $^{\circ}$ C, unless otherwise noted)



SWITCHING CHARACTERISTICS

	FROM	то	TEST	N 0.5V		N 5V	
PARAMETER	(INPUT)	(OUTPUT)	CONDITIONS	V _{CCB} = 2.5V	$V_{CCB} = 3.3V$	$V_{CCB} = 5V$	UNITS
4			Push-Pull Driving	2.4	3.0	4.3	
t _{PHL}	•	P	Open-Drain Driving	26.0	26.3	26.7	
	A	В	Push-Pull Driving	4.0	3.6	3.5	ns
t _{PLH}			Open-Drain Driving	175	145	110	
			Push-Pull Driving	2.0	1.9	2.1	
t _{PHL}		٨	Open-Drain Driving	26.0	26.1	26.2	
	В	A	Push-Pull Driving	1.7	1.5	1.4	ns
t _{PLH}			Open-Drain Driving	133	69	51	51
t _{EN} (t _{PZH} & t _{PZL})	OE	A or B		17	15	14	
t _{DIS} (t _{PHZ} & t _{PLZ})	OE	A or B		614	616	626	ns
•	A Dart [Rise Time	Push-Pull Driving	6.6	5.8	5.4	
t _{rA}	A POIL F	kise Time	Open-Drain Driving	89	31	10	ns
	P. Dort [Rise Time	Push-Pull Driving	5.6	4.6	3.9	
t _{rB}	D PUIL I	lise fille	Open-Drain Driving	128	98	58	ns
	A Dart [Push-Pull Driving	2.9	2.7	2.6	
t _{fA}	APOIL	all Time	Open-Drain Driving	1.9	1.7	1.6	ns
+	P. Dort [all Time	Push-Pull Driving	4.6	5.9	8.0	
t _{fB} B			Open-Drain Driving	2.2	2.3	2.9	ns
t _{sk(0)}	Channel-to-	Channel Skew		0.5	0.5	0.5	ns
Data Pata			Push-Pull Driving	21	22	24	Mbra
Data Rate			Open-Drain Driving	2	2	2	- Mbps

(V_{CCA} = 1.8V, typical values are at T_A = +25 $^\circ \! \mathrm{C}$, unless otherwise noted)



SWITCHING CHARACTERISTICS

	FROM	то	TEST		N 0.0V		
PARAMETER	(INPUT)	(OUTPUT)	CONDITIONS	$V_{CCB} = 2.5V$	$V_{CCB} = 3.3V$	$V_{CCB} = 5V$	UNITS
			Push-Pull Driving	2.7	3.3	4.8	
t _{PHL}		В	Open-Drain Driving	26.2	26.4	26.7	
	A	В	Push-Pull Driving	2.6	2.4	2.3	ns
t _{PLH}			Open-Drain Driving	169	144	110	
4			Push-Pull Driving	2.4	2.3	2.4	
t _{PHL}	P	٥	Open-Drain Driving	26.3	26.4	26.5	
	В	A	Push-Pull Driving	2.0	1.9	1.8	ns
t _{PLH}			Open-Drain Driving	165	118	55	
$t_{\text{EN}} \left(t_{\text{PZH}} \& t_{\text{PZL}} \right)$	OE	A or B		14	13	12	
t _{DIS} (t _{PHZ} & t _{PLZ})	OE	A or B		630	635	640	ns
	A David I		Push-Pull Driving	3.2	2.8	2.6	
t _{rA}	A Port F	Rise Time	Open-Drain Driving	120	70	10	ns
	D Dort I	Rise Time	Push-Pull Driving	4.5	3.4	2.6	
t _{rB}	BPOILF	kise Time	Open-Drain Driving	122	96	62	ns
			Push-Pull Driving	4.9	5.0	4.8	
t _{fA}	APOR	Fall Time	Open-Drain Driving	2.0	1.9	1.7	ns
4	P. Dort	Fall Time	Push-Pull Driving	4.8	6.1	8.3	20
t _{fB}	вроп	Fall Time	Open-Drain Driving	1.9	2.1	2.7	ns
t _{sk(0)}	Channel-to-	Channel Skew		0.5	0.5	0.5	ns
Data Pata			Push-Pull Driving	20	22	24	Mbps
Data Rate			Open-Drain Driving	2	2	2	Mbps

(V_{CCA} = 2.5V, typical values are at T_A = +25 $^\circ \! \mathrm{C}$, unless otherwise noted)



SWITCHING CHARACTERISTICS

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS	$V_{CCB} = 3.3V$	$V_{CCB} = 5V$	UNITS	
			Push-Pull Driving	3.5	4.9		
t _{PHL}			Open-Drain Driving	26.3	26.7		
4	A	В	Push-Pull Driving	2.2	2.0	ns	
t _{PLH}			Open-Drain Driving	133	104		
			Push-Pull Driving	3.0	3.2		
t _{PHL}	в		Open-Drain Driving	26.6	26.8		
	В	A	Push-Pull Driving	1.8	1.7	ns	
t _{PLH}			Open-Drain Driving	132	83		
t _{EN} (t _{PZH} & t _{PZL})	OE	A or B		12	11		
t _{DIS} (t _{PHZ} & t _{PLZ})	OE	A or B		630	635	ns	
+	A Dout	Rise Time	Push-Pull Driving	2.2	2.0		
t _{rA}	A POIL	kise nime	Open-Drain Driving	87	36	ns	
	D. Dort I	Rise Time	Push-Pull Driving	2.9	2.3		
t _{rB}	BPOIL	kise nime	Open-Drain Driving	87	56	– ns	
	A Dort		Push-Pull Driving	6.2	5.8		
t _{fA}	A Port	Fall Time	Open-Drain Driving	2.3	2.0	ns	
+	P Dort	Foll Time	Push-Pull Driving	6.5	8.2	20	
t _{fB}	B POR	Fall Time	Open-Drain Driving	2.0	2.5	– ns	
t _{sk(0)}	Channel-to-	Channel Skew		0.5	0.5	ns	
Data Data			Push-Pull Driving	23	24	N / h	
Data Rate			Open-Drain Driving	2	2	Mbps	

(V_{CCA} = 3.3V, typical values are at T_A = +25 $^\circ \! \mathrm{C}$, unless otherwise noted)



SWITCHING CHARACTERISTICS

	FROM	то	TEST	N/ 51/		
PARAMETER	(INPUT)	(OUTPUT)	CONDITIONS	$V_{CCB} = 5V$	UNITS	
t			Push-Pull Driving	5.4		
t _{PHL}	A	В	Open-Drain Driving	26.7	20	
	A	D	Push-Pull Driving	1.9	ns	
t _{PLH}			Open-Drain Driving	120		
t			Push-Pull Driving	5.6		
t _{PHL}	в	А	Open-Drain Driving	27.3	ns	
+	В	A	Push-Pull Driving	1.7	ns	
t _{PLH}			Open-Drain Driving	126		
t _{EN} (t _{PZH} & t _{PZL})	OE	A or B		10		
t _{DIS} (t _{PHZ} & t _{PLZ})	OE	A or B		636	– ns	
t _{rA}	A Port Rise Time		Push-Pull Driving	1.8	20	
ιrΑ			Open-Drain Driving	79	ns	
t _	P Dort P	ise Time	Push-Pull Driving	2.2	20	
t _{rB}	BFUILK		Open-Drain Driving	73	ns	
	A Dort F	all Time	Push-Pull Driving	8.7	20	
t _{fA}			Open-Drain Driving	2.7	– ns	
	P Dort F	all Time	Push-Pull Driving	8.6	20	
t _{fB}	D FUIL F		Open-Drain Driving	2.4	ns	
t _{sk(0)}	Channel-to-C	hannel Skew		0.5	ns	
Data Rate			Push-Pull Driving	24	– Mbp	
			Open-Drain Driving	2	quivi	

(V_{CCA} = 5.0V, typical values are at T_A = +25 °C, unless otherwise noted)



APPLICATION INFORMATION

The BCT0102 can be used to bridge the digital-switching compatibility gap between two voltage nodes to successfully interface logic threshold levels found in electronic systems. It should be used in a point-to-point topology for interfacing devices or systems operating at different interface voltages with one another. Its primary target application use is for interfacing with open-drain drivers on the data I/Os such as I2C or 1-wire, where the data is bidirectional and no control signal is available. The BCT0102 can also be used in applications where a push-pull driver is connected to the data I/Os

Block Diagram

The BCT0102 architecture (see Figure 1) is an auto-direction-sensing based translator that does not require a direction-control signal to control the direction of data flow from A to B or from B to A.

These two bidirectional channels independently determine the direction of data flow without a direction-control signal. Each I/O pin can be automatically reconfigured as either an input or an output, which is how this auto-direction feature is realized.

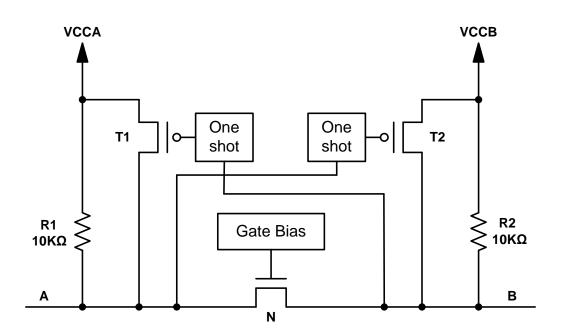


Figure 1 Block Diagram of BCT0102 I/O Cell



The BCT0102 employs two key circuits to enable this voltage translation: An N-channel pass-gate transistor topology that ties the A port to the B port. Output one-shot (O.S.) edge-rate accelerator circuitry to detect and accelerate rising edges on the A or B ports.

Input Driver Requirements

The fall time (t_{fA} , t_{fB}) of a signal depends on the output impedance of the external device driving the data I/Os of the BCT0102. Similarly, the t_{PHL} and data rates also depend on the output impedance of the external driver. The values for t_{fA} , t_{fB} , t_{PHL} , and data rates in the datasheet assume that the output impedance of the external driver is less than 50 Ω .

Power Up

During operation, ensure that $V_{CCA} \le V_{CCB}$ at all times. The sequencing of each power supply will not damage the device during the power up operation, so either power supply can be ramped up first.

Output Load Considerations

We recommend careful PCB layout practices with short PCB trace lengths to avoid excessive capacitive loading and to ensure that proper O.S. triggering takes place. PCB signal trace-lengths should be kept short enough such that the round trip delay of any reflection is less than the one-shot duration. This improves signal integrity by ensuring that any reflection sees a low impedance at the driver. The O.S. circuits have been designed to stay on for approximately 30ns. The maximum capacitance of the lumped load that can be driven also depends directly on the one-shot duration. With very heavy capacitive loads, the one-shot can time-out before the signal is driven fully to the positive rail. The O.S. duration has been set to best optimize trade-offs between dynamic I_{CC}, load driving capability, and maximum bit-rate considerations. Both PCB trace length and connectors add to the capacitance that the BCT0102 output sees, so it is recommended that this lumped-load capacitance be considered to avoid O.S. retriggering, bus contention, output signal oscillations, or other adverse system-level affects.



Enable and Disable

The BCT0102 has an OE input that is used to disable the device by setting OE low, which places all I/Os in the Hi-Z state. OE has an internal pull-down current source, as long as V_{CCA} is powered. The disable time (t_{DIS}) indicates the delay between the time when OE goes low and when the outputs are disabled (Hi-Z). The enable time (t_{EN}) indicates the amount of time the user must allow for the one-shot circuitry to become operational after OE is taken high.

Pull-up or Pull-down Resistors on I/O Lines

Each A port I/O has an internal $10k\Omega$ pull-up resistor to V_{CCA} , and each B port I/O has an internal $10k\Omega$ pull-up resistor to V_{CCB} . If a smaller value of pull-up resistor is required, an external resistor must be added from the I/O to V_{CCA} or V_{CCB} (in parallel with the internal $10k\Omega$ resistors). Adding lower value pull-up resistors will effect V_{OL} levels, however. The internal pull-ups of the BCT0102 are disabled when the OE pin is low.

Typical Application Circuit

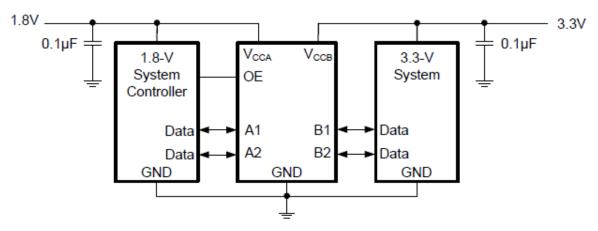


Figure 2 Typical Application Circuit



Test Circuits

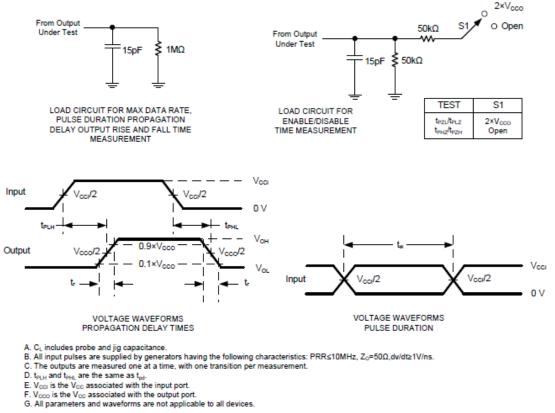
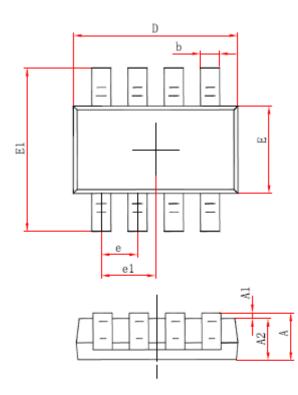


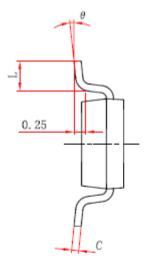
Figure 3 Load Circuits and Voltage Waveforms



PACKAGE OUTLINE DIMENSIONS

TSOT23-8 Outline Drawing

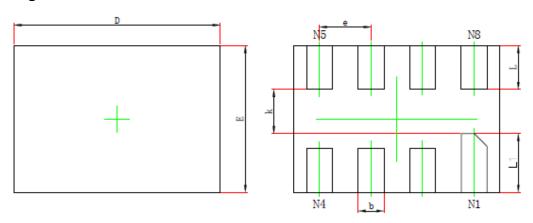




Symbol	Dimensions In	n Millimeters	Dimension	s In Inches
Symbol	Min	Max	Min	Max
A	0.700	0.900	0.028	0.035
A1	0.000	0.100	0.000	0.004
A2	0.700	0.800	0.028	0.031
b	0.300	0.400	0.012	0.016
с	0.080	0.200	0.003	0.008
D	2.820	3.020	0.111	0.119
E	1.600	1.700	0.063	0.067
E1	2.650	2.950	0.104	0.116
е	0.65 (E	BSC)	0.026	(BSC)
e1	0.975 (0.975 (BSC)		(BSC)
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

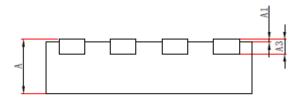


XTDFN1.4X1.0-8L Outline Drawing



TOP VIEW

BOTTOM VIEW



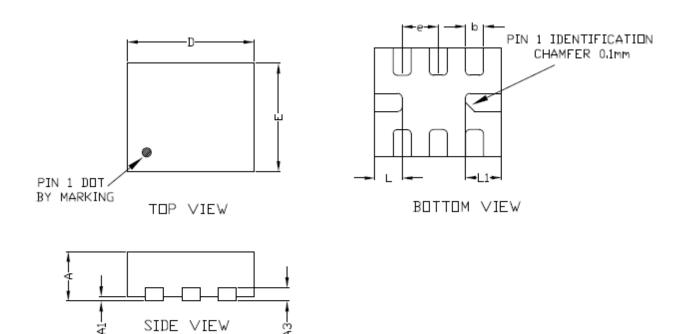
SIDE VIEW

Symbol	Dimensions Ir	n Millimeters	Dimension	s In Inches
Symbol	Min.	Max.	Min.	Max.
A	0.340	0.400	0.013	0.016
A1	0.000	0.050	0.000	0.002
A3	0.100	REF.	0.004	REF.
D	1.350	1.450	0.053	0.057
E	0.950	1.050	0.037	0.041
D1				
E1				
k	0.200	MIN.	0.008	BMIN.
b	0.150	0.200	0.006	0.008
е	0.350TYP.		0.014	ITYP.
L	0.250	0.350	0.010	0.014
L1	0.350	0.450	0.014	0.018



QFN1.4X1.2-8L **Outline Drawing**

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Αg

COMMON DIMENSIONS(MM)			
PKG,	UT:UL TRA THIN		
REF.	MIN.	NDM.	MAX
A	0,50	0,55	0.60
A1	0.00	_	0.05
A3	0.15 REF.		
D	1.35	1,40	1,45
E	1.15	1,20	1.25
0	0.15	0.20	0,25
L	0.20	0.30	0.40
L1	0.30	0.40	0.50
e	0.40 BSC		