

SRA2219UF

PNP Silicon Transistor

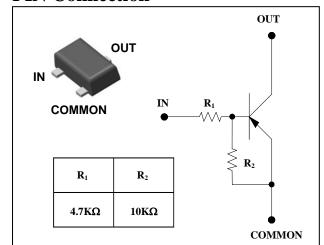
Descriptions

- Switching application
- Interface circuit and driver circuit application

Features

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

PIN Connection



Ordering Information

Type NO.	Marking	Package Code	
SRA2219UF	<u>CR</u> □ ① ②	SOT-323F	

①Device Code ② Year&Week Code

Absolute Maximum Ratings

(Ta=25°C)

Characteristic	Symbol	Rating	Unit
Output voltage	Vo	-50	V
Input voltage	V _I	-20, 7	V
Output current	I _O	-100	mA
Power dissipation	P_{D}	200	mW
Junction temperature	TJ	150	°C
Storage temperature range	T_{stg}	-55 ~ 150	°C

Electrical Characteristics

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Output cut-off current	I _{O(OFF)}	$V_0 = -50V, V_1 = 0$	-	-	-500	nA
DC current gain	Gı	$V_0 = -5V$, $I_0 = -10$ mA	30	-	-	-
Output voltage	$V_{O(ON)}$	I _O =-10mA, I _I =-0.5mA	ı	-0.1	-0.3	V
Input voltage (ON)	V _{I(ON)}	$V_0 = -0.2V$, $I_0 = -5mA$	-	-1.2	-1.6	V
Input voltage (OFF)	$V_{I(OFF)}$	$V_0 = -5V$, $I_0 = -0.1$ mA	-0.5	-0.82	-	V
Transition frequency	$f_{T}^{}^{X}}$	$V_0 = -10V$, $I_0 = -5$ mA, $f = 1$ MHz	ı	200	-	MHz
Input current	I_1	$V_1 = -5V, I_0 = 0$	-	-	-1.8	mA
Input resistor (Input to base)	R ₁	-	3.3	4.7	6.1	KΩ
Input resistor (Base to common)	R ₂	-	7	10	13	KΩ

^{* :} Characteristic of transistor only

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Electrical Characteristic Curves

Fig. 1 I_O - $V_{I(ON)}$

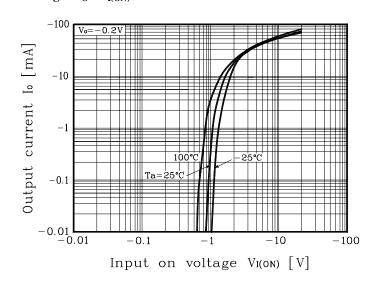


Fig. 2 I_O - $V_{I(OFF)}$

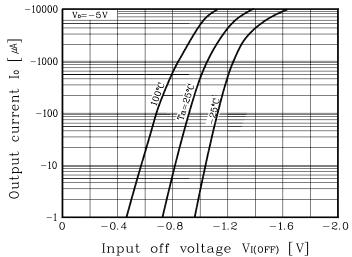
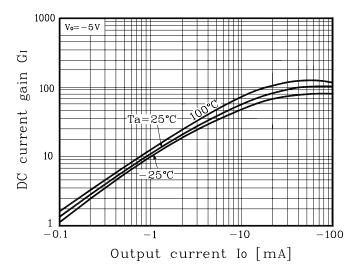
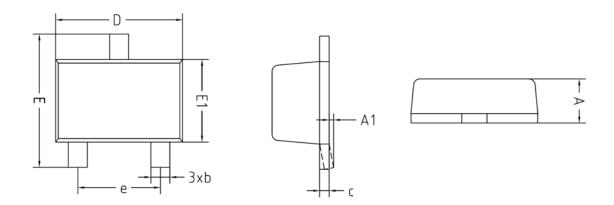


Fig. 3 G_I - I_O



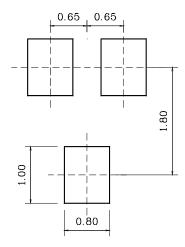
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Outline Dimension



SYMBOL	1	NOTE		
STRIBUL	MINIMUM	NOMINAL	MAXIMUM	NUTE
Α	0.60	-	0.80	
A1	0.00	-	0.10	
Ь	0.30	-	0.40	
С	0.08	-	0.16	
D	1.90	2.00	2.10	
Е	1.95	2.10	2.25	
E1	1.20	1.30	1.40	
е	1.30BSC			

*Recommend PCB solder land [Unit: mm]



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