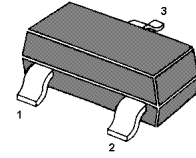
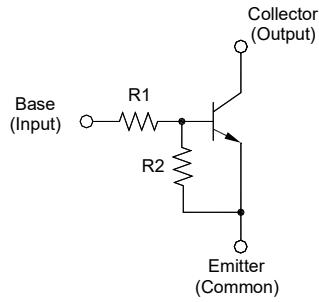


# MMDT5N431

## NPN Silicon Epitaxial Planar Digital Transistor

### Features

- With built-in bias resistors,  
R1 = R2 = 4.7 KΩ
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process



1. Base 2. Emitter 3. Collector  
SOT-23 Plastic Package

### Applications

- For switching and interface circuit and drive circuit

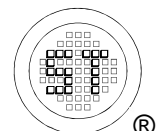
### Absolute Maximum Ratings ( $T_a = 25\text{ }^\circ\text{C}$ )

Parameter	Symbol	Value	Unit
Collector Base Voltage	$V_{CBO}$	50	V
Collector Emitter Voltage	$V_{CEO}$	50	V
Emitter Base Voltage	$V_{EBO}$	- 10 to + 30	V
Collector Current	$I_C$	500	mA
Power Dissipation	$P_{tot}$	200	mW
Junction Temperature	$T_j$	150	$^\circ\text{C}$
Storage Temperature Range	$T_{stg}$	- 55 to + 150	$^\circ\text{C}$

### Thermal Characteristics

Parameter	Symbol	Max.	Unit
Thermal Resistance from Junction to Ambient <sup>1)</sup>	$R_{\theta JA}$	625	$^\circ\text{C/W}$

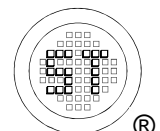
<sup>1)</sup> Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.



# MMDT5N431

## Characteristics at $T_a = 25\text{ }^\circ\text{C}$

Parameter	Symbol	Min.	Typ.	Max.	Unit
DC Current Gain at $V_{CE} = 5\text{ V}$ , $I_C = 50\text{ mA}$	$h_{FE}$	47	-	-	-
Collector Base Cutoff Current at $V_{CB} = 50\text{ V}$	$I_{CBO}$	-	-	0.5	$\mu\text{A}$
Emitter Base Cutoff Current at $V_{EB} = 5\text{ V}$	$I_{EBO}$	-	-	1.8	mA
Collector Emitter Saturation Voltage at $I_C = 50\text{ mA}$ , $I_B = 2.5\text{ mA}$	$V_{CE(sat)}$	-	-	0.3	V
Input on Voltage at $V_{CE} = 0.3\text{ V}$ , $I_C = 20\text{ mA}$	$V_{I(on)}$	3	-	-	V
Input off Voltage at $V_{CE} = 5\text{ V}$ , $I_C = 100\text{ }\mu\text{A}$	$V_{I(off)}$	-	-	0.5	V
Transition frequency at $V_{CE} = 10\text{ V}$ , $-I_E = 5\text{ mA}$ , $f = 100\text{ MHz}$	$f_T$	-	200	-	MHz
Input Resistance	$R_1$	3.29	4.7	6.11	$\text{K}\Omega$
Resistance Ratio	$R_2 / R_1$	0.8	1	1.2	-



# MMDT5N431

## Electrical Characteristics Curves

Fig 1.  $V_{I(ON)}$  Vs. Output Current

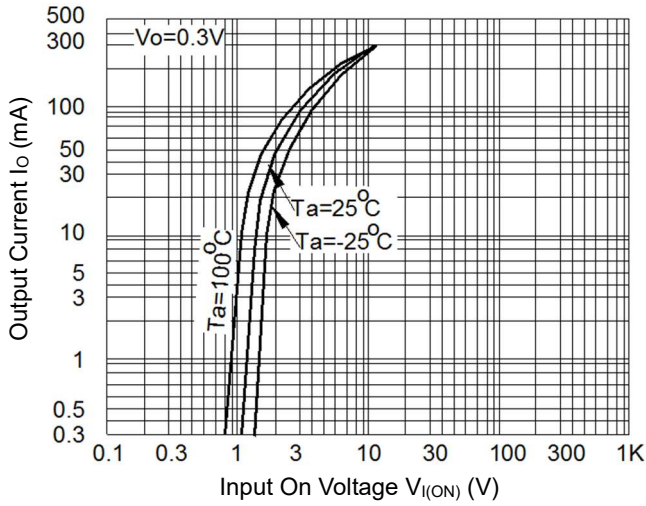


Fig 2.  $V_{I(OFF)}$  Vs. Output Current

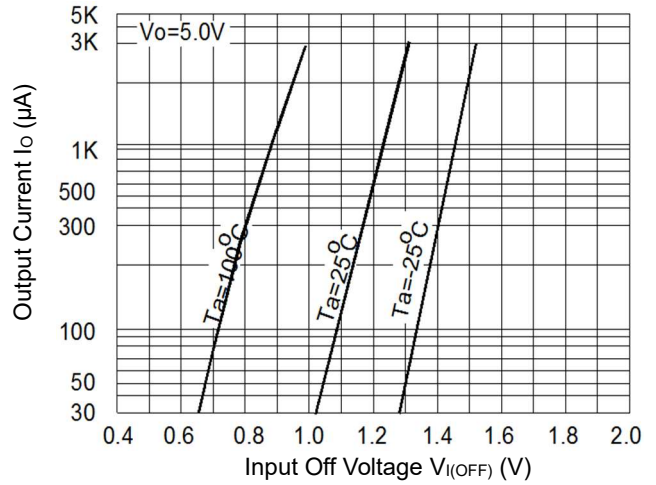


Fig 3. DC Current Gain Vs. Output Current

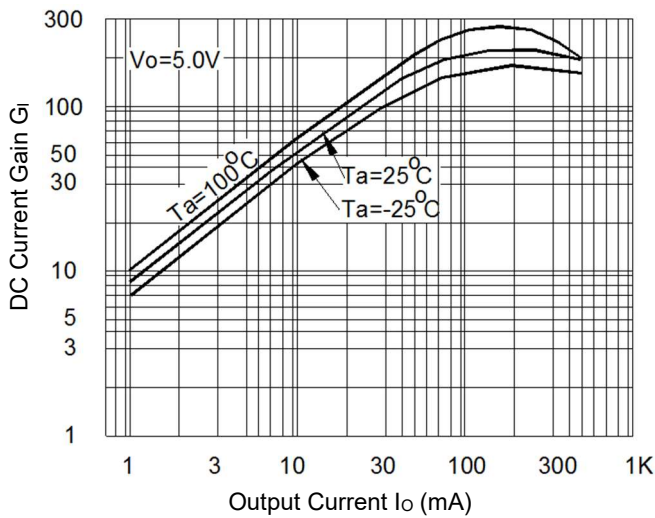
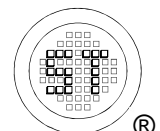
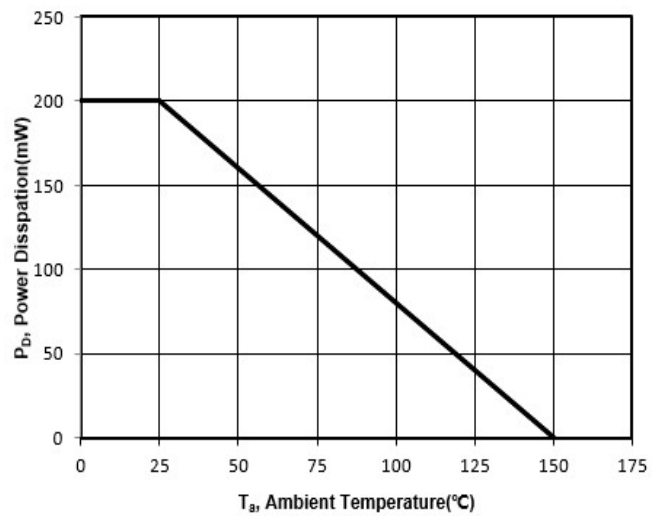


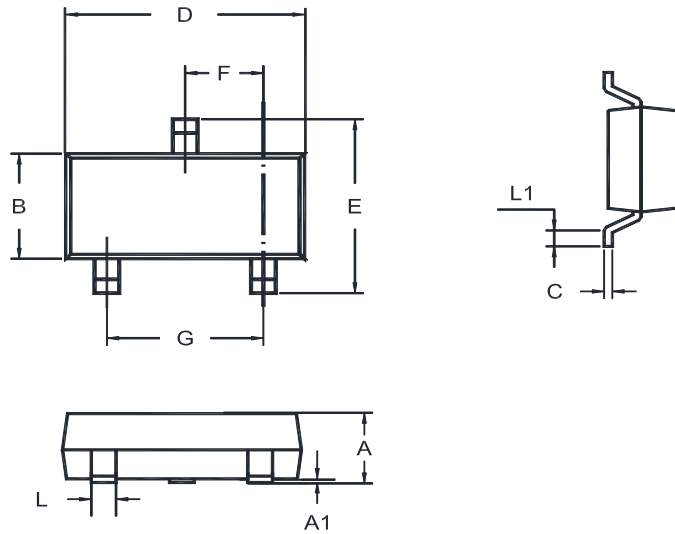
Fig 4. Power Derating Curve



# MMDT5N431

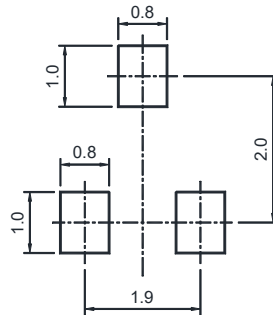
## Package Outline (Dimensions in mm)

SOT-23



Unit	A	A1	B	C	D	E	F	G	L	L1
mm	1.20	0.100	1.40	0.19	3.04	2.6	1.02	2.04	0.51	0.2
	0.89	0.013	1.20	0.08	2.80	2.2	0.89	1.78	0.37	MIN

## Recommended Soldering Footprint



## Packing information

Package	Tape Width (mm)	Pitch		Reel Size		Per Reel Packing Quantity
		mm	inch	mm	inch	
SOT-23	8	4 ± 0.1	0.157 ± 0.004	178	7	3,000

## Marking information

"HP" = Part No.

"YM" = Date Code Marking

"Y" = Year

"M" = Month

Font type: Arial

