TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74AC157P, TC74AC157F, TC74AC157FT

Quad 2-Channel Multiplexer

The TC74AC157 is an advanced high speed CMOS QUAD 2-CHANNEL MULTIPLEXER fabricated with silicon gate and double-layer metal wiring C^2MOS technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

This device consist of four 2-input digital multiplexer with common select and strobe inputs.

When the **STROBE** input is held "H" level, selection of data is inhibited and all the outputs become "L" level.

The SELECT decoding determines whether the A or B inputs get routed to their corresponding Y outputs.

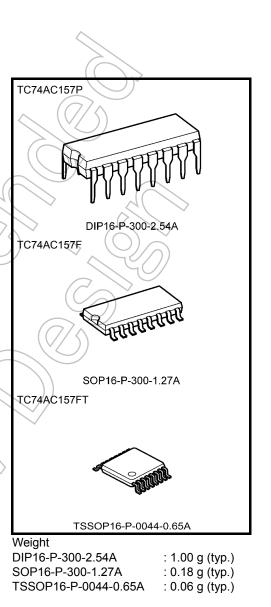
All inputs are equipped with protection circuits against static discharge or transient excess voltage.

Features

- High speed: t_{pd} = 4.5 ns (typ.) at V_{CC} = 5 V
- Low power dissipation: $I_{CC} = 8 \mu A (max)$ at $Ta = 25^{\circ}C$
- High noise immunity: $V_{\text{NIH}} = V_{\text{NIL}} = 28\% V_{\text{CC}}$ (min)
- Symmetrical output impedance: $|I_{OH}| = |I_{OL}| = 24 \text{ mA} (\text{min})$

Capability of driving 50 Ω transmission lines.

- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range: VCC (opr) = 2 to 5.5 V
- Pin and function compatible with 74E157

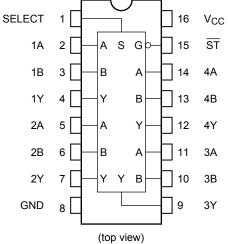


Start of commercial production 1987-05

TC74AC157P/F/FT

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Pin Assignment

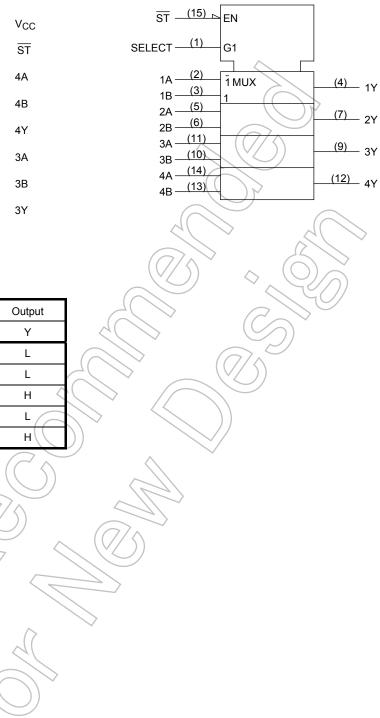


Truth Table

	Inputs	Output		
ST	SELECT	А	В	Y
Н	Х	Х	Х	L
L	L	L	Х	L
L	L	Н	Х	нζ
L	Н	Х	L	L
L	Н	Х	Н	н

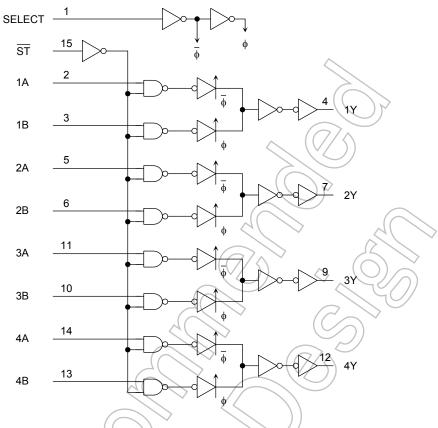
X: Don't care

IEC Logic Symbol



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System Diagram



Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	Vcc	-0.5 to 7.0	V
DC input voltage	V _{IN}	-0.5 to V _{CC} + 0.5	V
DC output voltage	νουτ ζ	-0.5 to V _{CC} + 0.5	V
Input diode current	 ۱ _{IK}	±20	mA
Output diode current	Іок	±50	mA
DC output current	IOUT	±50	mA
DC V _{CC} /ground current	ICC.	±100	mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP/TSSOP)	mW
Storage temperature	T _{stg}	-65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to 65°C. From Ta = 65 to 85°C, a derating factor of -10 mW/°C should be applied up to 300 mW.

Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	2.0 to 5.5	V
Input voltage	V _{IN}	0 to V _{CC}	V
Output voltage	V _{OUT}	0 to V _{CC}	v
Operating temperature	T _{opr}	-40 to 85	0°
Input rise and fall time	dt/dV	0 to 100 (V _{CC} = 3.3 ± 0.3 V)	ns/V
	abav	0 to 20 (V _{CC} = 5 ± 0.5 V)	

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.

Electrical Characteristics

DC Characteristics

				$\sim \sim$		1	$\langle \rangle$		
Characteristics	Symbol	Test Condition	Vcc	Min	Га = 25°С Тур.	; Max	-40 to Min		Unit
			(V)	IVIIII	Typ.	IVIEX		IVIAX	
High-level input			2.0	1.50	-((1.50	—	
voltage	VIH		3.0	2.10		$ \ge $	2.10	—	V
			5.5	3.85	(7/ <) -	3.85	_	
Laura laura l'increat			2.0			0.50	—	0.50	
Low-level input voltage	VIL		3.0	_)-	0.90	—	0.90	V
· oago			5.5	K))_	1.65	—	1.65	
			2.0	1.9	2.0		1.9	_	
	V _{OH}	I _{OH} = -50 μA	3.0	2.9	3.0	—	2.9	_	
High-level output		VIN = VIH or	4.5	4.4	4.5	—	4.4	—	V
voltage		V_{IL} $I_{OH} = -4 \text{ mA}$	3.0	2.58	_	_	2.48	_	v
		I _{OH} = -24 mA	4.5	3.94	—	—	3.80	—	
		I _{OH} = -75 mA (Note)	5.5		—	_	3.85	_	
			2.0	_	0.0	0.1	—	0.1	
	V _{OL} =	I _{OL} = 50 μA	3.0	_	0.0	0.1	—	0.1	
Low-level output voltage		V _{IN} = V _{IH} or	4.5	_	0.0	0.1	—	0.1	V
		V_{IL} $I_{OL} = 12 \text{ mA}$	3.0	_	—	0.36	—	0.44	v
		I _{OL} = 24 mA	4.5	—	—	0.36	—	0.44	
))	$I_{OL} = 75 \text{ mA}$ (Note)	5.5	_	—	—	—	1.65	
Input leakage current	IIN	$V_{IN} = V_{CC}$ or GND	5.5	_	_	±0.1	_	±1.0	μA
Quiescent supply current		$V_{IN} = V_{CC}$ or GND	5.5	—	_	8.0	_	80.0	μA

Note: This spec indicates the capability of driving 50 Ω transmission lines.

One output should be tested at a time for a 10 ms maximum duration.

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AC Characteristics (C_L = 50 pF, R_L = 500 Ω , input: t_r = t_f = 3 ns)

Characteristics	Symbol Test Condition		Ta = 25°C			Ta = −40 to 85°C		Unit	
	,		V _{CC} (V)	Min	Тур.	Max	Min	Max	
Propagation delay time (A, B-Y)	t _{pLH} t _{pHL}	_	3.3 ± 0.3 5.0 ± 0.5	_	7.2 5.5	12.2 7.9	1.0 1.0	14.0 9.1	ns
Propagation delay time (SELECT-Y)	t _{pLH} t _{pHL}	_	3.3 ± 0.3 5.0 ± 0.5	_	8.5 6.3	14.5 9.1	1.0 1.0	16.7 10.5	ns
Propagation delay time (ST -Y)	t _{pLH} t _{pHL}	_	3.3 ± 0.3 5.0 ± 0.5	-	8.6 6.4	14.6 9.2	1.0 1.0	16.8 10.6	ns
Input capacitance	CIN	_		_(5)	10	_	10	pF
Power dissipation capacitance	C _{PD}		(Note)		93	—	A	1	pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

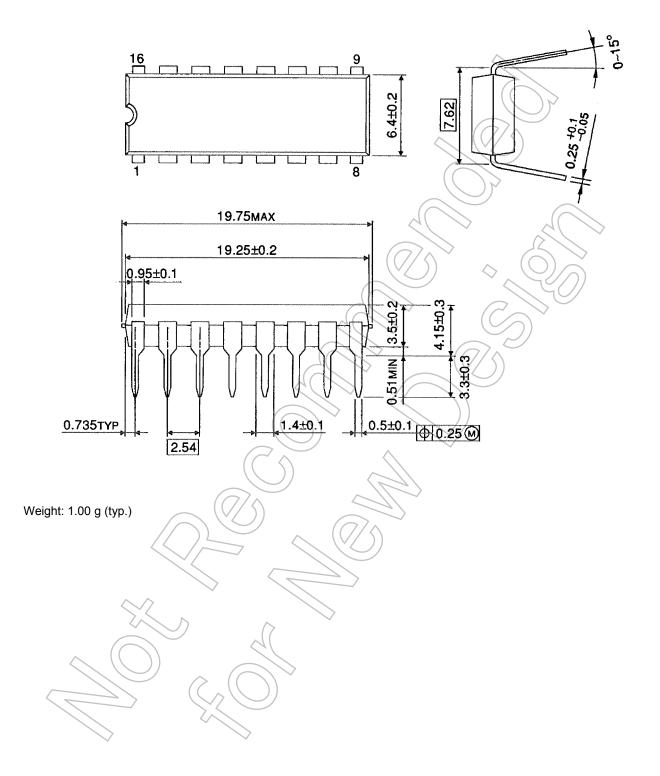
Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/4$ (per bit)

Package Dimensions

DIP16-P-300-2.54A

Unit : mm

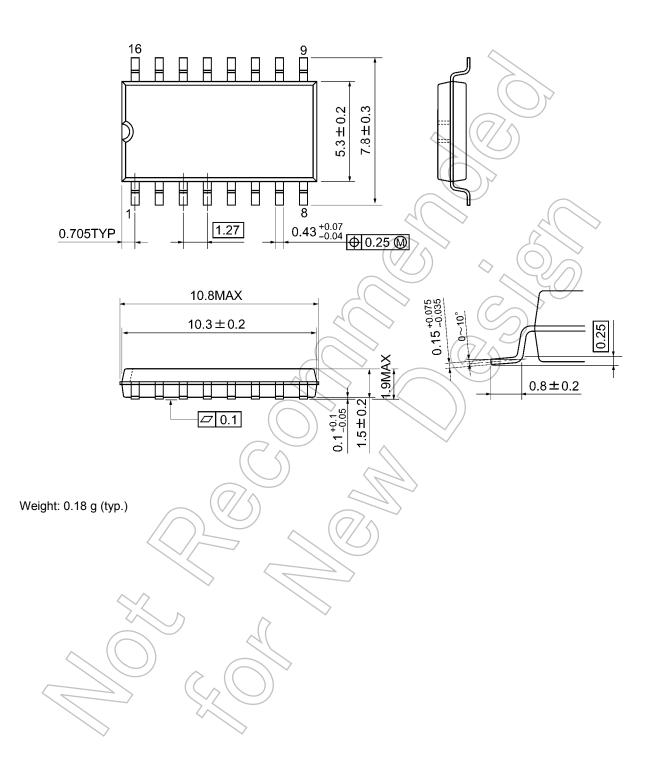




Package Dimensions

SOP16-P-300-1.27A

Unit: mm

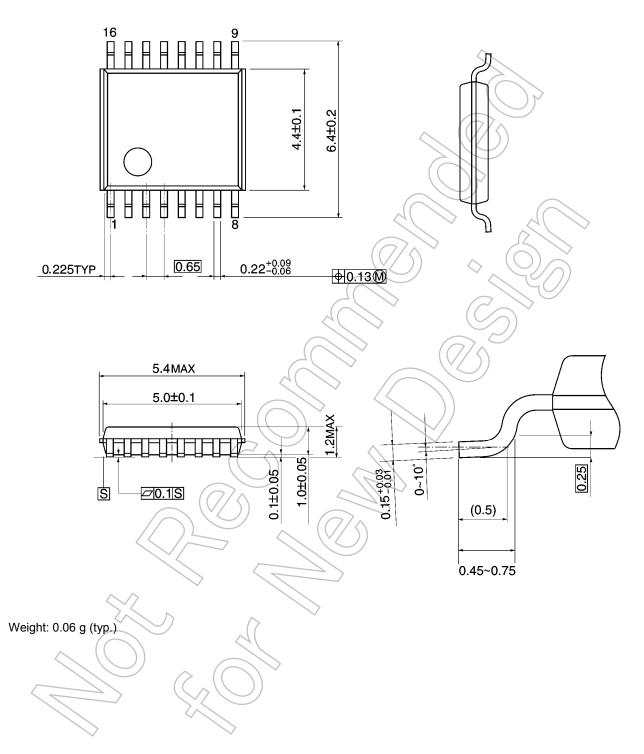


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Package Dimensions

TSSOP16-P-0044-0.65A

Unit: mm



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